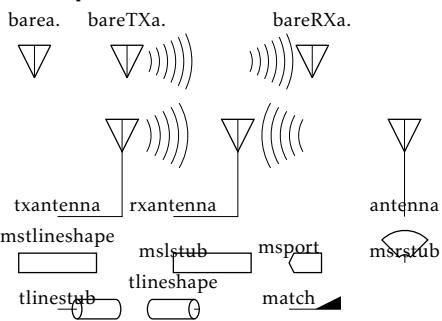
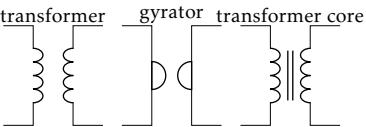


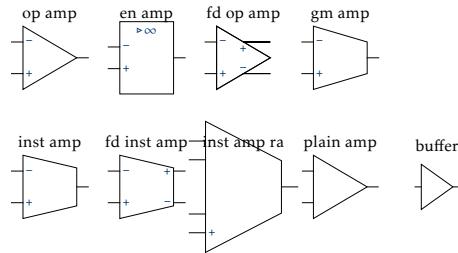
RF Components



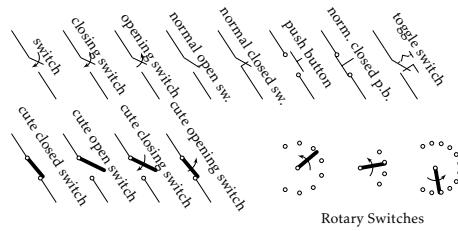
Transformers



Amplifiers

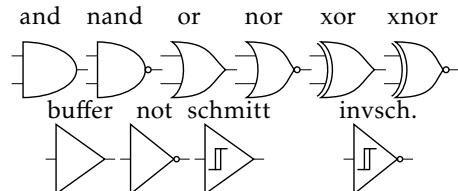


Switches



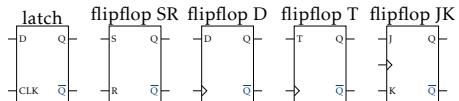
Logic Gates

All are nodes and end in "port" except schmitts. Buffers and schmitts can be used in the path style:



Flip-Flops

Customize further with `flipflop def` which takes several options for pin names (`t<n>`), and for the clock signal (`c<n>`). Can create not signal with `\ctikztextnot{}` macro.

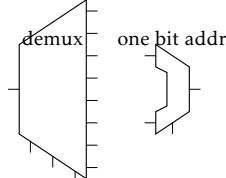


Multiplexers

Customize the number of pins left (NL), bottom (NB) and right(NR); and relative size left (Lh) and right (Rh):

`demux/.style={muxdemux muxdemux}`
`def={Lh=4, Rh=8, NL=1, NB=3, NR=8}}`

Can create insets with `w`, inset `w`, inset `Lh`:
`one bit adder/.style={muxdemux muxdemux}`
`def={... w=1.5, inset w=.5, inset Lh=2, inset Rh=1.5{}}`

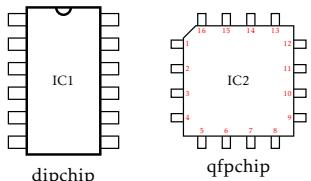


Chips

Customize chips with options `E`:

`width`
`pin spacing`
`extn. pins width`
`extn. pad fraction`
`num pins`
`hide numbers`
`thickness`

`node[dipchip,num pins=12,hide numbers,`
`external pins width=0.3, external pad fraction=4]`
`node[qfpchip,num pins=16, external pad fraction=6]`



Seven-Segment Displays

Options `E`:

`width`
`thickness`
`sep`
`box sep`
`color on`
`color off`

`\ctikzset{sevenseg/color on=black}`

Examples:

```
\foreach \i in {0,...,15} \path (\i,0)
  node[seven segment val=\i dot on box off]{};
\foreach \i in {0,...,15} \path(\i/2,-1)
  node[seven segment val=\i dot off box off,
    fill=gray!30!white]{};
```

0 1 2 3 4 5 6 7 8 9 A B C D E F

0 1 2 3 4 5 6 7 8 9 A B C D E F