Bachelors of Science SEM I Journal

Roll No.	001	
Name	Alston Alvares	
Subject	Digital Logic and Applications.	

ANANDIBAI DAMODAR KALE SHAIKSHANIK SANSTHA'S DEGREE



ANANDIBAI DAMODAR KALE COLLEGE OF ARTS & COMMERCE

Saibaba Nagar, Borivali (West), Mumbai - 400 092

Tel.: 2807 7126

Email: adkdcollege@yahoo.com / www.adkdcollege.in

ISO 9001: 2008 Certified

CERTIFICATE

This is here to certify that Mr/Ms. <u>Alston</u>
<u>Alvares</u>, Seat Number <u>001</u> of B.Sc. IT, has
satisfactorily completed the required number of
experiments prescribed by the <u>ANANDIBAI</u>
DAMODAR KALE DEGREE COLLEGE
AFFILIATED TO UNIVERSITY OF
MUMBAI during the academic year 2022 - 2023.

Date:

Place: Mumbai

Teacher In-Charge

Head of Department

External Examiner

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	1	Study of basic gates and Universal gates a. To verify the truth tables of OR, AND, NOR, NAND, EX-OR, EX-NOR gates b. To study IC 7400, 7402, 7404, 7408, 7432, 7486, 74266 c. To implement and verify NAND and NOR as Universal gates	01-05		
	2	 2. Study of Boolean expressions a. To verify De Morgan's laws b. Implement the given expression using a minimum number of gates. c. Implement the given expression using a minimum number of ICs. 	06-07		
	3	3. Design of Combinational Circuits using K-maps a. Design and implement combinational circuits for the given problem/problems using minimization techniques of K-maps.	08-10		

4	Design and implement code converters a. Design the circuit and implement Binary to gray code converter b. Design the circuit and implement Gray to Binary code converter c. Design the circuit and implement Binary to BCD code converter d.	11-14	
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5	Implement Adder and Subtractor circuits a . Design the circuit and implement Half Adder and Full Adder b . Design the circuit and implement BCD Adder, XS-3 Adder, Binary Subtractor	15-19	
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7	Implement Encoders and Decoders a. Design and implement 8: 3 encoder b. Design and implement 3:8 decode	23-26	
8	Multiplexers and Demultiplexers a. Design and Implement 4:1 multiplexer b. Design and Implement 1:4 demultiplexer c. Study IC 74151 8: 1 multiplexer and implement the expression d. Study IC 74138 3: 8 decoder and implement the expression	27-35	