

# 2.5A, 3MHz Switching Charger with Dynamic Power Path in 8-pin ESOP

### DESCRIPTION

ETAGOO2 is a switching Li-lon battery charger with dynamic power-path control and input current limiting.

When a battery is connected, depending on the battery voltage, the DC-DC switching regulator either pre-conditions, fast-charges the battery or just regulates a system voltage ( $V_{SYS}$ ) to a preset voltage. It does not require an external sense resistor

### **FEATURES**

- Switching Charger with Power Path Management
- ◆ Up to 95% DC-DC Efficiency
- 50mΩ Power Path MOSFET
- Up to 2.5A Max charging current
- Instant on with a dead Battery or no Battery
- No battery detection
- No External Sense resistor
- Programmable Charging Current

for current sensing. The fast-charging current is determined by programming ISET pin. When the battery voltage reaches the termination voltage i.e. 4.2V, the charging path disconnects SYS to BATT. The ETA6002 also includes a dynamic power path when the SYS load current exceeds current limit of the DCDC regulator internally set, the SYS voltage falls below  $V_{BATT}$ , ETA6002 turns on the power-path to supplement the system load through the battery.

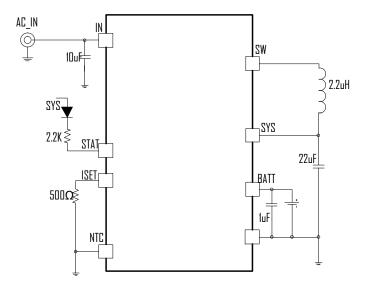
### **APPLICATIONS**

- ◆ Tablet, MID
- Smart Phone
- Power Bank

#### DRDERING INFORMATION

PART	PACKAGE PIN	(AGE PIN TOP MARK	
ETA6002E8A	ESOP-8	ETA6002	
		<u>YWWPL</u>	

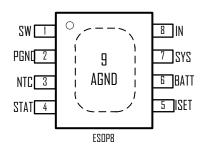
### TYPICAL APPLICATION



2A Switching Charger with Minimum Component Count



## PIN CONFIGURATION



### ABSOLUTEMAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

IN, BATT Voltage	0.3V to 6V
All Other Pin Voltage	VIN-0.3V to VIN+0.3
SW, SYS, BATT to ground current	Internally limited
Operating Temperature Range	40°C to 85°
Storage Temperature Range	55°C to 150°C
Thermal Resistance	AL $\Theta$
FSNPR	5N nC/W

### **ELECTRICAL CHACRACTERISTICS**

( $V_{\text{IM}}$  = 5V, unless otherwise specified. Typical values are at TA = 25oC.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
IN INPUT					
INPUT Range		4.4		5.5	٧
INPUT UVLO	Rising, Hys=500mV		4.35		V
INDUT D D	Switcher Enable, Switching		5		mA
INPUT Operating Current	Switcher Enable, No Switching		70		μА
BATT to INPUT leakage Current	Input Floating		0	5	μА
DC-DC and SYS OUTPUT					
NIMZYZV	I <sub>SYS</sub> =1A, Default		3.6		٧
VSYSMAX			4.5		V
Load Regulation			40		mV/A
Line Regulation	V <sub>IN</sub> = 4.75 to 5.25V		0.04		%/V
Switching Frequency			3		MHz
Max duty		100			%
HIGHSIDE MOS RDSON	I <sub>SW</sub> =500mA		100		mΩ
LOWSIDE MOS RDSON I <sub>SW</sub> =500mA			60		mΩ
HIGHSIDE Current limit			3.5		Α
ZAZ NAFO	Falling, Hys=200mV	2.25		V	
Thermal Shutdown	Rising, Hys=30°C		160		°C
POWER PATH Management					
BATT TO SYS RDSON			50		mΩ
BATTERY CHARGER					
Battery CV voltage	I <sub>BAT</sub> = DmA, default	4.16	4.2	4.24	V
Charger Restart Threshold	From DONE to Fast Charge		-200		m۷
Battery Pre-condition Voltage	V <sub>BAT</sub> Rising Hys=180mV		2.9		٧





PARAMETER	CONDITIONS	MIN	ТҮР	MAX	ZTINU
			100		mA
AC Fast Charge Current	Riseti =500Ω, nUSB_DET= Vin		2		Α
Pre-condition Timer			120		min
Fast-Charge Timer			960		min
THERMISTOR MONITOR					
NTC Threshold, Cold	Charger Suspended		76.5		%V <sub>IN</sub>
NTC Threshold, Hot	Charger Suspended		35		%Vin
NTC Threshold Hysteresis			1.5		%V <sub>IN</sub>
NTC Disable Threshold			100		m۷
NTC Input Leakage			0		μА
STATS					
STAT Output Low Voltage	I <sub>STATS</sub> =10mA			0.2	٧

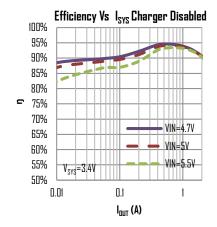
# PIN DESCRIPTION

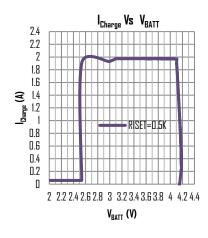
PIN#	NAME	DESCRIPTION	
1	SW	Switching node of the Switching Regulator. Connect a 1µH to 2.2µH inductor from this pin to	
		ZYZ	
2	PGND	Power Ground Pin. Bypass with a 10μF capacitor to IN	
3	NTC	Thermistor input	
4	ZTATZ	Status pin for Charging status indications. An open drain device capable of driving 10mA current	
5	ISET	Fast Charge Current set pin. Connecting a Resistor between ISET to GND This sets the fast	
		charge current value	
6	BATT	Battery pin. Connect a Battery to this pin	
7	SYS	System Voltage Pin. It is also the Switching regulator's output pin. Connect an inductor and	
		capacitor to form the output filter	
8	IN	Input pin. Can be connected to an AC adaptor or a USB charger output. Bypass with a $10\mu F$	
		capacitor each to PGND	
9 (EP)	AGND	Exposed pad for analog ground connection. Must be connected to PGND on PCB	

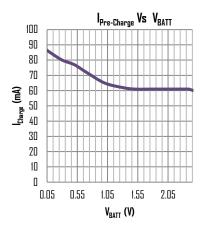


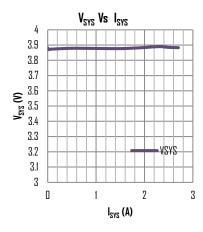
### TYPICAL CHARACTERISTICS

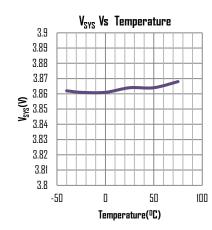
(Typical values are at  $T_A=25\,^{\circ}\text{C}$  unless otherwise specified.)

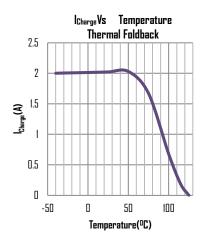














### PACKAGE DUTLINE

