

DS1691A/DS3691 (RS-422/RS-423) Line Drivers with TRI-STATE® Outputs

General Description

The DS1691A/DS3691 are low power Schottky TTL line drivers designed to meet the requirements of EIA standards RS-422 and RS-423. They feature 4 buffered outputs with high source and sink current capability with internal short circuit protection. A mode control input provides a choice of operation either as 4 single-ended line drivers or 2 differential line drivers. A rise time control pin allows the use of an external capacitor to slow the rise time for suppression of near end crosstalk to other receivers in the cable. Rise time capacitors are primarily intended for waveshaping output signals in the single-ended driver mode. Multipoint applications in differential mode with waveshaping capacitors is not allowed.

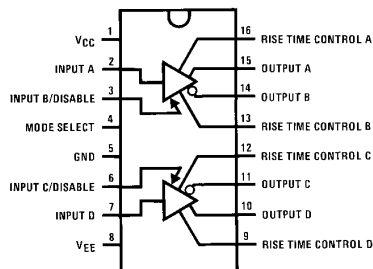
With the mode select pin low, the DS1691A/DS3691 are dual-differential line drivers with TRI-STATE outputs. They feature $\pm 10V$ output common-mode range in TRI-STATE mode and 0V output unbalance when operated with $\pm 5V$ supply.

Features

- Dual RS-422 line driver with mode pin low, or quad RS-423 line driver with mode pin high
- TRI-STATE outputs in RS-422 mode
- Short circuit protection for both source and sink outputs
- Outputs will not clamp line with power off or in TRI-STATE
- 100 Ω transmission line drive capability
- Low I_{CC} and I_{EE} power consumption
 - RS-422 $I_{CC} = 9$ mA/driver typ
 - RS-423 $I_{CC} = 4.5$ mA/driver typ
 - $I_{EE} = 2.5$ mA/driver typ
- Low current PNP inputs compatible with TTL, MOS and CMOS
- Pin compatible with AM26LS30

Connection Diagrams

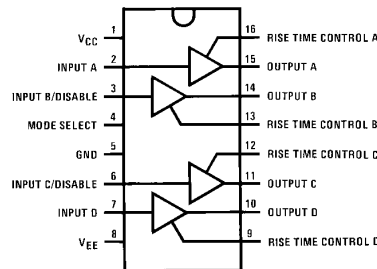
**With Mode Select LOW
(RS-422 Connection)**



Top View

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**With Mode Select HIGH
(RS-423 Connection)**



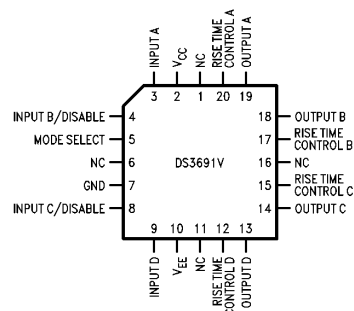
Top View

TL/F/5783-2

Truth Table

Operation	Inputs			Outputs	
	Mode	A (D)	B (C)	A (D)	B (C)
RS-422	0	0	0	0	1
	0	0	1	TRI-STATE	TRI-STATE
	0	1	0	1	0
	0	1	1	TRI-STATE	TRI-STATE
RS-423	1	0	0	0	0
	1	0	1	0	1
	1	1	0	1	0
	1	1	1	1	1

Order Number DS1691AJ, DS3691J,
DS3691M, DS3691N or DS3691V
See NS Package Number J16A, M16A, N16A or V20A
For Complete Military 883 Specifications,
see RETS Data Sheet
Order Number DS1691AJ/883
See NS Package Number J16A



Top View

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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage	
V_{CC}	7V
V_{EE}	-7V
Maximum Power Dissipation* at 25°C	
Cavity Package	1509 mW
Molded DIP Package	1476 mW
SO Package	1051 mW
Input Voltage	15V
Output Voltage (Power OFF)	± 15V
Storage Temperature	-65°C to + 150°C
Lead Temperature (Soldering, 4 seconds)	260°C

*Derate cavity package 10.1 mW/°C above 25°C; derate molded DIP package 11.9 mW/°C above 25°C. Derate SO package 8.41 mW/°C above 25°C.

Operating Conditions

	Min	Max	Units
Supply Voltage			
DS1691A			
V_{CC}	4.5	5.5	V
V_{EE}	-4.5	-5.5	V
DS3691			
V_{CC}	4.75	5.25	V
V_{EE}	-4.75	-5.25	V
Temperature (T_A)			
DS1691A	-55	+ 125	°C
DS3691	0	+ 70	°C

DC Electrical Characteristics (Notes 2, 3, 4 and 5)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
RS-422 CONNECTION, V_{EE} CONNECTION TO GROUND, MODE SELECT ≤ 0.8V						
V_{IH}	High Level Input Voltage		2			V
V_{IL}	Low Level Input Voltage				0.8	V
I_{IH}	High Level Input Current	$V_{IN} = 2.4V$		1	40	μA
		$V_{IN} \leq 15V$		10	100	μA
I_{IL}	Low Level Input Current	$V_{IN} = 0.4V$		-30	-200	μA
V_I	Input Clamp Voltage	$I_{IN} = -12 \text{ mA}$			-1.5	V
V_O $\overline{V_O}$	Differential Output Voltage $V_{A,B}$	$R_L = \infty$	$V_{IN} = 2V$	3.6	6.0	V
				-3.6	-6.0	V
V_T $\overline{V_T}$	Differential Output Voltage $V_{A,B}$	$R_L = 100\Omega$ $V_{CC} \geq 4.75V$	$V_{IN} = 2V$	2	2.4	V
			$V_{IN} = 0.8V$	-2	-2.4	V
$V_{OS}, \overline{V_{OS}}$	Common-Mode Offset Voltage	$R_L = 100\Omega$		2.5	3	V
$ V_T - \overline{V_T} $	Difference in Differential Output Voltage	$R_L = 100\Omega$		0.05	0.4	V
$ V_{OS} - \overline{V_{OS}} $	Difference in Common-Mode Offset Voltage	$R_L = 100\Omega$		0.05	0.4	V
V_{SS}	$ V_T - \overline{V_T} $	$R_L = 100\Omega, V_{CC} \geq 4.75V$	4.0	4.8		V
V_{CMR}	Output Voltage Common-Mode Range	$V_{DISABLE} = 2.4V$	± 10			V
I_{XA}	Output Leakage Current Power OFF	$V_{CC} = 0V$	$V_{CMR} = 10V$		100	μA
I_{XB}			$V_{CMR} = -10V$		-100	μA
I_{OX}	TRI-STATE Output Current	$V_{CC} = \text{Max}$ $V_{EE} = 0V \text{ and } -5V$	$V_{CMR} \leq 10V$		100	μA
			$V_{CMR} \geq -10V$		-100	μA
I_{SA}	Output Short Circuit Current	$V_{IN} = 0.4V$	$V_{OA} = 6V$	80	150	mA
			$V_{OB} = 0V$	-80	-150	mA
I_{SB}	Output Short Circuit Current	$V_{IN} = 2.4V$	$V_{OA} = 0V$	-80	-150	mA
			$V_{OB} = 6V$	80	150	mA
I_{CC}	Supply Current			18	30	mA

AC Electrical Characteristics $T_A = 25^\circ\text{C}$ (Note 5)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
RS-422 CONNECTION, $V_{CC} = 5\text{V}$, MODE SELECT = 0.8V						
t_r	Output Rise Time	$R_L = 100\Omega$, $C_L = 500\text{pF}$ (Figure 1)		120	200	ns
t_f	Output Fall Time	$R_L = 100\Omega$, $C_L = 500\text{pF}$ (Figure 1)		120	200	ns
t_{PDH}	Output Propagation Delay	$R_L = 100\Omega$, $C_L = 500\text{pF}$ (Figure 1)		120	200	ns
t_{PDL}	Output Propagation Delay	$R_L = 100\Omega$, $C_L = 500\text{pF}$ (Figure 1)		120	200	ns
t_{PZL}	TRI-STATE Delay	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0\text{pF}$ (Figure 4)		250	350	ns
t_{PZH}	TRI-STATE Delay	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0\text{pF}$ (Figure 4)		180	300	ns
t_{PLZ}	TRI-STATE Delay	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0\text{pF}$ (Figure 4)		180	300	ns
t_{PHZ}	TRI-STATE Delay	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0\text{pF}$ (Figure 4)		250	350	ns

DC Electrical Characteristics (Notes 2, 3, 4 and 5)

Symbol	Parameter	Conditions		Min	Typ	Max	Units
RS-423 CONNECTION, $ V_{CC} = V_{EE} $, MODE SELECT $\geq 2V$							
V_{IH}	High Level Input Voltage			2			V
V_{IL}	Low Level Input Voltage					0.8	V
I_{IH}	High Level Input Current	$V_{IN} = 2.4V$			1	40	μA
		$V_{IN} \leq 15V$			10	100	μA
I_{IL}	Low Level Input Current	$V_{IN} = 0.4V$			-30	-200	μA
V_I	Input Clamp Voltage	$I_{IN} = -12\text{ mA}$				-1.5	V
V_O	Output Voltage	$R_L = \infty$, (Note 6) $V_{CC} \geq 4.75V$	$V_{IN} = 2V$	4.0	4.4	6.0	V
$\overline{V_O}$			$V_{IN} = 0.4V$	-4.0	-4.4	-6.0	V
V_T	Output Voltage	$R_L = 450\Omega$ $V_{CC} \geq 4.75V$	$V_{IN} = 2.4V$	3.6	4.1		V
$\overline{V_T}$			$V_{IN} = 0.4V$	-3.6	-4.1		V
$ V_T - \overline{V_T} $	Output Unbalance	$ V_{CC} = V_{EE} = 4.75V, R_L = 450\Omega$			0.02	0.4	V
I_{X^+}	Output Leakage Power OFF	$V_{CC} = V_{EE} = 0V$	$V_O = 6V$		2	100	μA
I_{X^-}	Output Leakage Power OFF	$V_{CC} = V_{EE} = 0V$	$V_O = -6V$		-2	-100	μA
I_{S^+}	Output Short Circuit Current	$V_O = 0V$	$V_{IN} = 2.4V$		-80	-150	mA
I_{S^-}	Output Short Circuit Current	$V_O = 0V$	$V_{IN} = 0.4V$		80	150	mA
I_{SLEW}	Slew Control Current				± 140		μA
I_{CC}	Positive Supply Current	$V_{IN} = 0.4V, R_L = \infty$			18	30	mA
I_{EE}	Negative Supply Current	$V_{IN} = 0.4V, R_L = \infty$			-10	-22	mA

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.

Note 2: Unless otherwise specified, min/max limits apply across the -55°C to $+125^\circ\text{C}$ temperature range for the DS1691A and across the 0°C to $+70^\circ\text{C}$ range for the DS3691. All typicals are given for $V_{CC} = 5\text{V}$ and $T_A = 25^\circ\text{C}$. V_{CC} and V_{EE} as listed in operating conditions.

Note 3: All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to ground unless otherwise specified.

Note 4: Only one output at a time should be shorted.

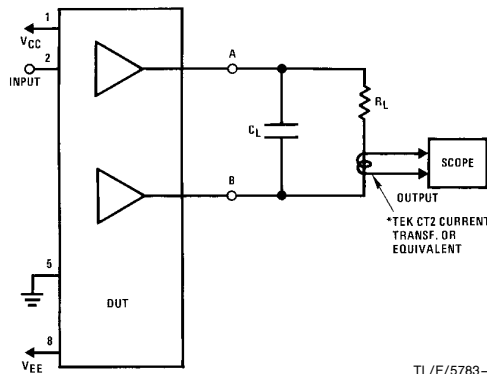
Note 5: Symbols and definitions correspond to EIA RS-422 and/or RS-423 where applicable.

Note 6: At -55°C , the output voltage is $+3.9\text{V}$ minimum and -3.9V minimum.

AC Electrical Characteristics $T_A = 25^\circ\text{C}$ (Note 5)

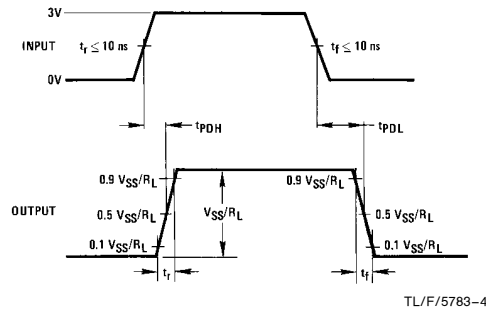
Symbol	Parameter	Conditions	Min	Typ	Max	Units
RS-423 CONNECTION, $V_{CC} = 5\text{V}$, $V_{EE} = -5\text{V}$, MODE SELECT = 2.4V						
t_r	Rise Time	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0$ (Figure 2)		120	300	ns
t_f	Fall Time	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0$ (Figure 2)		120	300	ns
t_r	Rise Time	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 50\text{pF}$ (Figure 3)		3.0		μs
t_f	Fall Time	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 50\text{pF}$ (Figure 3)		3.0		μs
t_{rc}	Rise Time Coefficient	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 50\text{pF}$ (Figure 3)		0.06		$\mu\text{s}/\text{pF}$
t_{PDH}	Output Propagation Delay	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0$ (Figure 2)		180	300	ns
t_{PDL}	Output Propagation Delay	$R_L = 450\Omega$, $C_L = 500\text{pF}$, $C_C = 0$ (Figure 2)		180	300	ns

AC Test Circuits and Switching Time Waveforms

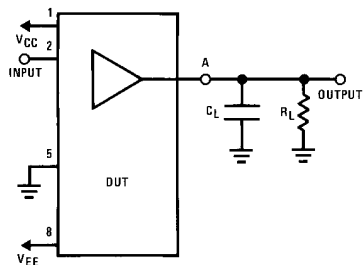


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FIGURE 1. Differential Connection

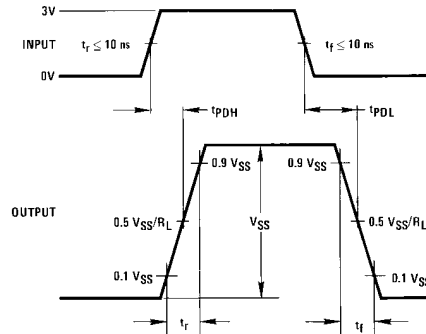


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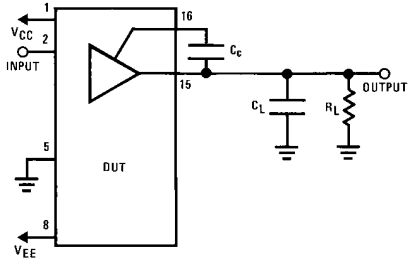
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FIGURE 2. RS-423 Connection

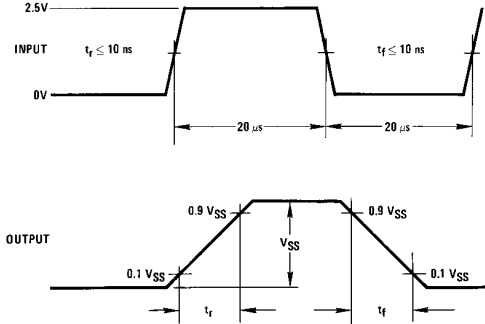


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AC Test Circuits and Switching Time Waveforms (Continued)

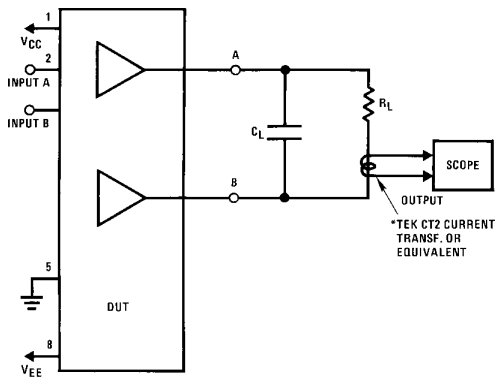


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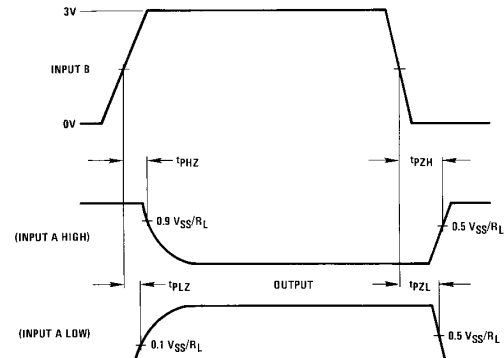


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FIGURE 3. Rise Time Control for RS-423



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TL/F/5783-10

FIGURE 4. TRI-STATE Delays

Switching Waveforms

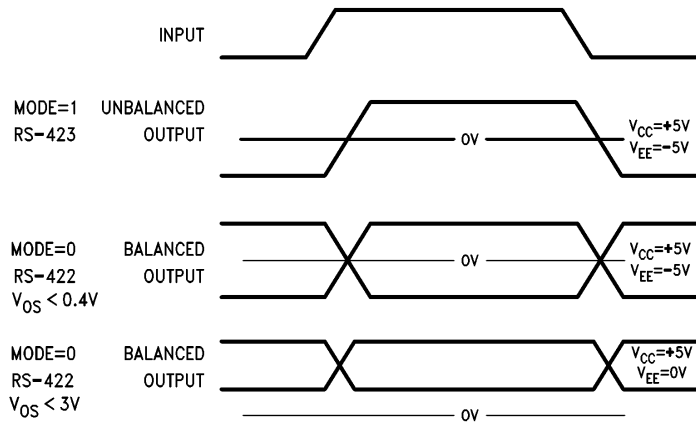
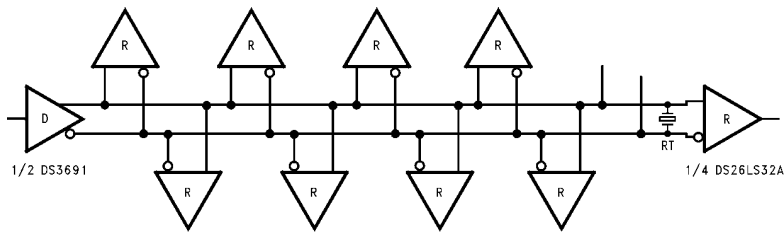


FIGURE 5. Typical Output Voltage

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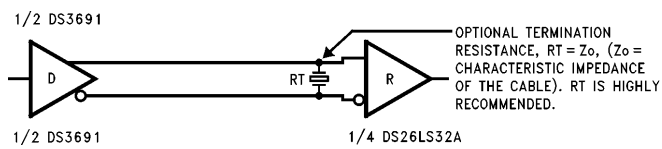
Typical Application Information

Fully Loaded RS-422 Interface



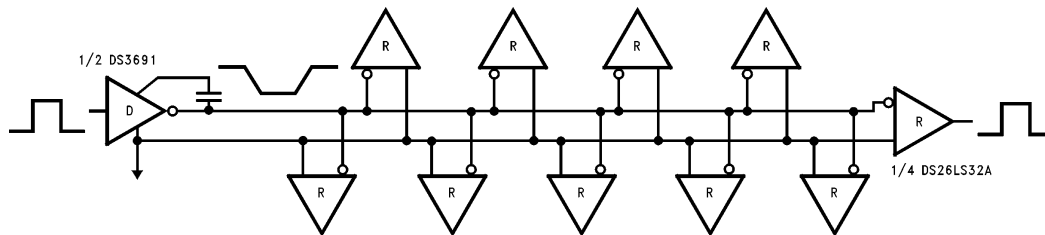
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RS-422 Point to Point Application



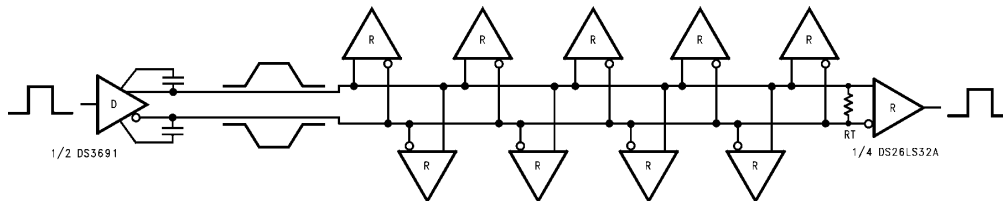
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Fully Loaded RS-423 Interface



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Differential Application with Rise Time Control

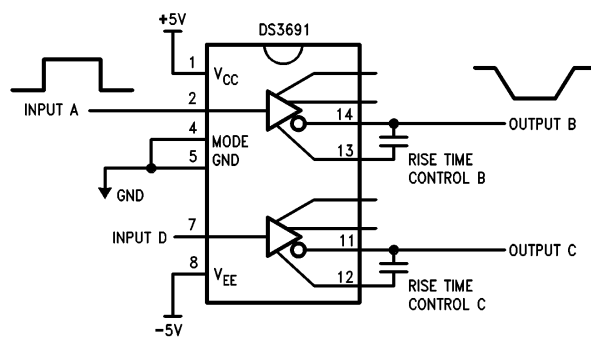


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***Note:** Controlled edge allows longer stub lengths. Multiple Drivers are NOT allowed.

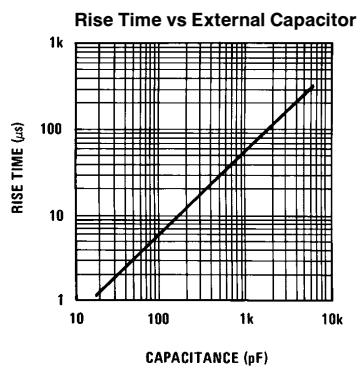
Typical Application Information (Continued)

Dual RS-423 Inverting Driver



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Typical Rise Time Control Characteristics (RS-423 Mode)



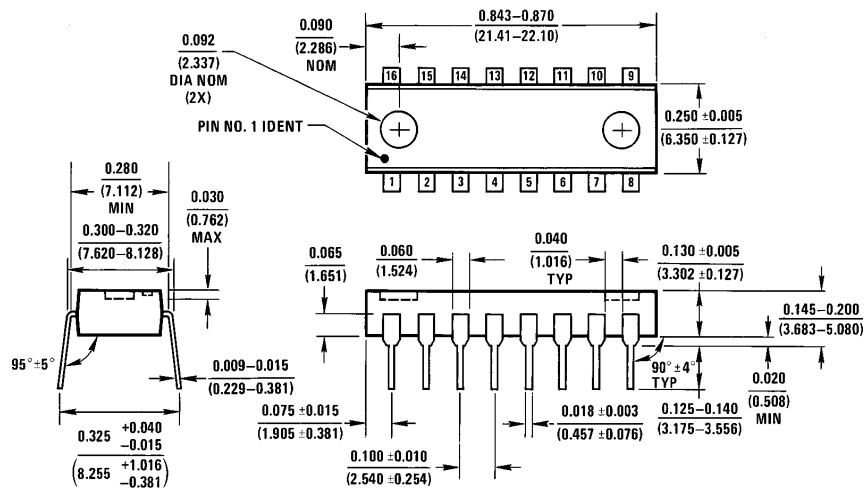
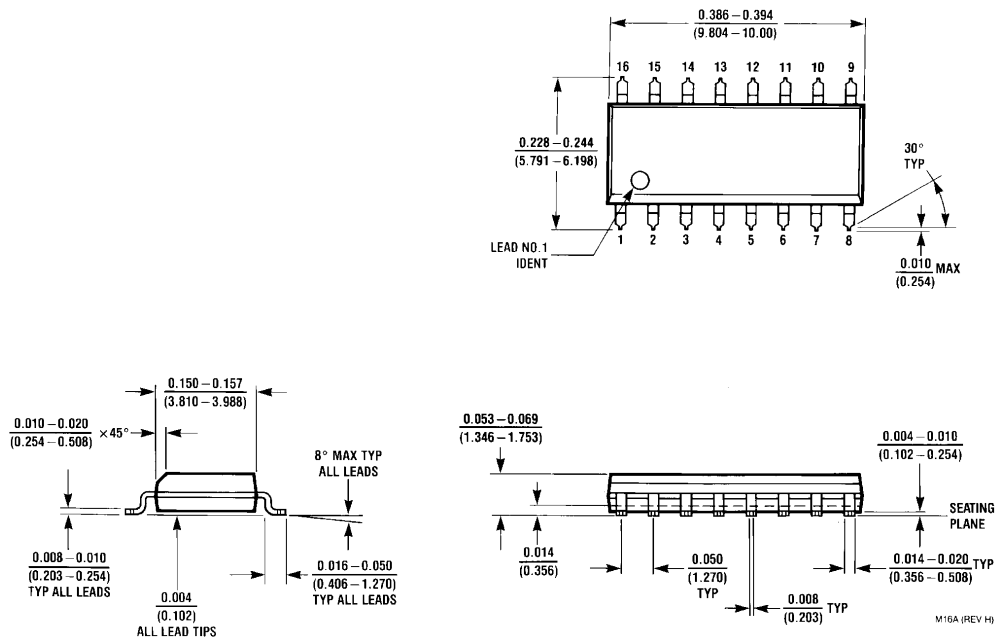
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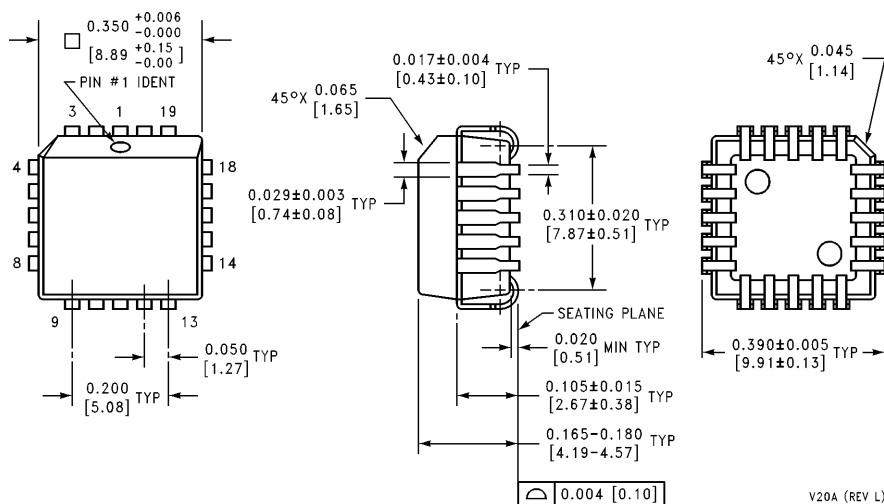
The drawing consists of three views: a top view, a side view, and a detail view of the lead profile.

- Top View:** Shows a rectangular package with 16 pins on the left and 9 pins on the right. The overall width is 0.785" [19.94] MAX. The pin pitch is 0.025" [0.64]. The distance from the center of the first pin to the center of the last pin on the right is 0.220-0.310" [5.59-7.87]. The radius of the pin base is R 0.005-0.020 TYP [0.13-0.51].
- Side View:** Shows the package height and lead dimensions. The total height is 0.200" [5.08] MAX TYP. The lead height is 0.037 ± 0.005" [0.94 ± 0.13] TYP. The lead thickness is 0.055 ± 0.005" [1.40 ± 0.13] TYP. The lead width is 0.020-0.060" [0.51-1.52] TYP. The lead angle is 90° ± 4° TYP. The lead length is 0.150" [3.81] MIN TYP. The lead thickness at the base is 0.018 ± 0.003" [0.46 ± 0.08] TYP. The lead width at the base is 0.125-0.200" [3.18-5.08] TYP. The lead thickness at the base is 0.080" [2.03] MAX TYP. The lead width at the base is 0.100 ± 0.010" [2.54 ± 0.25] TYP. The lead thickness at the base is 0.010 ± 0.002" [0.25 ± 0.05] TYP. The lead width at the base is 0.310-0.410" [7.87-10.41] TYP. The lead thickness at the base is 0.010 ± 0.002" [0.25 ± 0.05] TYP. The lead width at the base is 0.310-0.410" [7.87-10.41] TYP.
- Detail View:** Shows a cross-section of the lead with a 95° ± 5° angle. The lead thickness is 0.010 ± 0.002" [0.25 ± 0.05] TYP. The lead width is 0.310-0.410" [7.87-10.41] TYP. The lead thickness is 0.010 ± 0.002" [0.25 ± 0.05] TYP. The lead width is 0.310-0.410" [7.87-10.41] TYP.

J16A (REV L)

Physical Dimensions inches (millimeters) (Continued)



Physical Dimensions inches (millimeters) (Continued)

Plastic Chip Carrier (V)
Order Number DS3691V
NS Package Number V20A

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