## Black Oak Engineering

		blackoakeng	g.com		Nev	w York USA											40		
		BlueJay PIC32MZ		ard													V		
		ver 1.2	'_n' means asserted low.  Polarity is with respect to BlueJay.					MISO = Micro In, Serial Device Out							Ш				
		©2021-2024						MOSI = Micro	Out, Seria					tata					
32MZ nin #	BlueJay name, primary functionality	Complete functionality	Header pin	5 V tolerant	Ethernet name	Ethernet function	Polarity	RMII		Arduino	Pin #	MikroBus	Pin #		JTAG & Trace	JTAG H5 Pin #	Ш	EBI Parallel II	PN Interface
1	GioG15	AN23/AERXERR/RG15	H5.2															- raranern	
2	GioA5	EBIA5/AN34/PMA5/RA5	H5.3														E	EBIA5	PMA!
3	GioE5	EBID5/AN17/RPE5/ PMD5/RE5	H5.4														E	EBID5	PMD
4	GioE6	EBID6/AN16/PMD6/RE6	H5.5															EBID6	PMD
5	GioE7	EBID7/AN15/PMD7/RE7	H5.6											П				EBID7	PMD
		EBIA6/AN22/RPC1/PMA6/												H					
6	GioC1	RC1 EBIA12/AN21/RPC2/	H5.7											Н				EBIA6	PMA
7	GioC2	PMA12/RC2 EBIWE/AN20/RPC3/	H5.8		+				+					Н				EBIA12	PMA:
8	GioC3	PMWR/RC3 EBIOE/AN19/RPC4/PMRD/	H4.1						+					Н			E	EBIWE	PMW
9	GioC4	RC4 AN14/C1IND/ECOL/RPG6/	H4.2											Н		$\vdash$	E	EBIOE	PMRI
10	GioG6	SCK2/RG6 EBIA4/AN13/C1INC/	H4.3		ECOL	Collision Detect	i	No						Н			Н		
11	GioG7	ECRS/RPG7/SDA4/ PMA4/RG7	H4.4		ECRS	Carrier Sense	i	No									F	EBIA4	PMA-
11	0.007	EBIA3/AN12/C2IND/ ERXDV/AERXDV/	114.4		ECRS	carrier sense		140						П				BIAT	TWA
12	GioG8	AECRSDV/RPG8/SCL4/ PMA3/RG8	H4.5		ERXDV	Receive Data Valid	i	No									E	EBIA3	PMA:
15	MCLR_n														MCLR_n	10			
		EBIA2/AN11/C2INC/ ERXCLK/EREFCLK/AERXCLK/ AEREFCLK/RPG9/PMA2/																	
16	GioG9	RG9	H4.6		ERXCLK	Receive Clock	i	No						Н			E	EBIA2	PMA:
17	GioA0	TMS/EBIA16/AN24/RA0	H4.7		-				-					Н	JTAG_TMS	2	E	EBIA16	+
18	GioE8	AN25/AERXD0/RPE8/RE8	H2.2														$\sqcup$		_
19	GioE9	AN26/AERXD1/RPE9/RE9	H2.3														Ц		_
20	GioB5	AN45/C1INA/RPB5/RB5	H2.4														Ц		
	Low Noise Analog	,															Ц		
21	Gan4	AN4/C1INB/RB4	H3.6																
22	Gan3	AN3/C2INA/RPB3/RB3	H3.5							PC3/A3	17								
23	Gan2	AN2/C2INB/RPB2/RB2	H3.4							PC2/A2	16								
24	Gan1	PGEC1/AN1/RPB1/RB1	нз.з							PC1/A1	15								
25	Gan0	PGED1/ANO/RPBO/RBO	H3.2							PCO/AO	14	AN	1						
		PGEC2/AN46/RPB6/RB6								T CO/AU	17	AIN	1						+
26	PGEC2		ICSP.5											Н			$\vdash$		+
27	PGED2	PGED2/AN47/RPB7/RB7 VREF-/CVREF-	ICSP.4											Н			H		+
28	AGnd Vref+ =	/AN27/AERXD2/RA9 VREF+/CVREF+/AN28/AERXD3							+					Н			$\vdash$	-	+
29	3.0 Vref	/RA10 EBIA10/AN48/RPB8/PMA10/R	H3.8				+		+				+	H		+	$\vdash$		+
32	GioB8	B8	H2.5				-		+				+	H		$\vdash$	E	EBIA10	PMA
33	GioB9 BattHalfV/	EBIA7/AN49/RPB9/PMA7/RB9 EBIA13/CVREFOUT/AN5/	H2.6						-					Н		$\vdash$	E	EBIA7	PMA:
34	GioB10	RPB10/PMA13/RB10 AN6/ERXERR/AETXERR/	Н3.7										1			$\sqcup$	E	EBIA13	PMA
35	GioB11	RB11	H2.7		ERXERR	Receive Error	i	Yes					_	Ц		igsquare	Ц		
38	GioA1	TCK/EBIA19/AN29/RA1	H2.8										1	Ш	JTAG_TCK	4	E	EBIA19	
39	GioF13	TDI/EBIA18/AN30/RPF13/ SCK5/RF13	H12.1												JTAG_TDI	8	E	EBIA18	
40	GioF12	TDO/EBIA17/AN31/RPF12/ RF12	H12.2							PD2/Int0				П	JTAG_TDO	6		EBIA17	

		EBIA11/AN7/ERXD0/AECRS/P													Г				П	
41	GioB12	MA11/RB12	H12.3		ERXD0	Receive Data 0	i	Yes		PD3/Int1	3							EBIA11	Ш	PMA11
42	GioB13	AN8/ERXD1/AECOL/RB13	H12.4		ERXD1	Receive Data 1	i	Yes		PD4	4									
43	GioB14	EBIA1/AN9/ERXD2/AETXD3/RP B14/SCK3/PMA1/RB14	H12.5		ERXD2	Receive Data 2	i .	No		PD5	5							EBIA1	П	PMA1
43	010814	EBIAO/AN10/ERXD3/	H12.5		ERADZ	Receive Data 2	<u> </u>	INO	t	PDS	3							EDIAI	Н	PIVIAI
	6: 845	AETXD2/RPB15/OCFB/			50/02					20.6								50140	П	
44	GioB15	PMA0/RB15	H12.6		ERXD3	Receive Data 3	i	No		PD6	6							EBIA0	H	PMA0
47	GioD14	AN32/AETXD0/RPD14/RD14 AN33/AETXD1/RPD15/SCK6/R	H12.7							PD7	7								H	
48	GioD15	D15	H12.8							PB0	8								Ц	
49	24MHz Osc																			ı
50	BattCheck	OSC2/CLKO/RC15																		
																			П	
51	Vbus	External USB 5V		Yes					+			$\vdash$							H	
52	Vusb3V3							-	╄										H	
54	D-																		Ц	
55	D+																			ı
56	GioF3	RPF3/USBID/RF3	H11.7							PB1	9									
									T			Г							П	
57	GioF2	EBIRDY3/RPF2/SDA3/RF2*	H11.6	Yes			1		t	PB2/SPI_CS_n	10	H				<del>                                     </del>		EBIRDY3	H	
58	GioF8	EBIRDY2/RPF8/SCL3/RF8*	H11.5	Yes			1		+			$\vdash$					_	EBIRDY2	H	
59	GioA2	EBICSO/SCL2/RA2*	H11.4	Yes					1							<u> </u>		EBICS0	Ц	
60	GioA3	EBIRDY1/SDA2/RA3*	H11.3	Yes								L						EBIRDY1	Ш	
61	GioA4	EBIA14/PMCS1/PMA14/ RA4*	H11.2	Yes														EBIA14	Π	PMA14
		EBIA9/RPF4/SDA5/PMA9/	Н8.3,								_		_						П	
64	UA1_RxD	RF4* EBIA8/RPF5/SCL5/PMA8/	H10.6 H8.2,	Yes					+	PD1/TxOut	1		Tx	13				EBIA9	Н	PMA9
65	UA1_TxD	RF5*	H10.5	Yes				-	+	PD0/RxIn	0		Rx	14				EBIA8	Н	PMA8
66	IIC1_SCL	AETXCLK/RPA14/SCL1/RA14*	H10.4	Yes						PC5/A5/SCL	19		SCL	12					Ц	
67	IIC1_SDA	AETXEN/RPA15/SDA1/RA15*	H10.3	Yes						PC4/A4/SDA	18		SDA	11						
68	GioD9	EBIA15/RPD9/PMCS2/ PMA15/RD9*	H10.2	Yes														EBIA15	П	PMA15
		RPD10/SCK4/RD10*																LDIAIS	Ħ	TIVIAL
69	GioD10	TestLED	H10.1	Yes		Ethernet			H										H	
70	GioD11	EMDC/AEMDC/RPD11/ RD11*	Н9.8	Yes	EMDC	Management Data Clock	0	No												
		EMDIO/AEMDIO/RPD0/				Ethernet			t										Ħ	
71	GioD0	RTCC/INTO/RD0*	H9.7	Yes	EMDIO	Management Data	io	Yes	+										H	
72	CLK_32KHz	SOSCI/RPC13/RC13	Н9.6						-									-	Н	
73	GioC14	SOSCO/RPC14/T1CK/RC14	Н9.5																Ц	
76	SPI1_SCK	RPD1/SCK1/RD1*	Н9.4	Yes						PB5/ SPI_SCK	13		SCK	4						
77	SPI1_MISO	EBID14/ETXEN/RPD2/ PMD14/RD2*	Н9.3	Yes	EMXEN	Transmit Enable	0	Yes		PB4/ SPI_MISO	12		MISO	5				EBID14	П	PMD14
		EBID15/ETXCLK/RPD3/							T	PB3/									П	
78	SPI1_MOSI	PMD15/RD3* EBID12/ETXD2/RPD12/	H9.2	Yes	ETXCLK	Transmit Clock	0	No		SPI_MOSI	11		MOSI	6				EBID15	H	PMD15
79	GioD12	PMD12/RD12* EBID13/ETXD3/PMD13/	H7.7	Yes	ETXD2	Transmit Data 2	0	No	-				RST	2				EBID12	Н	PMD12
80	GioD13	RD13*	H7.6	Yes	ETXD3	Transmit Data 3	0	No					cs	3				EBID13	Ц	PMD13
81	SQI_CS0_n	SQICSO/RPD4/RD4*		Yes															Ll	
82	GioD5	SQICS1/RPD5/RD5*	H7.5	Yes									INT	15					П	
		EBID11/ETXD1/RPF0/							T					13				soup : :	H	D1 /=
85	GioF0	PMD11/RF0* EBID10/ETXD0/RPF1/	H7.4	Yes			1		+			$\vdash$			$\vdash$	<del>                                     </del>	+	EBID11	Н	PMD11
86	GioF1	PMD10/RF1* EBID9/ETXERR/RPG1/	H7.3	Yes			1		+			$\vdash$				<u> </u>	_	EBID10	Н	PMD10
87	GioG1	PMD9/RG1*	H7.2	Yes				1	1									EBID9	Ц	PMD9
88	GioG0	EBID8/RPG0/PMD8/RG0*	H7.1	Yes						<u></u>								EBID8	Ш	PMD8
89	SQI_CLK	TRCLK/SQICLK/RA6*		Yes					Γ										П	
									t			Т			T				Ħ	
90	SQI_D3	TRD3/SQID3/RA7*		Yes			1		+			-				<del>                                     </del>			Н	
91	GioE0	EBIDO/PMD0/RE0*	H6.6	Yes			1		+				PWM	16			_	EBID0	Н	PMD0
94	GioE1	EBID1/PMD1/RE1*	H6.5	Yes														EBID1	Ц	PMD1
95	SQI_D2	TRD2/SQID2/RG14*		Yes																
							1		T			Γ							П	
96	SQI_D1	TRD1/SQID1/RG12*	$\vdash$	Yes			1		+			$\vdash$					-+	1	H	
97	SQI_D0	TRD0/SQID0/RG13*		Yes															Ш	

98	GioE2	EBID2/PMD2/RE2*	H6.4	Yes									EBID2	PMD2
99	GioE3	EBID3/RPE3/PMD3/RE3*	H6.3	Yes									EBID3	PMD3
100	GioE4	EBID4/AN18/PMD4/RE4	H6.2					PC4/Reset	X1				EBID4	PMD4