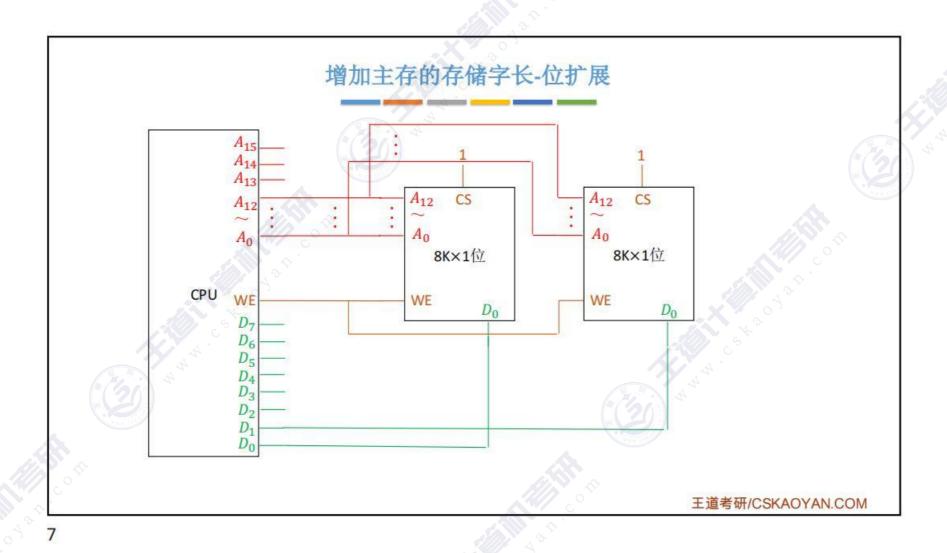
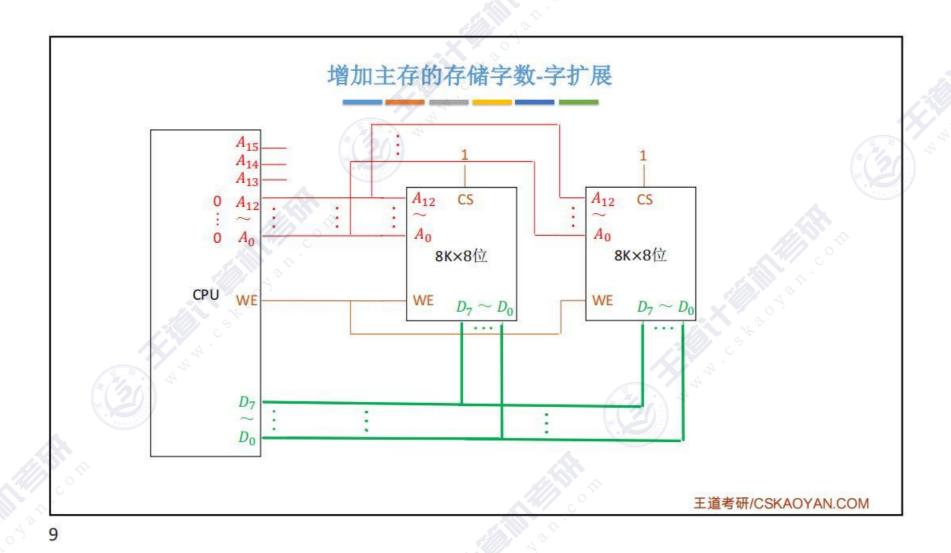
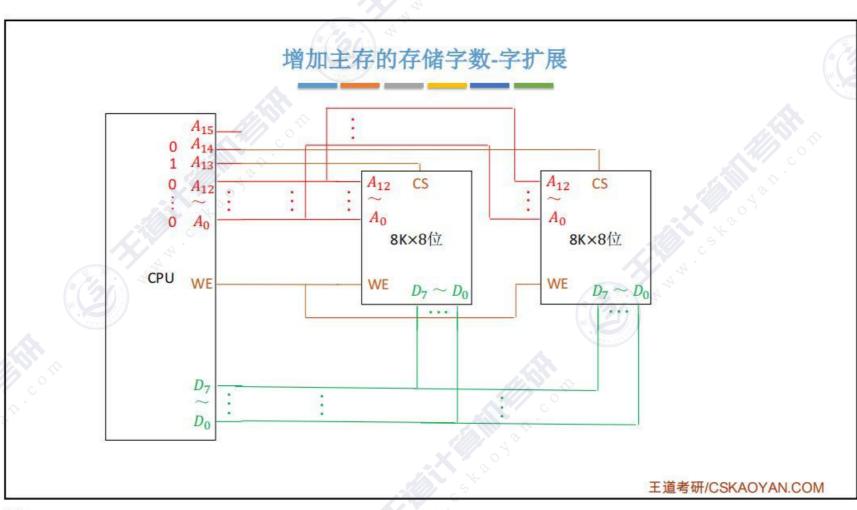


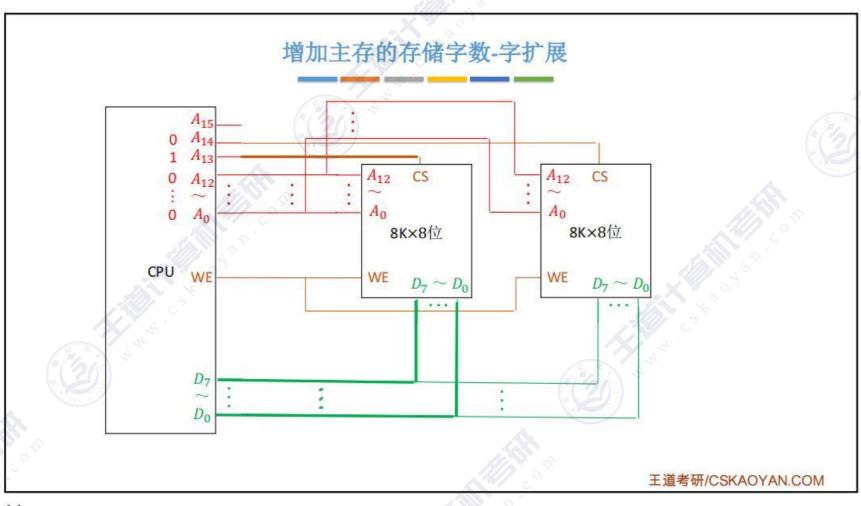


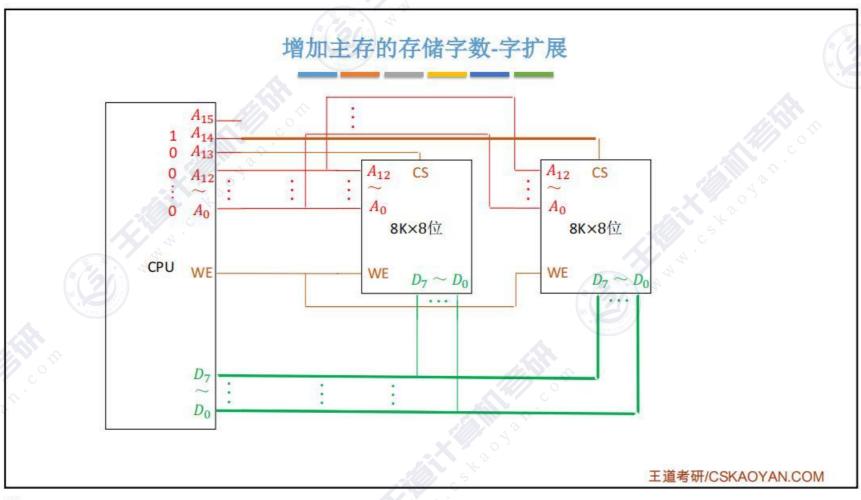
存储器芯片的输入输出信号 地址 存储矩 数据线 译 读 写 码 线 驱 电 动 阵 片选线· 读/写控制线 (CS 或 CE) (WE或WR) (CS或CE) 也可能分开为 WE 和 OE 两根读写线 王道考研/CSKAOYAN.COM

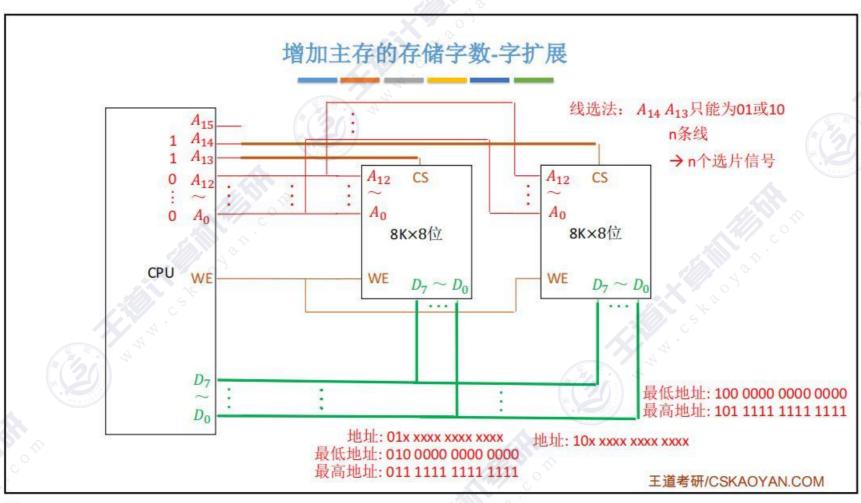


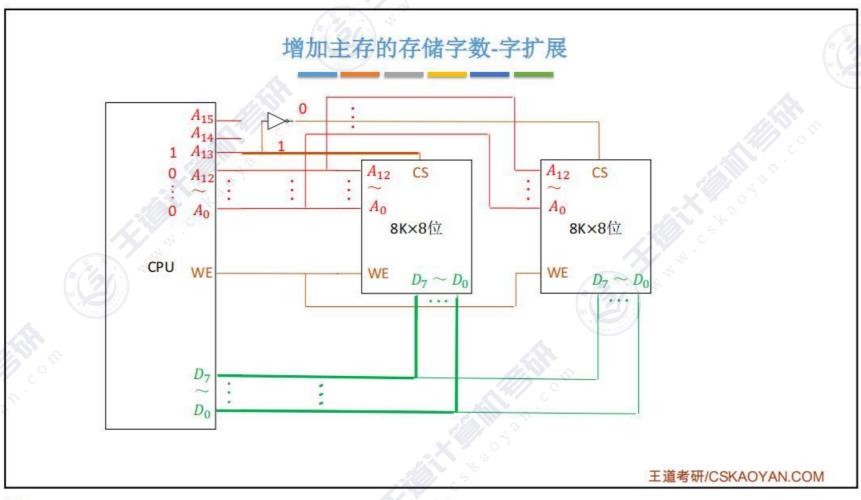


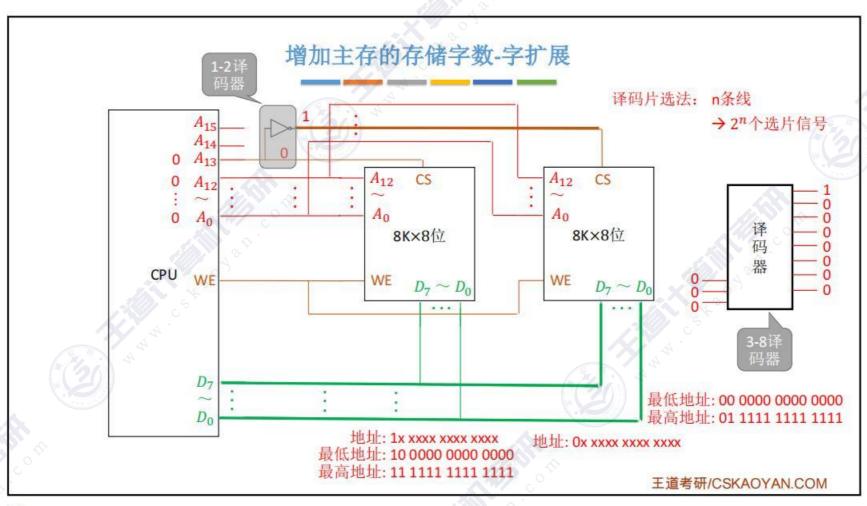


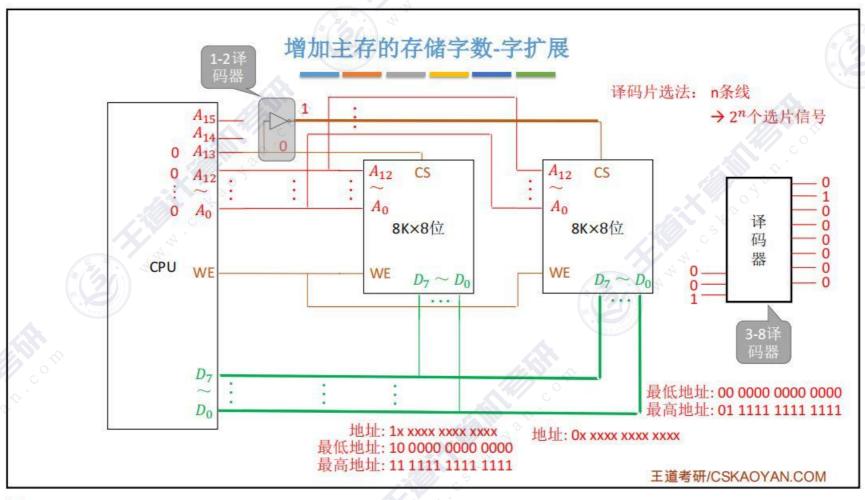


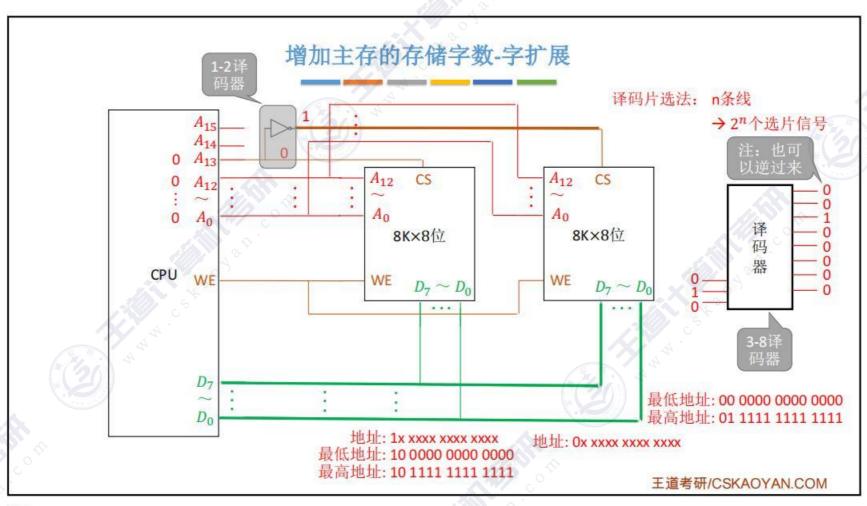


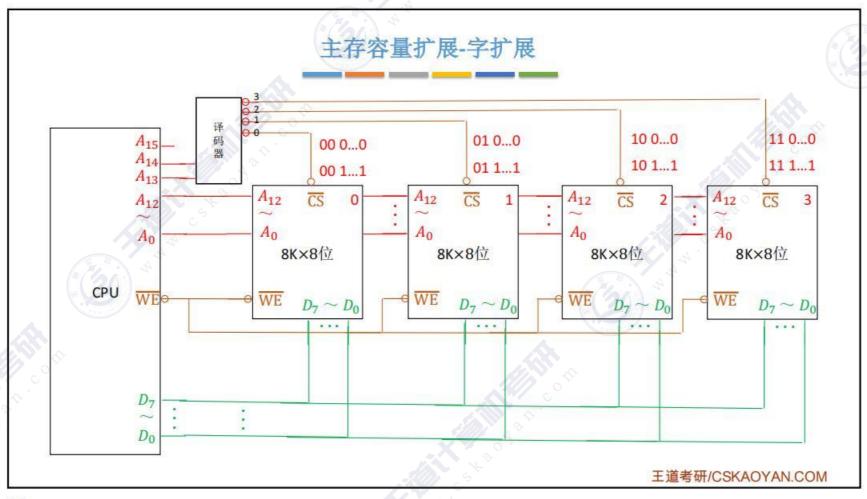


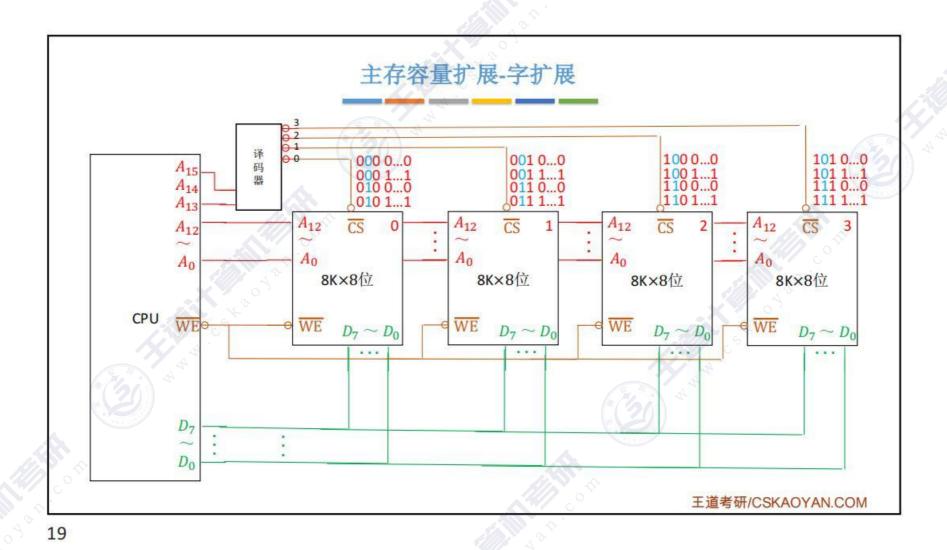


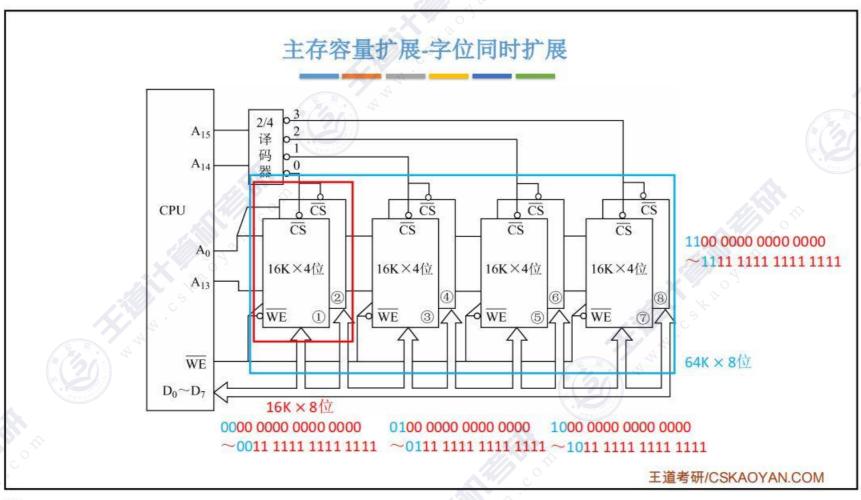


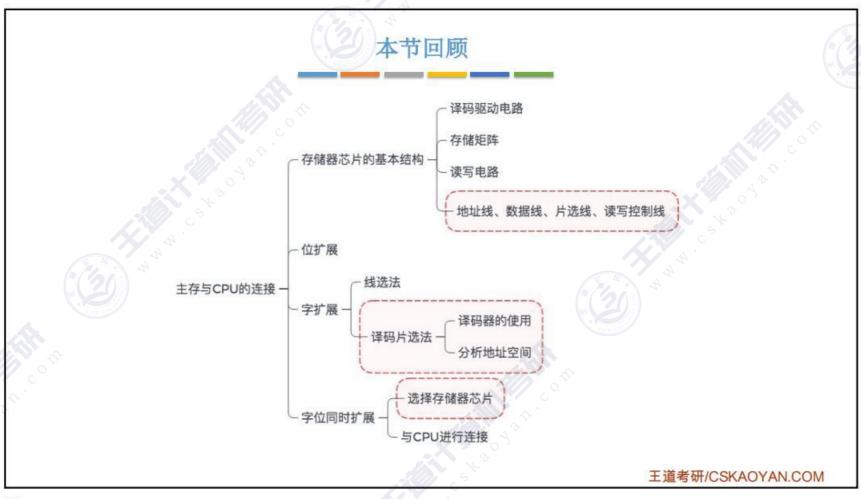


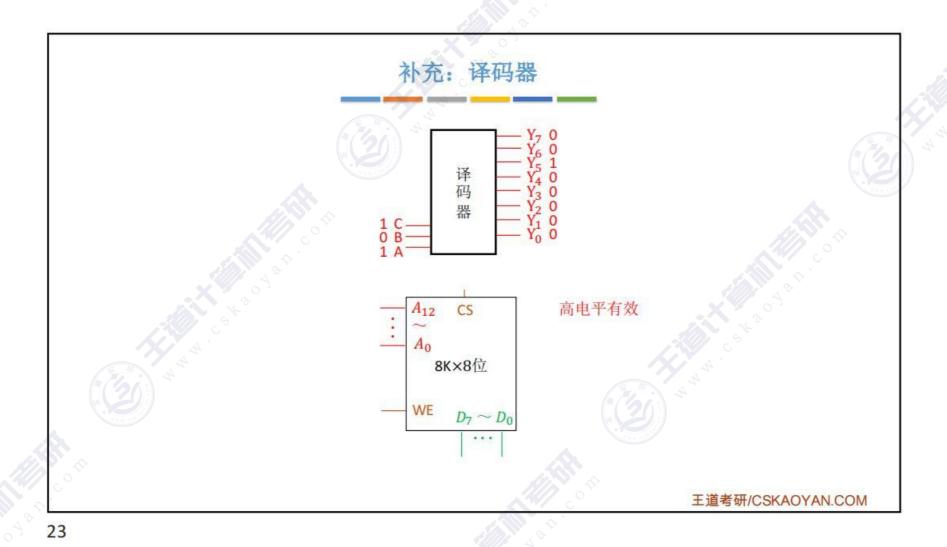


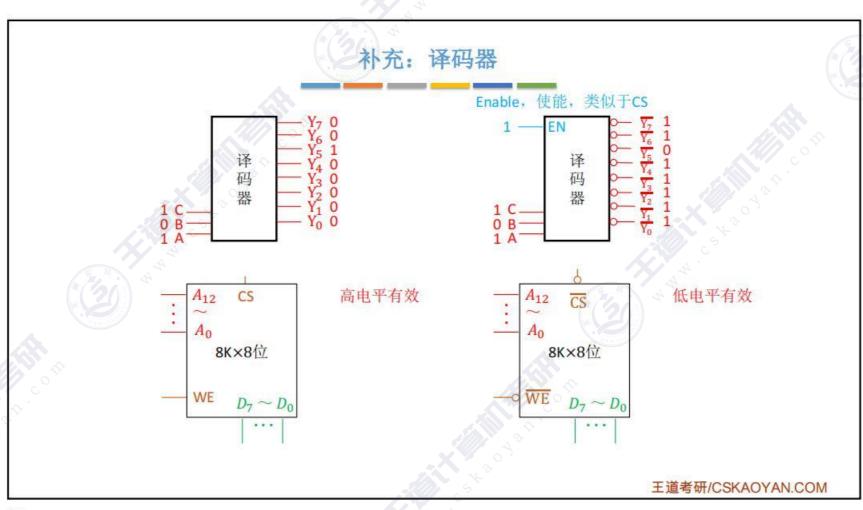


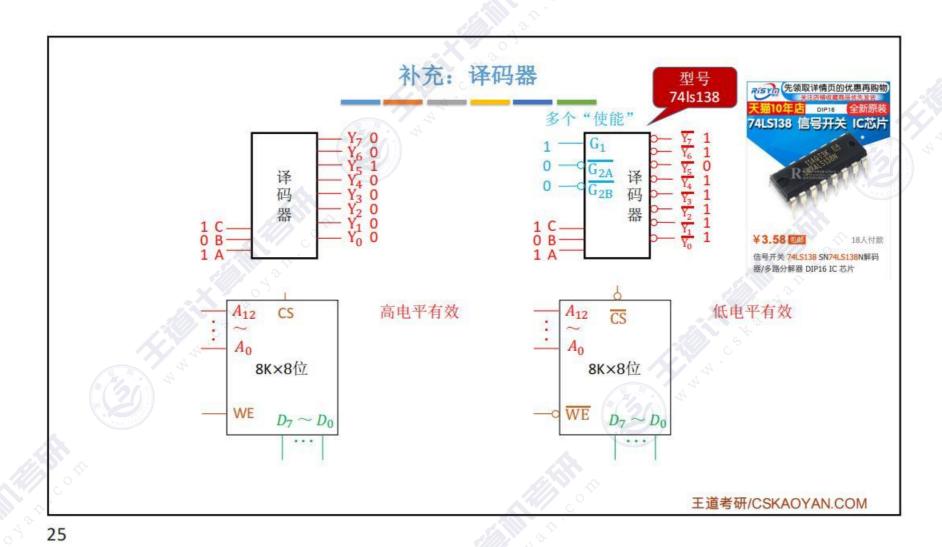


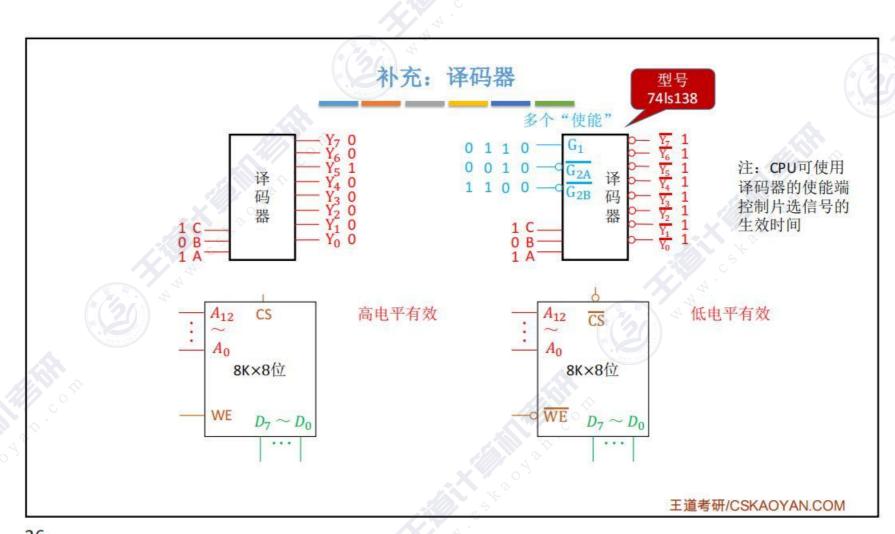


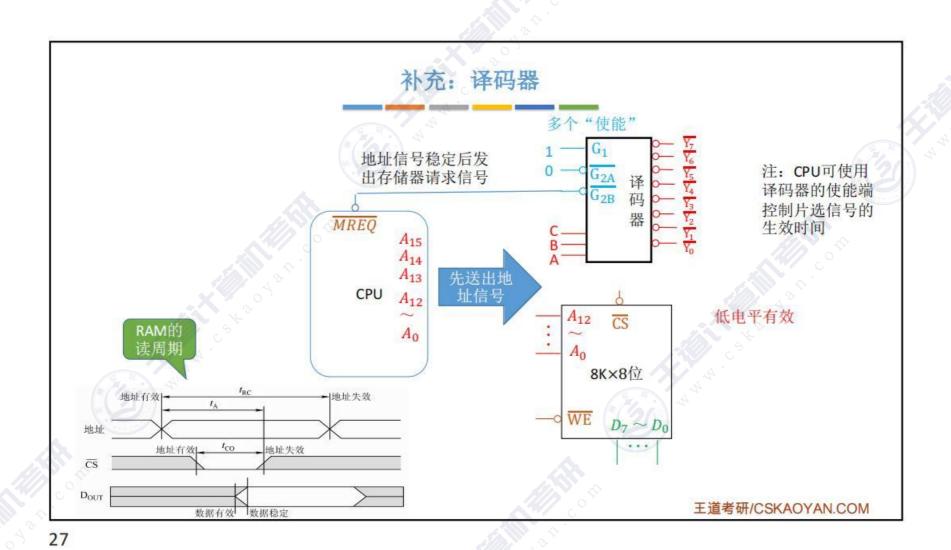












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