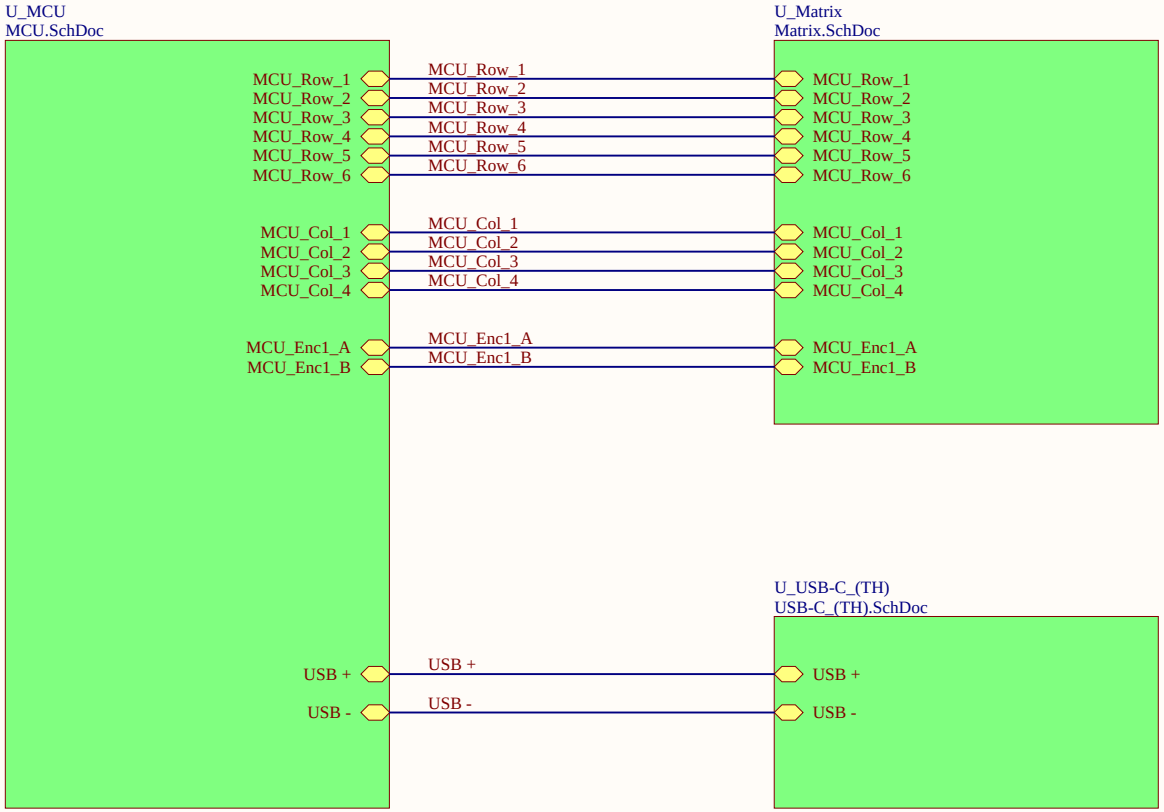
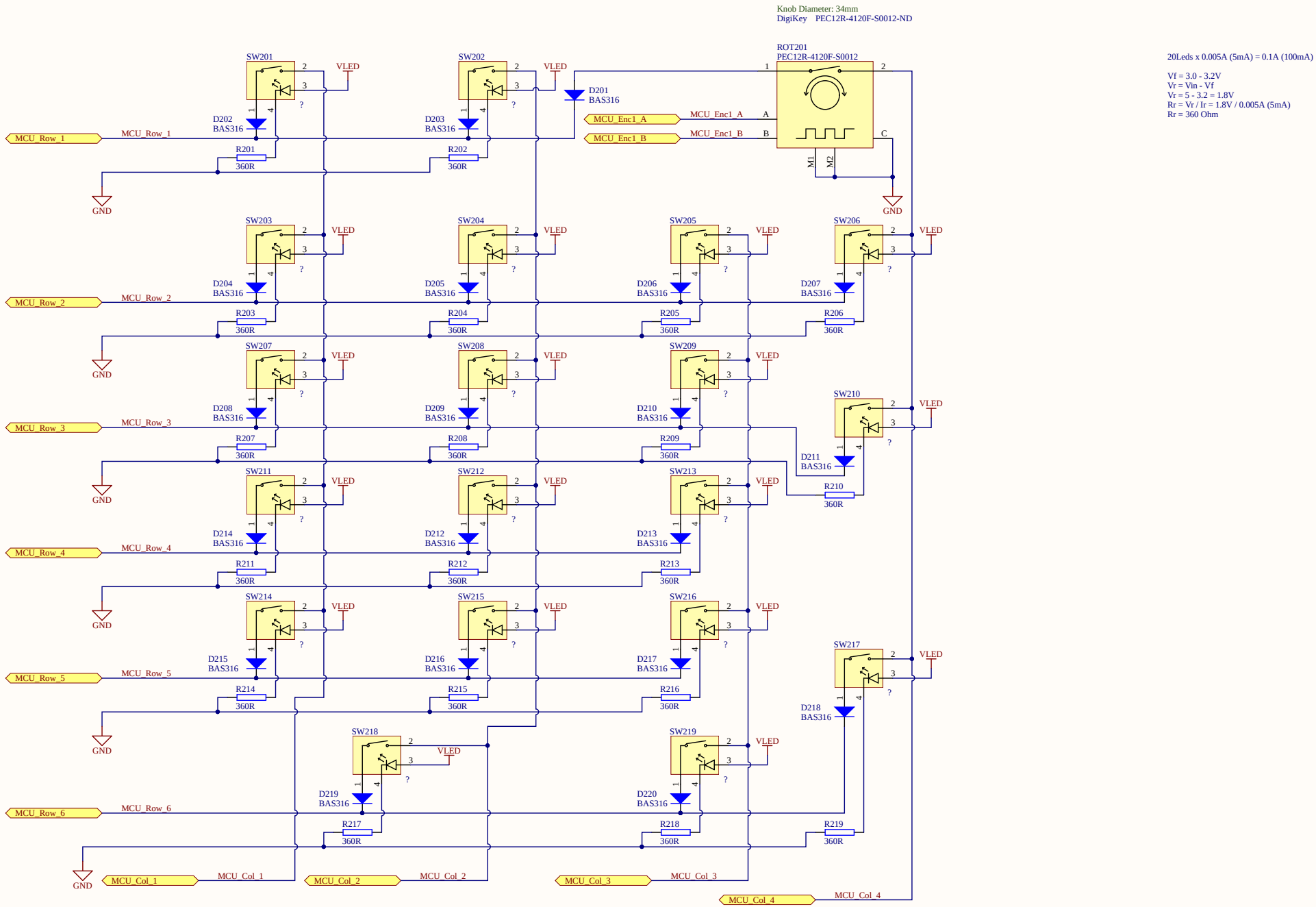
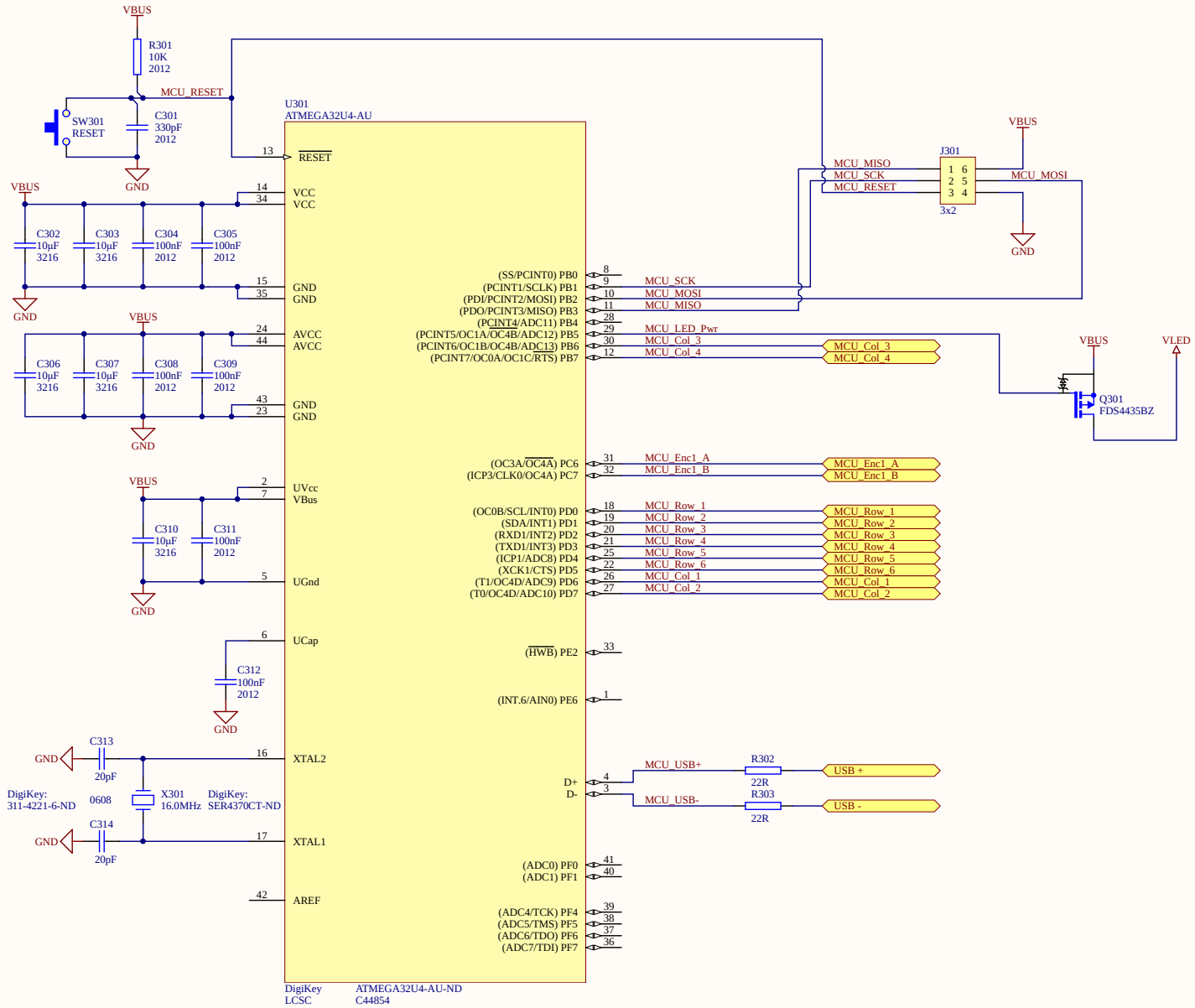


FD101 FD102 FD103 FD104

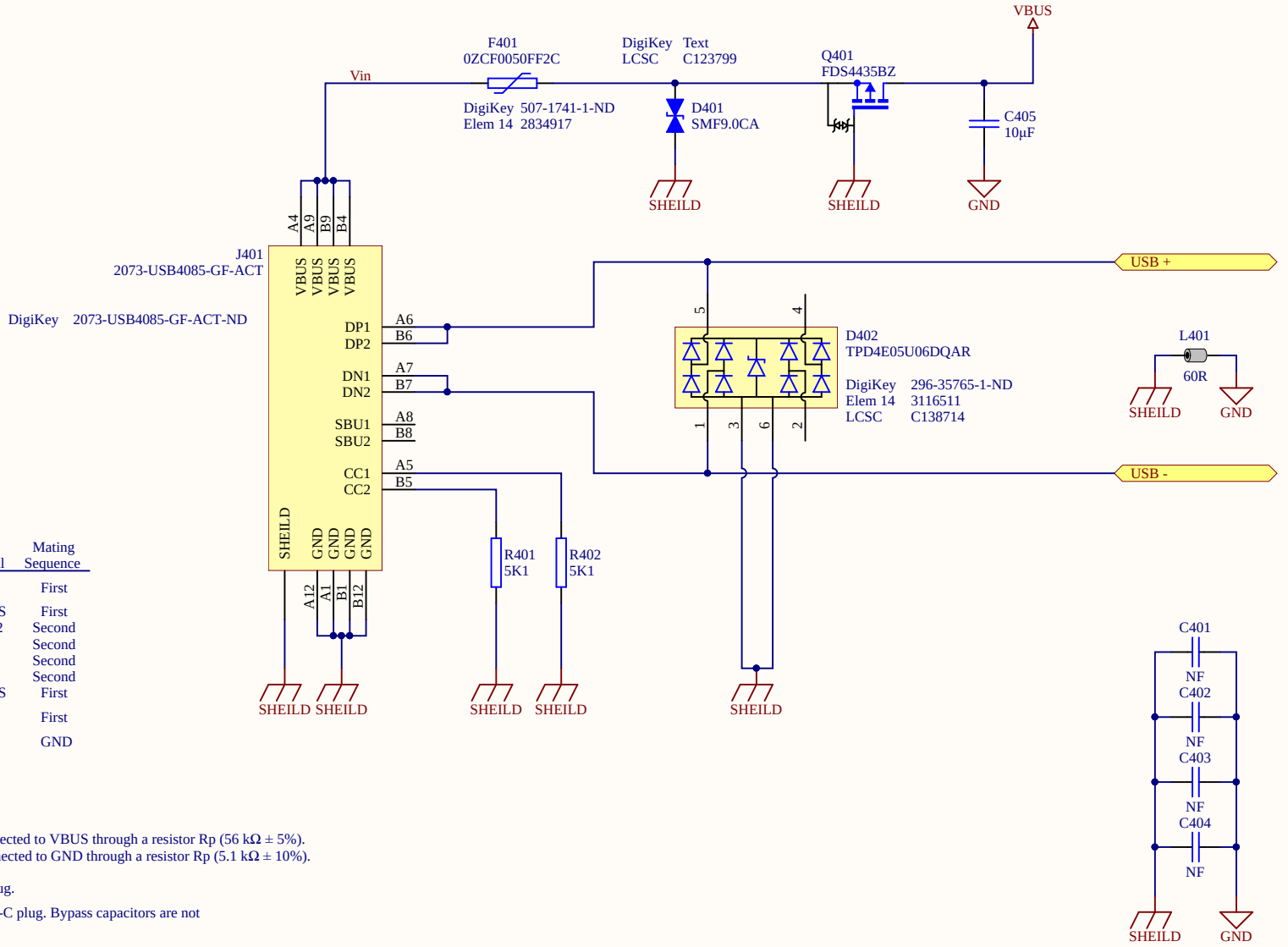
Title	TestNumPad - Top Sheet	
Size: A4	Number:	Revision: r1
Date: 11/07/2020	Sheet 1 of 4	
File: E:\Dropbox\..\Top Sheet.SchDoc	Drawn By: JimBobJones	



Title	TestNumPad - Matrix	
Size: A3	Number	Revision: r1
Date: 11/07/2020	Sheet 2 of 4	
File: E:\Dropbox\..\Matrix.SchDoc	Drawn By: JimBobJones	



Title	TestNumPad - MCU	
Size: A3	Number	Revision: r1
Date: 11/07/2020	Sheet 3 of 4	
File: E:\Dropbox\..\MCU.SchDoc	Drawn By: JimBobJones	



Pin	Signal	Mating Sequence
A1	GND	First
A4	VBUS	First
A5	CC1	Second
A6	DP1	Second
A7	DN1	Second
A8	SBU1	Second
A9	VBUS	First
A12	GND	First
SHELL		GND

Pin	Signal	Mating Sequence
B12	GND	First
B9	VBUS	First
B8	SBU2	Second
B7	DN2	Second
B6	DP2	Second
B5	CC2	Second
B4	VBUS	First
B1	GND	First
SHELL		GND

[1]Notes:

- CC Pin Connection
  # Source - Pin A5 (CC) of the USB Type-C plug shall be connected to VBUS through a resistor Rp (56 kΩ ± 5%).
  # Sink - Pin A5 (CC) of the USB Type-C plug shall be connected to GND through a resistor Rp (5.1 kΩ ± 10%).

- Contacts B6 and B7 should not be present in the USB Type-C plug.

- All VBUS pins shall be connected together within the USB Type-C plug. Bypass capacitors are not required for the VBUS pins in this cable.

- All Ground return pins shall be connected together within the USB Type -C plug.

- Shield and GND grounds shall be connected within the USB Type -C and USB 2.0 Standard-A plugs on both ends of the cable assembly.

- All USB Type-C plug pins that are not listed in this table shall be open (not connected)

[1]USB Type-C Cable and Connector Specification, Release 2.0. August 2019, Section 3.5.2, Page 78

Title: TestNumPad - USB-C (TH)		
Size: A4	Number:	Revision: r1
Date: 11/07/2020		Sheet 4 of 4
File: E:\Dropbox\..\USB-C_(TH).SchDoc		Drawn By: JimBobJones