

## DESIGN , SIMULATION AND IMPLEMENTATION OF MOORE FSM

Use FSM to build an electronic combination lock with a reset button, two number buttons (0 and 1), and an unlock output. The combination should be 01011. To synthesize the design and implement using Altera FPGA.

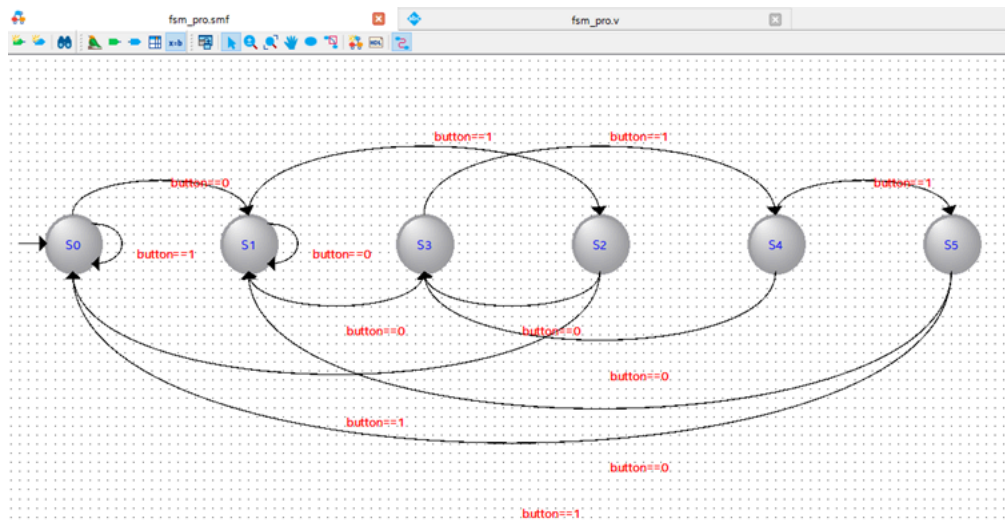
### REQUIREMENTS:

EDA Tool: Altera Quartus Prime v17

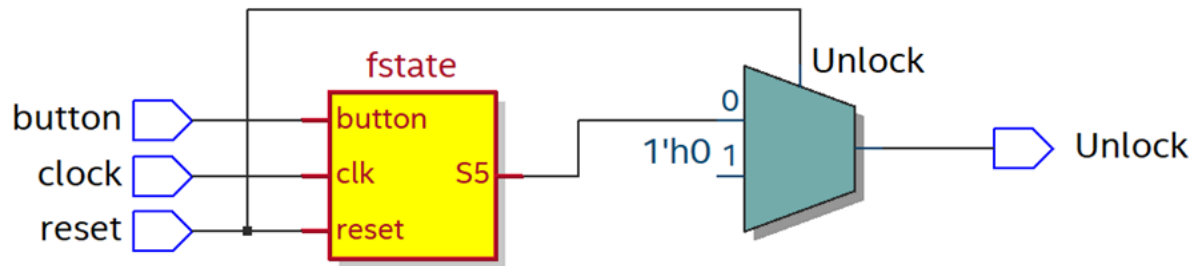
Hardware: DE1-SoC board

### STATE MACHINE FILE

fsm\_pro.smf



### RTL SCHEMATIC



## SIMULATION WAVEFORM

