1.1 Pin-assignements - BLE-cube circuit board (nRF52)

http://bit.ly/1XA3fwh

Name	Pin	Type / Function	Comment		
DEC1	1	Decoupling cap. 1	0V9 regulator digital supply decoupling.		
P0.00	2	Serial data 1	Shiftreg.1: serial data input.		
P0.01	3	Serial data 2	Shiftreg.2: serial data input.		
P0.02	4	Serial data 3	Shiftreg.3: serial data input.		
P0.03	5	RCLK	Shiftreg.1, 2, 3: storage register clock.		
P0.04	6	SRCLK	Shiftreg.1, 2, 3: shift register clock.		
P0.05	7	OE (inv.)	Shiftreg.1, 2, 3: Output Enable.		
P0.06	8	SRCLK (inv.)	Shiftreg.1, 2, 3: direct overriding clear.		
P0.07	9	Button 2	GPIO, used for button inputs.		
P0.08	10	Button 1	GPIO, used for button inputs.		
P0.09	11	Button 4	GPIO, used for button inputs.		
P0.10	12	Button 3	GPIO, used for button inputs.		
VDD	13	Power	Supply Voltage.		
P0.11	14	Button 6	GPIO, used for button inputs.		
P0.12	15	Button 5	GPIO, used for button inputs.		
P0.13	16	P0.13	GPIO.		
P0.14	17	P0.14	GPIO.		
P0.15	18	P0.15	GPIO.		
P0.16	19	P0.16	GPIO.		

P0.17	20	Accel. SDA/SDI/SDO	Internal accelerometer pin (GPIO if unused).	
P0.18	21	Debug pin 6: TRACECTL	Single Wire Output.	
P0.19	22	Accel. SCL/SPC	Internal accelerometer (GPIO if unused).	
P0.20	23	Accel. CS	Internal accelerometer (GPIO if unused).	
P0.21/RESET	24	Reset	Debug pin 10.	
SWDCLK	25	Debug pin 4	Serial Wire Debug clock input for debug and programming.	
SWDIO	26	Debug pin 2	Serial Wire Debug I/O for debug and programming.	
P0.22	27	Accel. SDO/SA0	Internal accelerometer pin (GPIO if unused).	
P0.23	28	Accel. INT1	Internal accelerometer pin: Interrupt signal 1 (GPIO if unused).	
P0.24	29	Accel. INT2	Internal accelerometer pin: Interrupt signal 2 (GPIO if unused).	
ANT	30	RF	Single-ended radio antenna connection.	
VSS	31	Power	Ground pin (Radio supply).	
DEC2	32	Decoupling cap. 2	1V3 regulator supply decoupling (Radio supply).	
DEC3	33	Decoupling cap. 3	Power supply decoupling.	
XC1	34	Analog Input	Connection for 32 MHz crystal.	
XC2	35	Analog Input	Connection for 32 MHz crystal.	
VDD	36	Power	Supply Voltage.	
P0.25	37	RumbleGate (G)	Gate on transistor U3.	
P0.26	38	TP3	Testpoint 3.	
P0.27	39	TP2	Testpoint 2.	
P0.28	40	Z-signal - (AIN4 - Analog input)	External accelerometer: Z-values.	
P0.29	41	Y-signal - (AIN5 - Analog input)	External accelerometer: Y-values.	

P0.30	42	X-signal - (AIN6 - Analog input)	External accelerometer: X-values.	
P0.31	43	TP1 (AIN7 - Analog input)	Testpoint 1.	
N.C	44		Not Connected.	
VSS	45	Power	Ground Pin.	
DEC4	46	Decoupling cap. 4	1V3 regulator supply decoupling. Input from DC/DC regulator. Output from 1.3 V LDO.	
DCC	47	Power	DC/DC regulator output pin.	
VDD	48	Power	Supply Voltage.	