

**SCHOOL OF ENGINEERING (SOE)**

**PROVISIONAL STATEMENT OF RESULTS**

**DEPARTMENT:** ELECTRICAL AND ELECTRONICS ENGINEERING

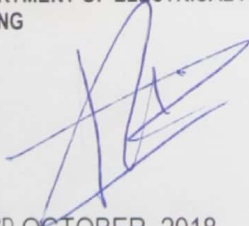
**PROGRAM:** ELECTRONICS AND TELECOMMUNICATION ENGINEERING



Name	Registration Number
<b>MUHAWENAYO GEDEON</b>	<b>216138272</b>

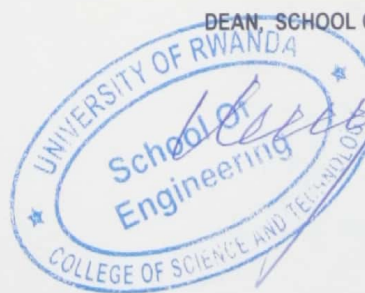
THIRD YEAR				2017 - 2018			
FIRST SEMESTER				SECOND SEMESTER			
MODULE CODE	CREDIT	MARKS	GRADE	MODULE CODE	CREDIT	MARKS	GRADE
EEE3161	10	82	A	ETE3264	10	76	B+
ETE3161	10	73	B+	ETE3261	10	87	A
EEE3162	10	77	B+	SOE3261	10	79	B+
ETE3164	10	98	A	EEE3261	10	83	A
ETE3165	10	82	A	ETE3263	10	72	B+
SOE3161	10	79	B+	ETE3262	10	75	B+
Total Credit Hours (level 3)				120			
Annual Average Marks (%)				80.09			
Failed Modules to RETAKE							
Remarks				PROGRESS			

HOD, DEPARTMENT OF ELECTRICAL AND ELECTRONICS  
ENGINEERING



Date: 23<sup>RD</sup> OCTOBER, 2018

DEAN, SCHOOL OF ENGINEERING



## REMARKS

1. Any alteration or overwriting makes this Statement of Result, invalid. Anyone coming across such a statement of result, is requested to bring it to the notice of The Principal, College of Science and Technology, University of Rwanda, BP 3900, Kigali, Rwanda, East Africa or E-mail to: [principal.cst@ur.ac.rw](mailto:principal.cst@ur.ac.rw) or [registrar.cst@ur.ac.rw](mailto:registrar.cst@ur.ac.rw)
2. Any clarification or query on this academic transcript should be addressed to the Principal.
3. Pass mark for all subjects is 50%;
4. Maximum mark for all subjects is 100%

Grading Scale

Marks	Letter	Grade Points
80-100	A (Excellent)	5
70-79	B+ (Very Good)	4
60-69	B (Good)	3
50-59	C (Pass)	2
Less than 50	F (Fail)	0

Degree classification scale

Marks	Classification
80-100%	First Class Honours
70-79.5%	Second class Honours-Upper division
60-69.5%	Second class Honours-Lower division
50-59.5%	Pass

## MODULES Description:

Module Code	Module Title	Module Code	Module Title
EEE3161	Power Electronics	ETE3264	Digital Signal Processing
ETE3161	Analog Filters	ETE3261	Image Processing
EEE3162	Advanced Control Engineering	SOE3261	Entrepreneurship Development
ETE3164	Digital Communications	EEE3261	Micro controller & embedded systems
ETE3165	Linear Integrated Circuits and Applications	ETE3263	Antenna and Propagation
SOE3161	Research Methodology	ETE3262	Telecommunication Networks

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**DEPARTMENT:** ELECTRICAL AND ELECTRONICS ENGINEERING  
**PROGRAM:** ELECTRONICS AND TELECOMMUNICATIONS ENGINEERING  
**CAMPUS:** HUYE



Name	Registration Number
MUHAWENAYO GEDEON	216138272

SECOND YEAR				2016 - 2017			
FIRST SEMESTER				SECOND SEMESTER			
MODULE CODE	CREDIT	MARKS	GRADE	MODULE CODE	CREDIT	MARKS	GRADE
EEE 2161	10	82	A	EEE2261	10	74	B+
ETE 2161	10	59	C	ETE2261	10	91	A
ETE 2162	10	79	B+	ETE2264	10	80	A
ELE 2162	10	84	A	ETE2265	10	82	A
COE 2161	10	78	B+	ELE2263	10	88	A
MAT2165	10	85	A	MAT2263	10	78	B+
				EAP2112	20	67	B
Total Credit Hours (level 2)				140			
Annual Average Marks (%)				78.1			
Failed Modules to RETAKE				-			
Remarks				PROGRESS			

HEAD, DEPARTMENT OF ELECTRICAL AND ELECTRONICS  
ENGINEERING



Date: 12<sup>TH</sup> APRIL, 2018

DEAN, SCHOOL OF ENGINEERING





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Less than 50	F (Fail)	0

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80-100%	First Class Honours
70-79.5%	Second class Honours-Upper division
60-69.5%	Second class Honours-Lower division
50-59.5%	Pass

## MODULES Description:

Module Code	Module Title	Module Code	Module Title
MAT2165	Mathematics for Engineers III	MAT2263	Mathematics for Engineers IV
ELE2162	Fluid Mechanics & Thermodynamics	ETE2261	Digital Electronic Circuits
COE2161	Advanced Computer Programming	EEE2261	Control Systems
ETE2162	Signals & Systems	ELE2263	Electrical Machines
EEE2161	Measurements & Instrumentations	ETE2264	Microprocessor and Its Applications
ETE2161	Engineering Electromagnetics	ETE2265	Analogue Communication Systems
EAP2112	English for Academic Purposes		

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PROGRAM: ELECTRONICS & TELECOMMUNICATIONS ENGINEERING  
CAMPUS: HUYE



Name	Registration Number
MUHAWENAYO GEDEON	216138272

FIRST YEAR				2015 - 2016			
FIRST SEMESTER				SECOND SEMESTER			
MODULE CODE	CREDIT	MARKS	GRADE	MODULE CODE	CREDIT	MARKS	GRADE
PHY1161	10	75	B+	PHY1261	10	69	B
MAT1161	10	69	B	CST1261	10	72	B+
SST1161	10	81	A	EEE1261	10	89	A
ETE1161	10	83	A	MAT1261	10	84	A
TWP1161	00	90	A	ETE1261	10	72	B+
ETE1162	10	76	B+	MEE1262	10	79	B+
COE1161	10	74	B+	EST1161	00	73	B+
EST1161	00	77	B+	TWP1261	00	80	A
Total Credit Hours (level 1)				120			
Annual Average Marks (%)				76.83			
Failed Modules to RETAKE				-			
Remarks				PROGRESS			

HEAD, DEPARTMENT OF ELECTRICAL AND ELECTRONICS  
ENGINEERING



Date: 12<sup>TH</sup> APRIL, 2018

for DEAN, SCHOOL OF ENGINEERING



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80-100%	First Class Honours
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50-59.5%	Pass

## MODULES Description:

Module Code	Module Title	Module Code	Module Title
SST 1161	Study Skills	EST 1261	English For Science And Technology Ii
MAT 1161	Mathematics For Engineers I	MAT 1261	Mathematics For Engineers II
ETE 1162	Analogue Electronic Circuits	PHY 1261	Physics For Engineers Ii
PHY 1161	Physics For Engineers I	CST 1261	Computer Programming
TWP 1161	Workshop Technology I	MEE 1262	Engineering Drawing And CAD
COE 1161	Ict Skills	ETE 1261	Analogue Electronic Circuits II
ETE 1161	Electrical Circuit Analysis	EEE 1261	Introduction To Matlab Programming
EST1161	English For Science & Technology	TWP 1261	Workshop Technology II