



** all pipeline values with no incoming arrows simply acquire their values from the previous pipeline

s_1: rst1 := s_3 || s_4 || id/ex.opc == jsr/jsrr || id/ex.opc == jmp/ret
s_2: load := ~stall1 && ~stall2
s_3: wrong_taken := id/ex.opc == br && !br_en && id/ex_pred && id/ex_dest != 3'b000
s_4: wrong_not_taken := id/ex.opc == br && br_en && !id/ex_pred
s_5: single_branch := id/ex.opc == op_br && !global_stall && id/ex_dest != 3'b000
s_6: branch_taken := single_branch && br_en
s_7: rst2 := from D_C, when opc = trap

a: sr1mux_sel **h:** cc_out **o:** wbdamux_sel
b: sr2mux_sel **i:** jsrmux_sel **p:** counter_val_out
c: adjmux_sel **j:** aluop **q:** load_cc
d: regAmux_sel **k:** marmux_sel **r:** destmux_out
e: immmux_sel **l:** exme_mar **s:** mewb_cw.load_dst
f: aluBmux_sel **m:** wbdamux_out **t:** wbdamux_out
g: aluBmux_out **n:** mewb_wbdata **u:** ** look up fwd_in signals **