

"server"

①



Module interface

②

+



testbench code

+

student code

=

Instructor  
script +  
Gradio

③

⑥

verilog &  
other tools

Google



Professor

Gradio "clients"

I

O

ID:

module add( ... );

Complete  
a Verilog Code

④

⑤

submit

students

$x_0$	$x_1$	$\overline{x_1}$	$x_0 \& \overline{x_1}$
0	0	1	0
0	1	0	0
1	0	1	1
1	1	0	0

⑦

