BLIZZARD FINNEGAN

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Seeking co-op in firmware and FPGA use. Available May - August 2022

EDUCATION

Rochester Institute of Technology

Aug. 2018 - Aug. 2024

Bachelor of Science Computer Engineering Technology; GPA:3.3/4.0

SKILLS

Software: Autodesk AutoCAD, ModelSim, MultiSim, Altera Quartus, LaTeX, Markdown, Linux,

Microsoft Excel, MATLAB, Git, QUCS, Microsoft Access

Languages: C++, VHDL, BASH/ZSH, NGSpice/PSpice, Python

Hardware: Oscilloscope, Soldering iron, Digital multimeter, FPGAs, Signal/function generator,

Scanning laser microscope, Scanning electron microscope

PROJECTS

Arch Linux Livability Scripts

Apr. 2020 - Current

ZSH/BASH scripts built to improve stability of Arch Linux

- Update Restriction Script
- Arch Linux Install Automation Script
- Git Update Automation Script

Encryption Key Generator

Mar. 2020

VHDL design to create and transmit an encryption key, given an 8-bit value.

- Using Altera Cyclone V FPGA
- Extensive use of Quartus and Modelsim

Gesture-controlled Motor System

May 2019

C designed program to control a motor, given a gyroscopic input.

- · Using the ATMEL328 Arduino platform
- Integral use of I2C protocols

ADC and DAC Simulation

May 2019

Build Analog-Digital-Converter and Digital-Analog-Converter using BJTs, ADC ICs, and DAC ICs.

- BJTs characterized in lab before simulation
- Simulations performed in Multisim
- Circuits were then built and tested in lab

EMPLOYMENT

MACOM Technology Solutions

Laboratory Technician (pre-college summer job), Horseheads, NY

June 2017 - Aug. 2018

- Operated scanning laser microscope measurement system to assess failures and improve yield
- Compiled acquired data, and generated visualizations using Excel for ease of understanding
- Facilitated the writing of the standard operating procedure for the scanning laser microscope

Texas Roadhouse

Dish Machine Operator; Cold Preparation, Henrietta, NY

Feb. 2021 - Aug. 2021

- Followed all standard operating procedures for preparation and labeling of food
- Managed inventory according to First In, First Out procedures