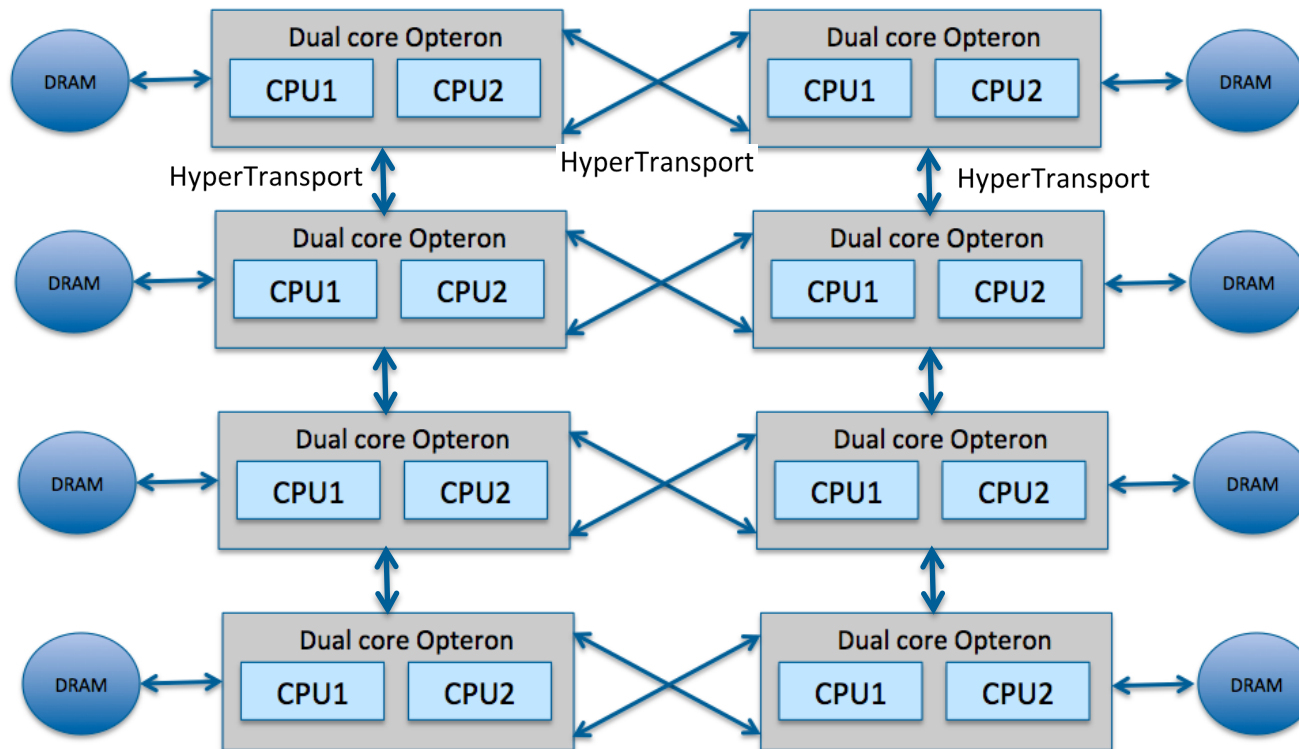
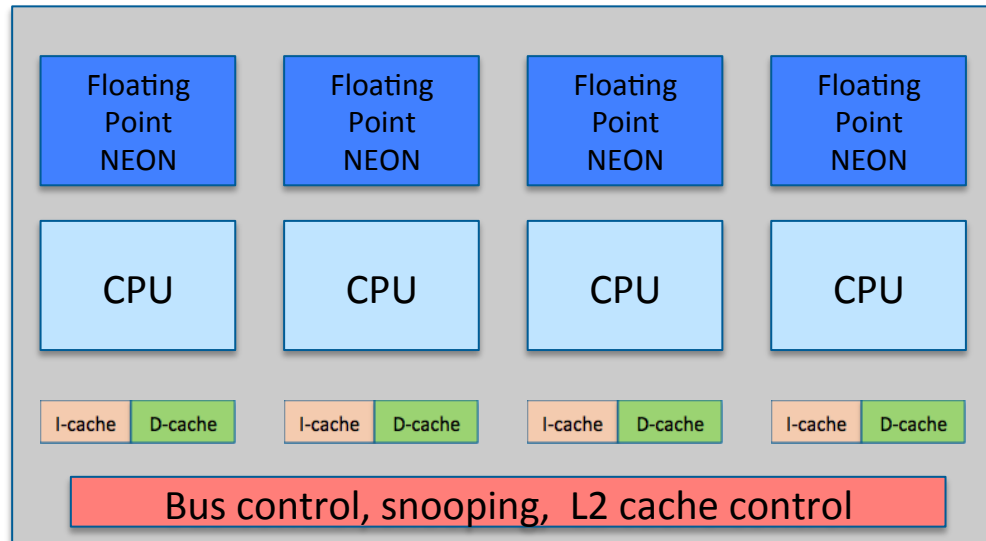


- Introduced in 2008 with the Core i7
 - The QPI interface transfers data on both the rising and falling edge of the clock (at 3.2 GHz) for a bandwidth of 25.6GB/s.



Has 8 dual-core processors linked via HyperTransport

- A NUMA system with a high speed cross-linked network

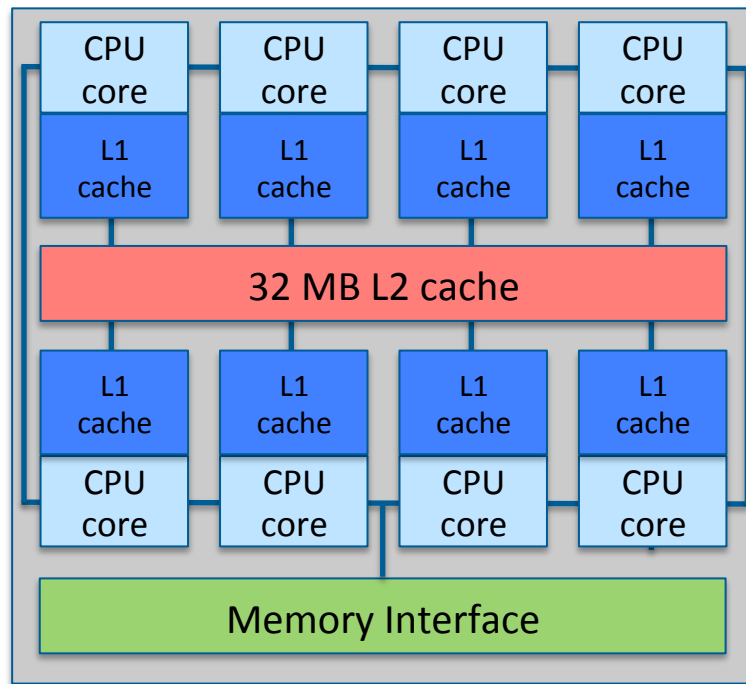


A general-purpose processor intended for use in mobile devices

Available with 1, 2, 3, or 4 superscalar cores

Can directly execute Java bytecode

No L2 cache, but includes on chip bus snooping control



Eight cores, can be turned off individually to save energy

Cores contain 2 integer ALUs & 2 floating point units

Also includes a decimal floating-point (DFP) unit for financial applications

Hardware DFP is 100 to 1000 times faster than software decimal arithmetic