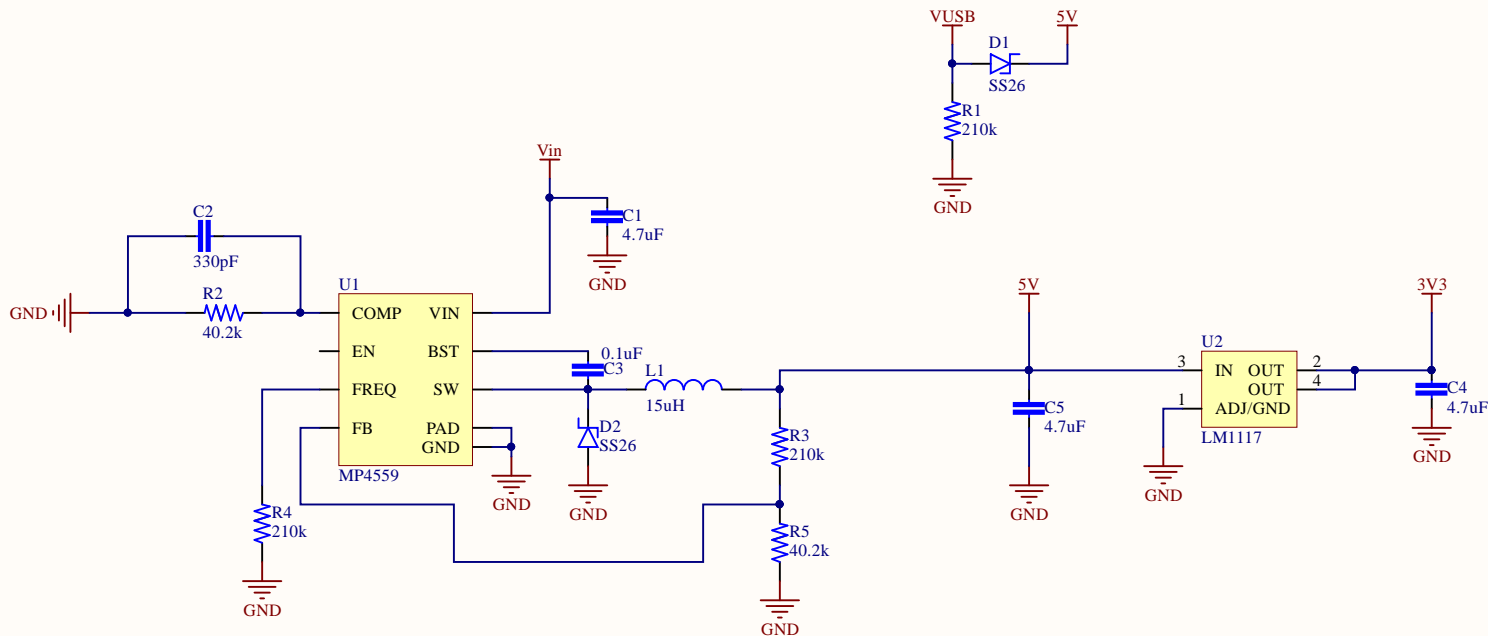


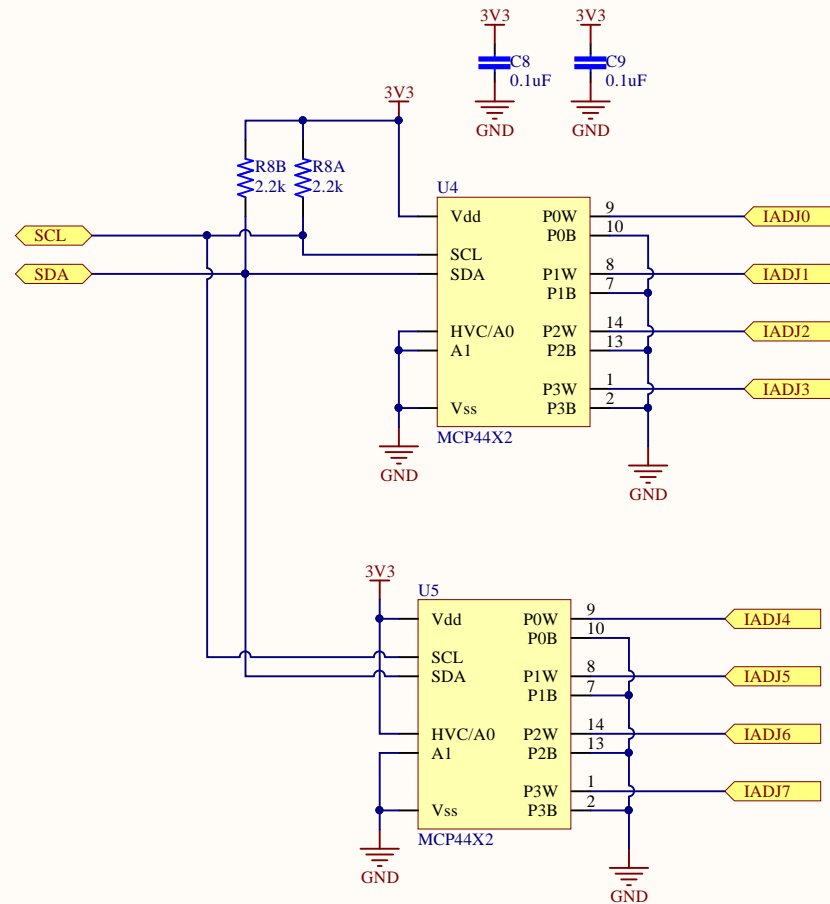
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Date: 3/4/2019	Time: 7:34:57 AM	Sheet* of *
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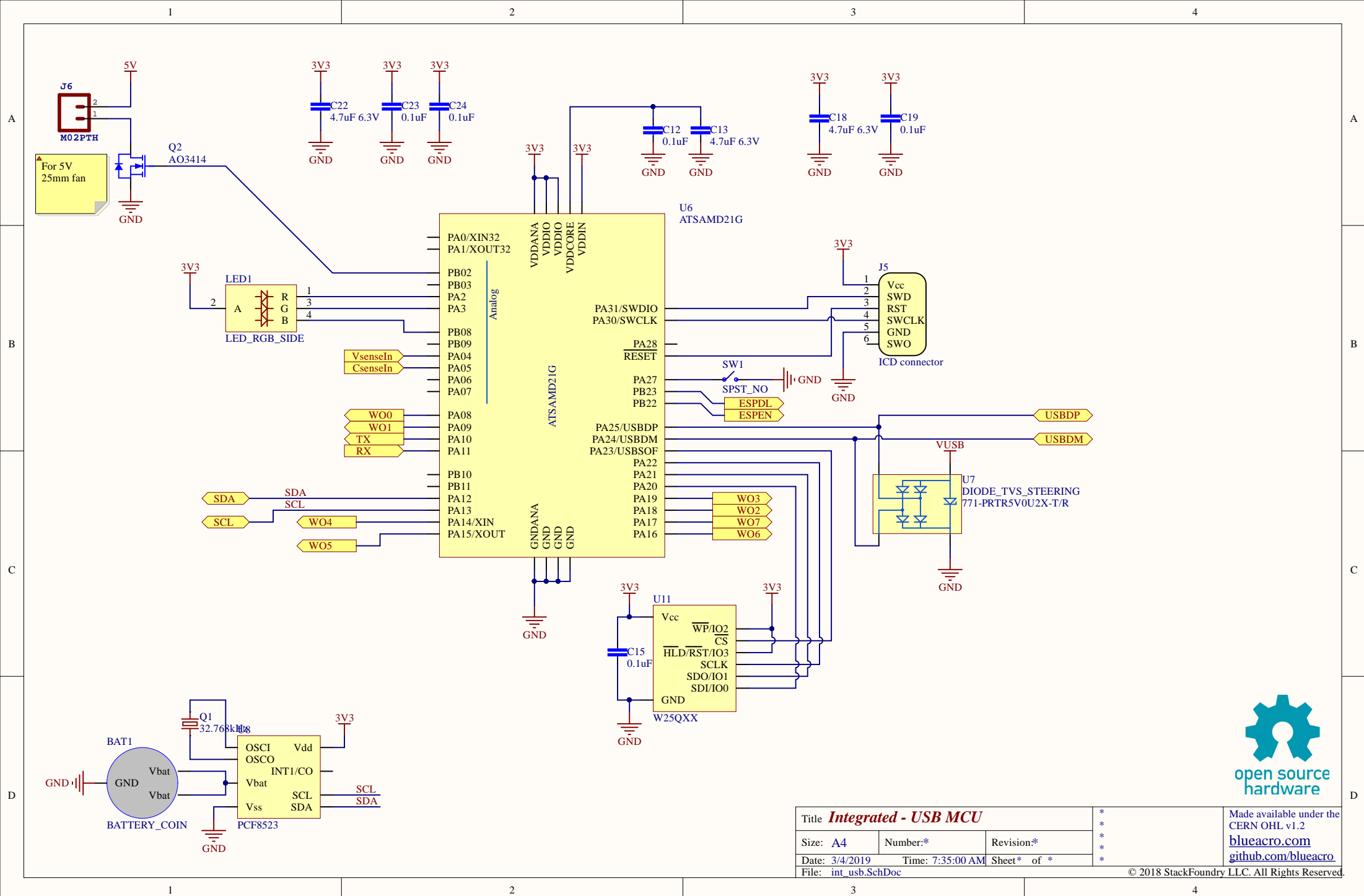


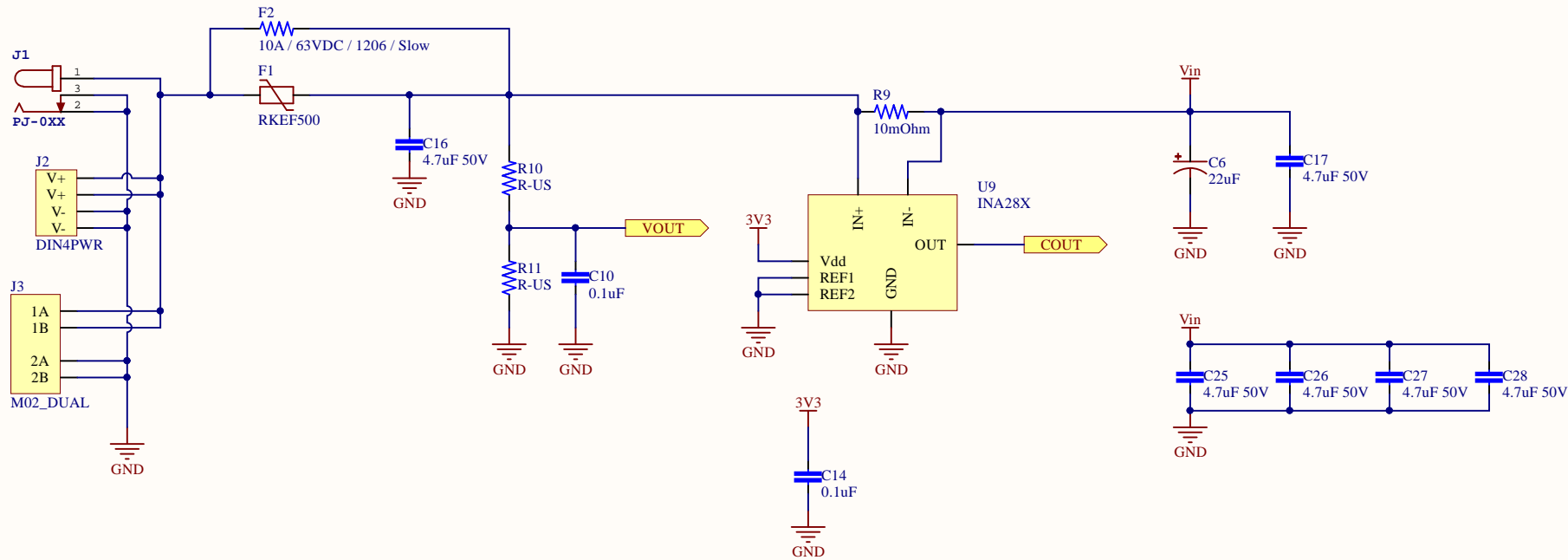
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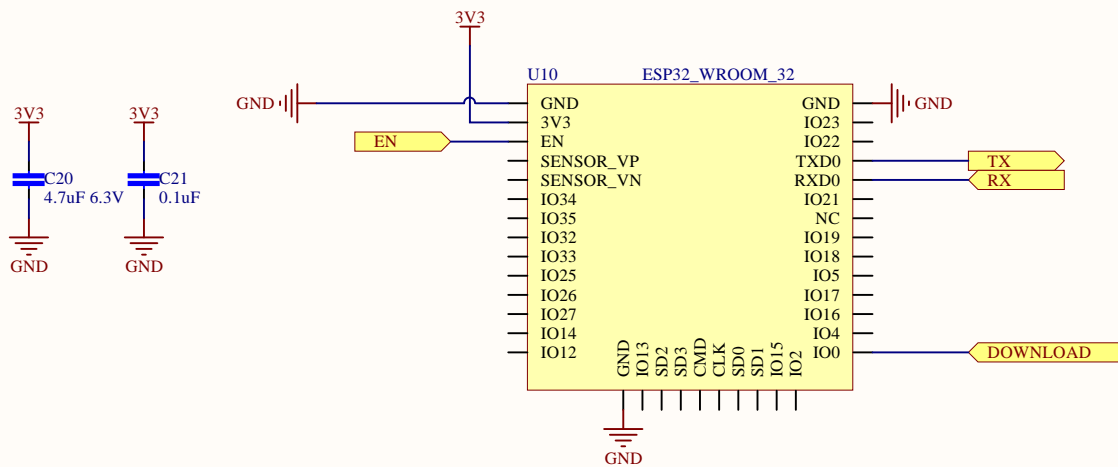
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Size: A4	Number:*	Revision*		
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File: int_logic.SchDoc				
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Title <i>Integrated - Power Input / Monitor</i>			* * * *	Made available under the CERN OHL v1.2 blueacro.com github.com/blueacro
Size: A4	Number:*	Revision:*		
Date: 3/4/2019	Time: 7:35:01 AM	Sheet* of *		
File: int_pwrin.SchDoc				

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Title <i>Integrated - EPS32 Module</i>			* * * *	Made available under the CERN OHL v1.2 blueacro.com github.com/blueacro
Size: A4	Number:*	Revision:*		
Date: 3/4/2019	Time: 7:35:01 AM	Sheet* of *		
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