Name: Muhammad Sherjeel Akhtar

Roll No: 20P-0101

Section: 2D1

DLD LAB TASK: 2

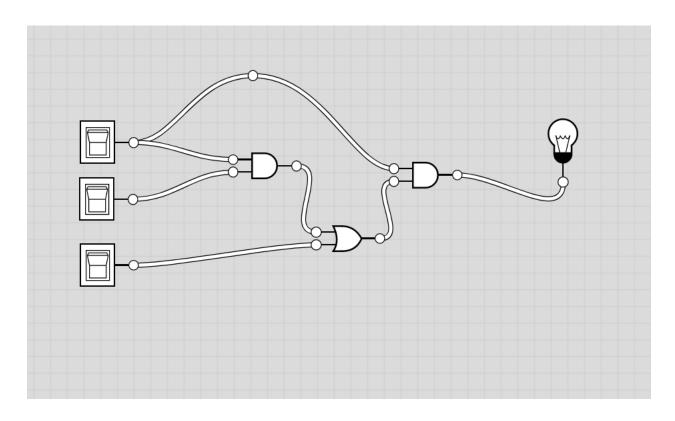
Submitted To: Maam Anam

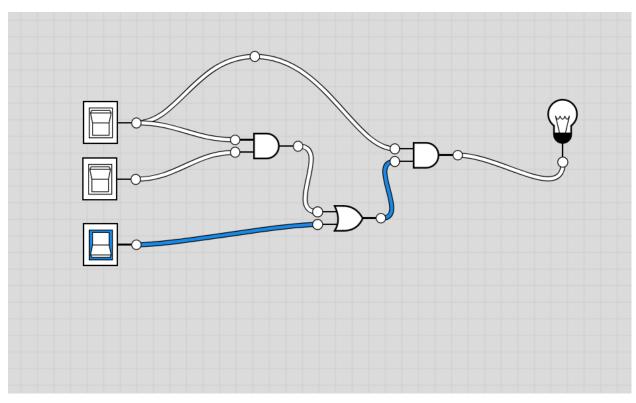
Question: 1

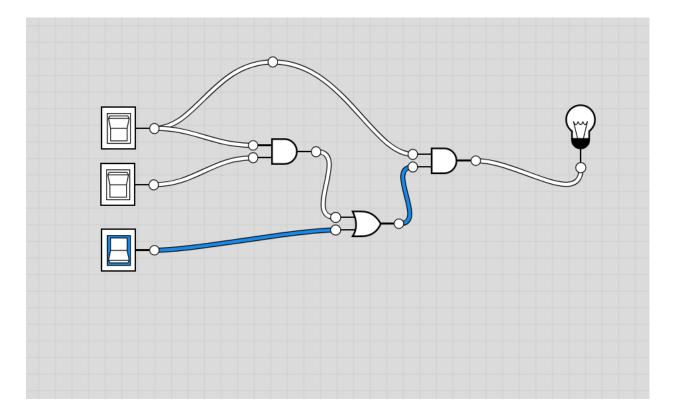
Part 1:

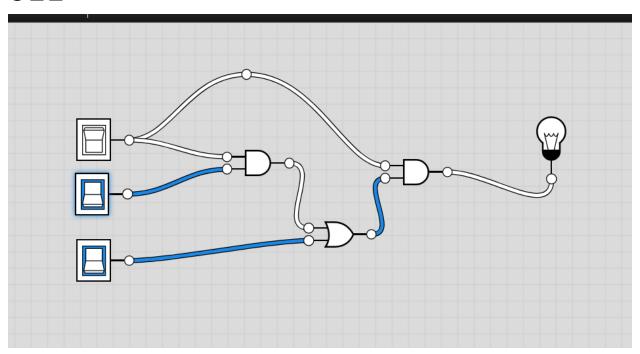
Output Expression: A. ((A.B) +C)

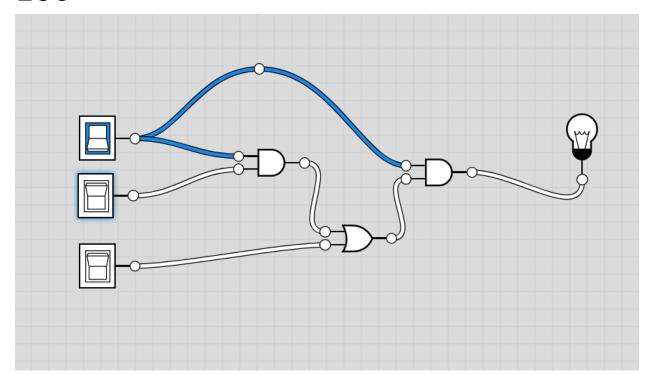
Α	В	С	R
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

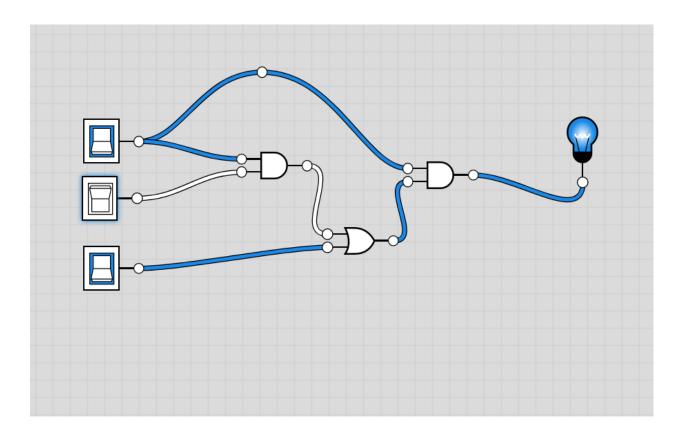


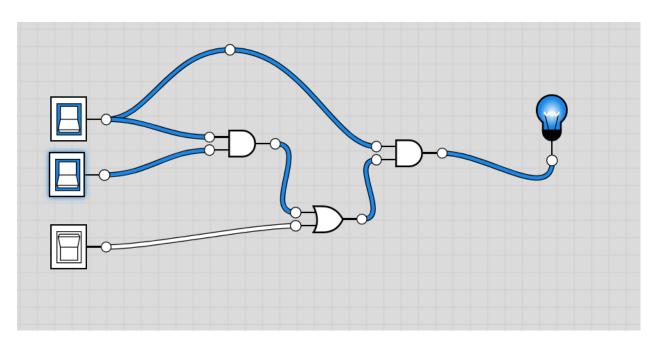


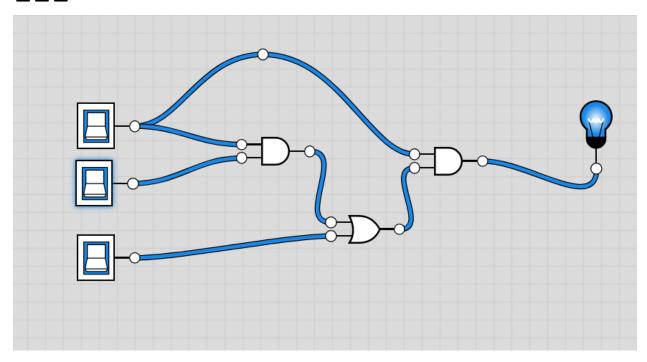










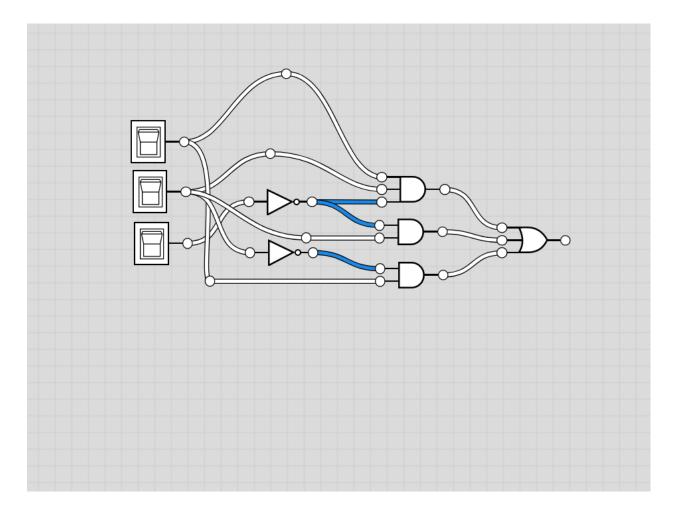


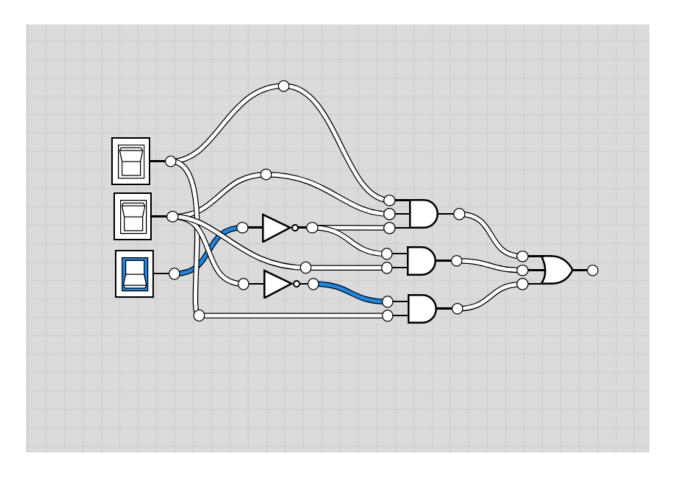
Part 2:

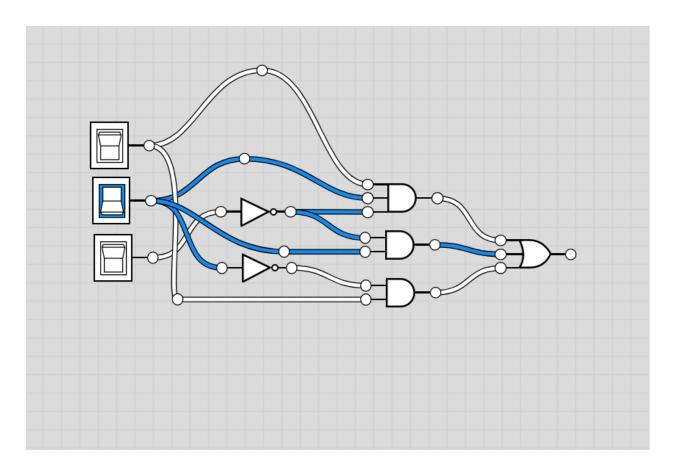
Expression: (A.B.C')+(B.C')+(A.B')

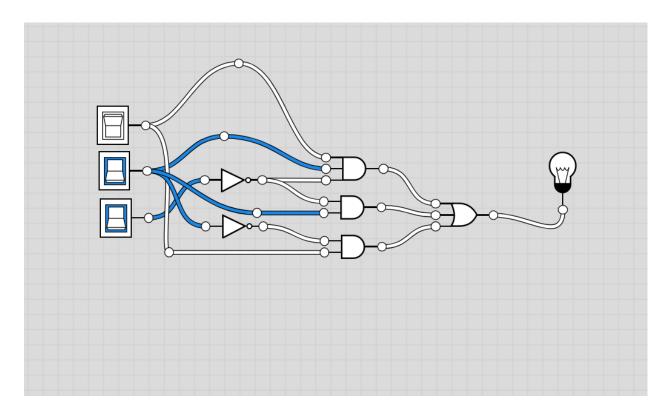
Α	В	С	R
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1

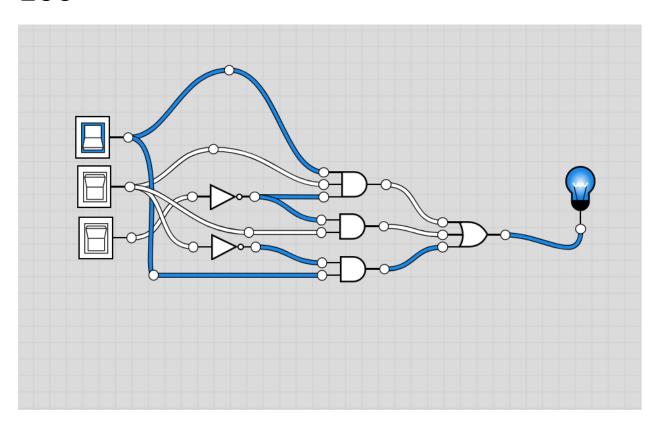
1	1	0	1
1	1	1	0

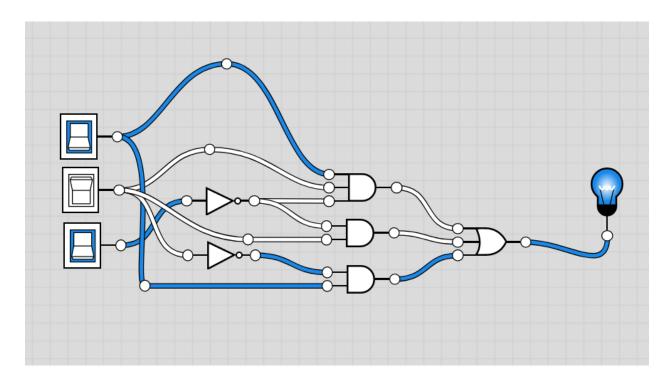


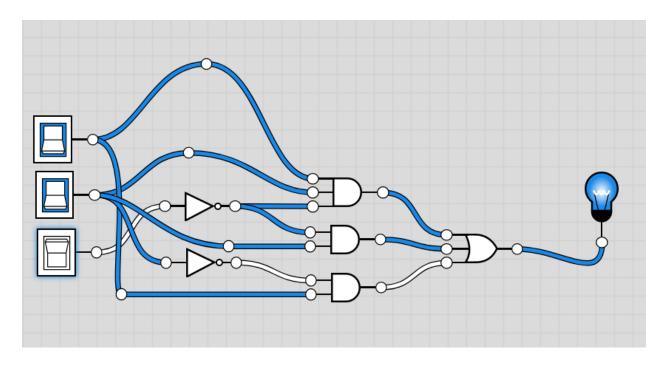


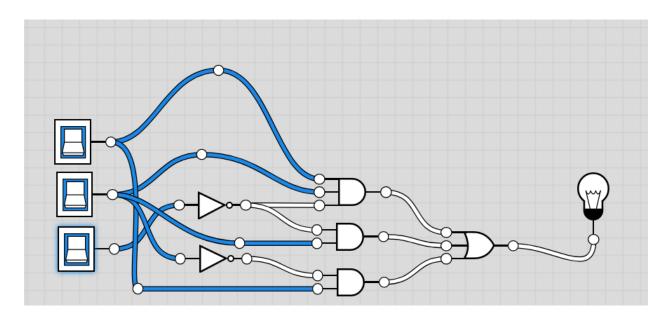












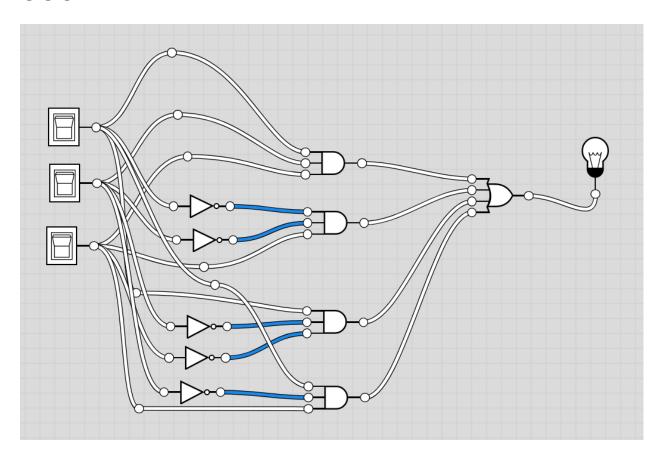
QUESTION 1 COMPLETED

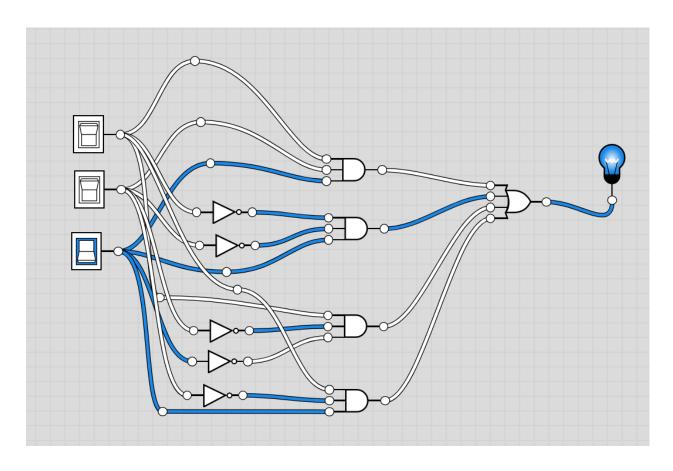
QUESTION: 2

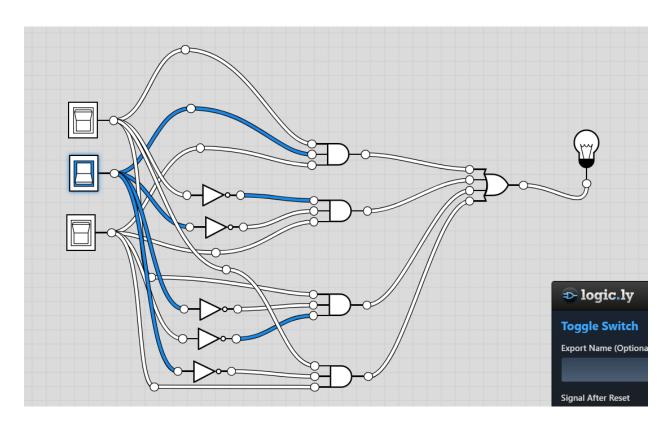
PART 1:

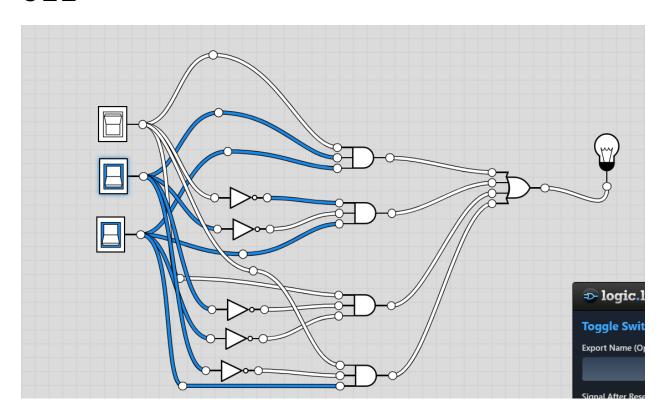
CIRCUIT DIAGRAM:

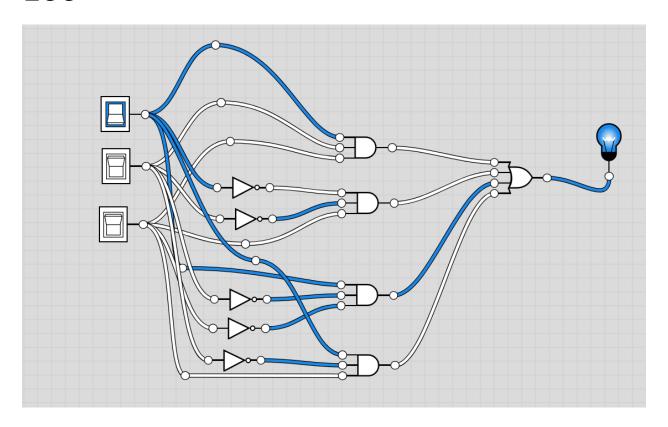
000

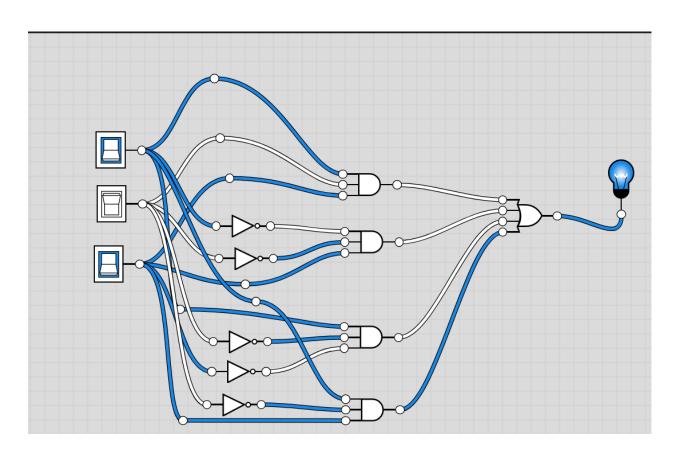


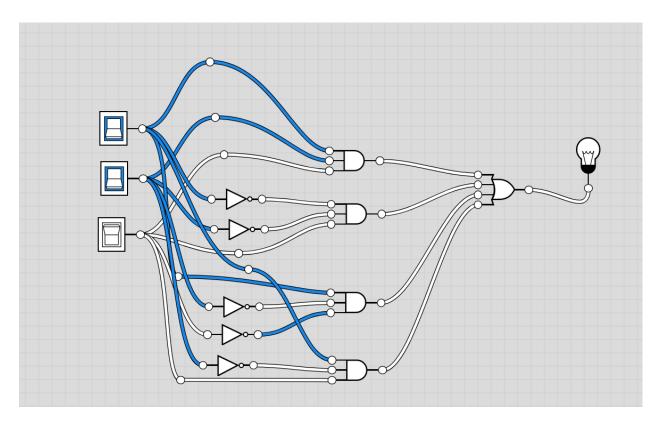


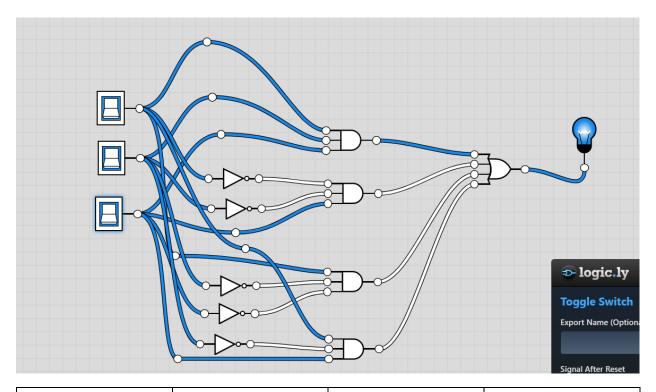










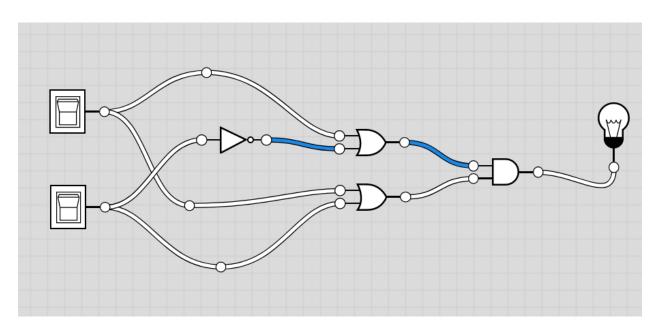


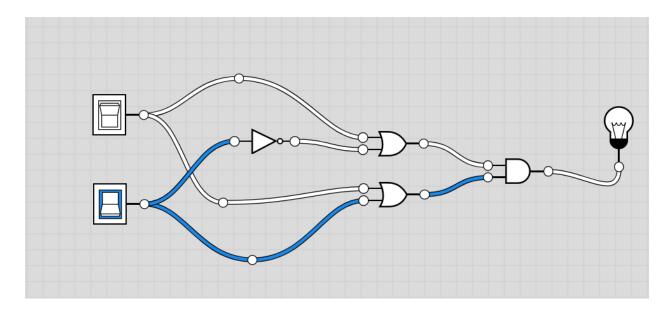
Α	В	С	R
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	1

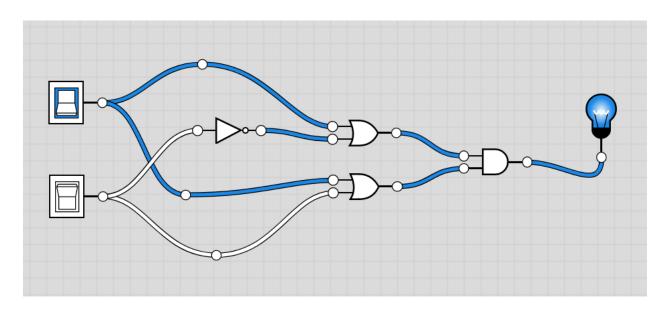
Part 2:

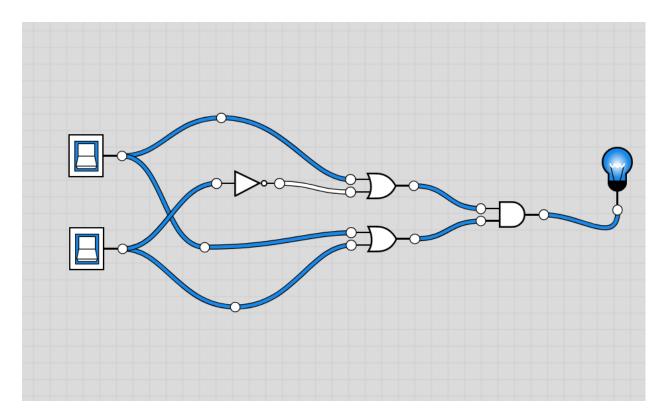
Circuit Diagram:

00









Truth Table:

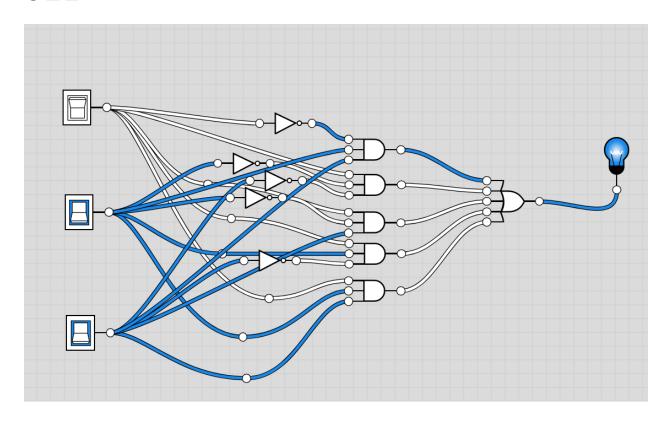
Α	В	R
0	0	0
0	1	0
1	0	1
1	1	1

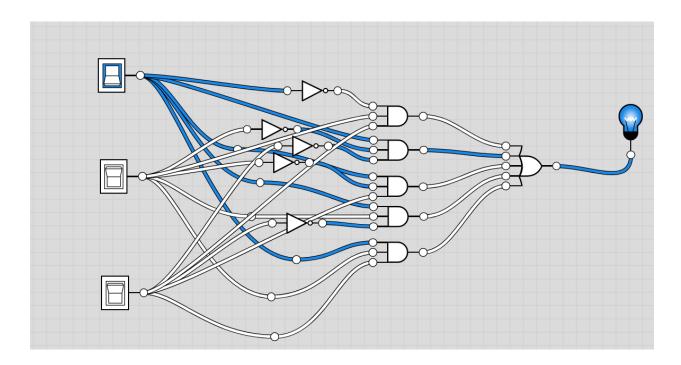
Question 2 Completed

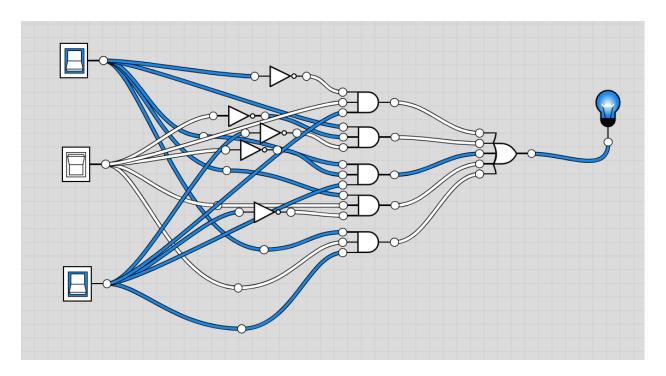
Question 3:

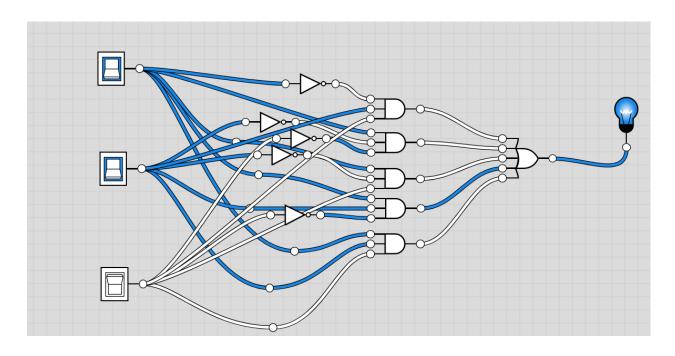
Expression Using SOP:

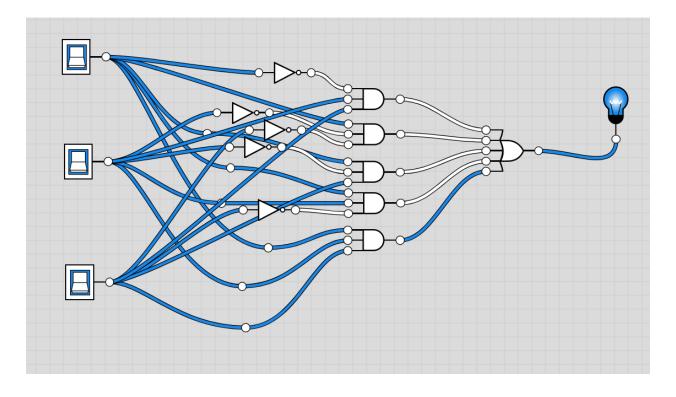
A'BC+AB'C'+AB'C+ABC'+ABC 011





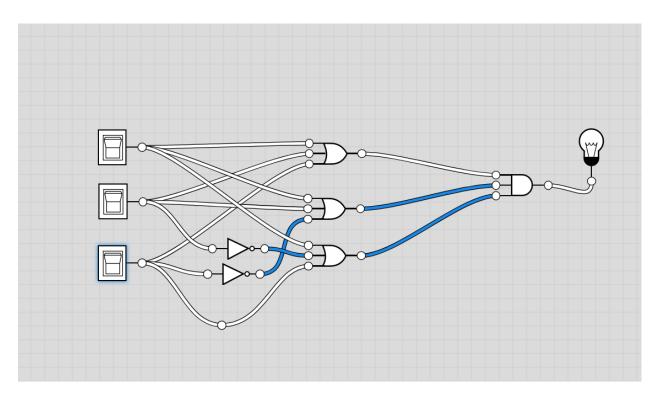


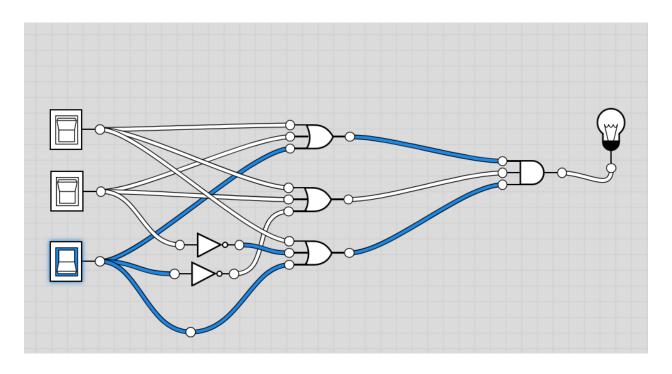


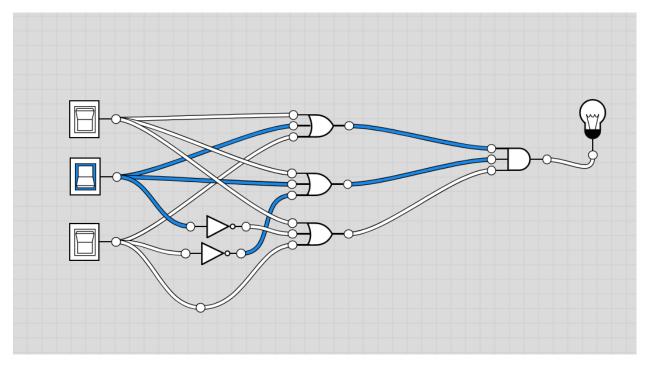


EXPRESSION OF POS:

(A+B+C). (A+B+C'). (A+B'+C)







QUESTION 3 COMPLETED

Question:1