

Name: Muhammad Sherjeel Akhtar

Roll NO: 20P-0101

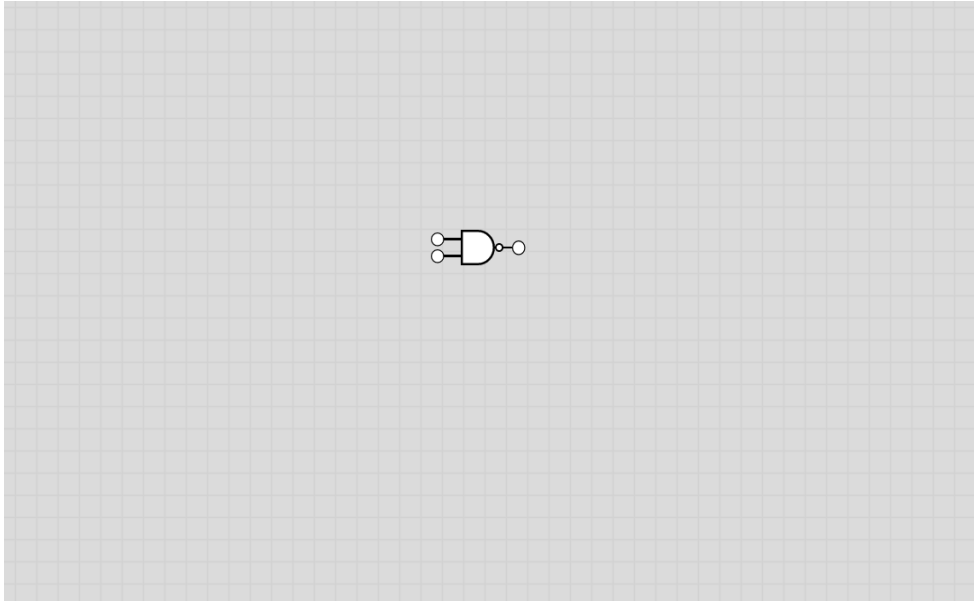
Subject: DLD LAB

Date: 17th March, 2020

Submitted To: Maam Anam

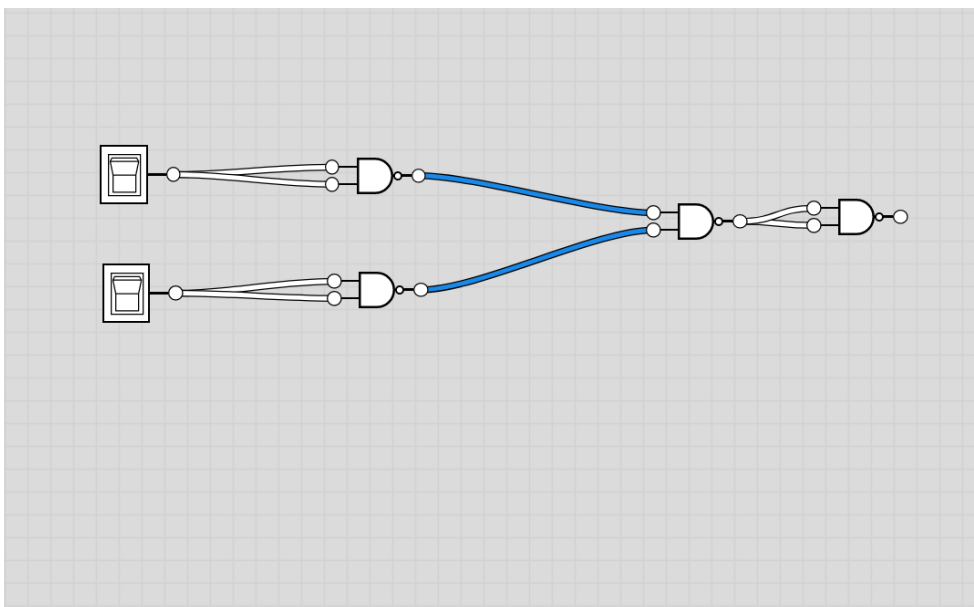
NAND Gate Only:

NAND:



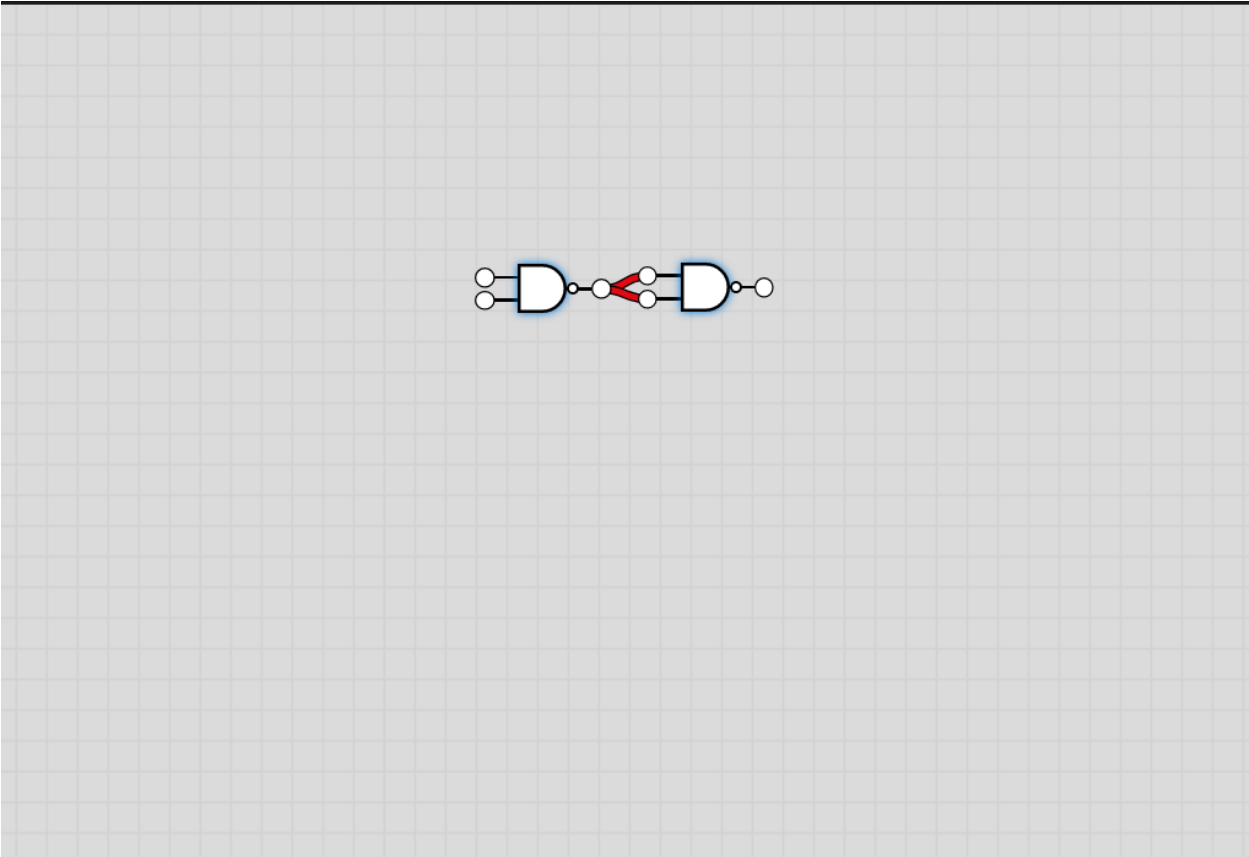
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0	1	1
1	0	1
1	1	0

NOR:



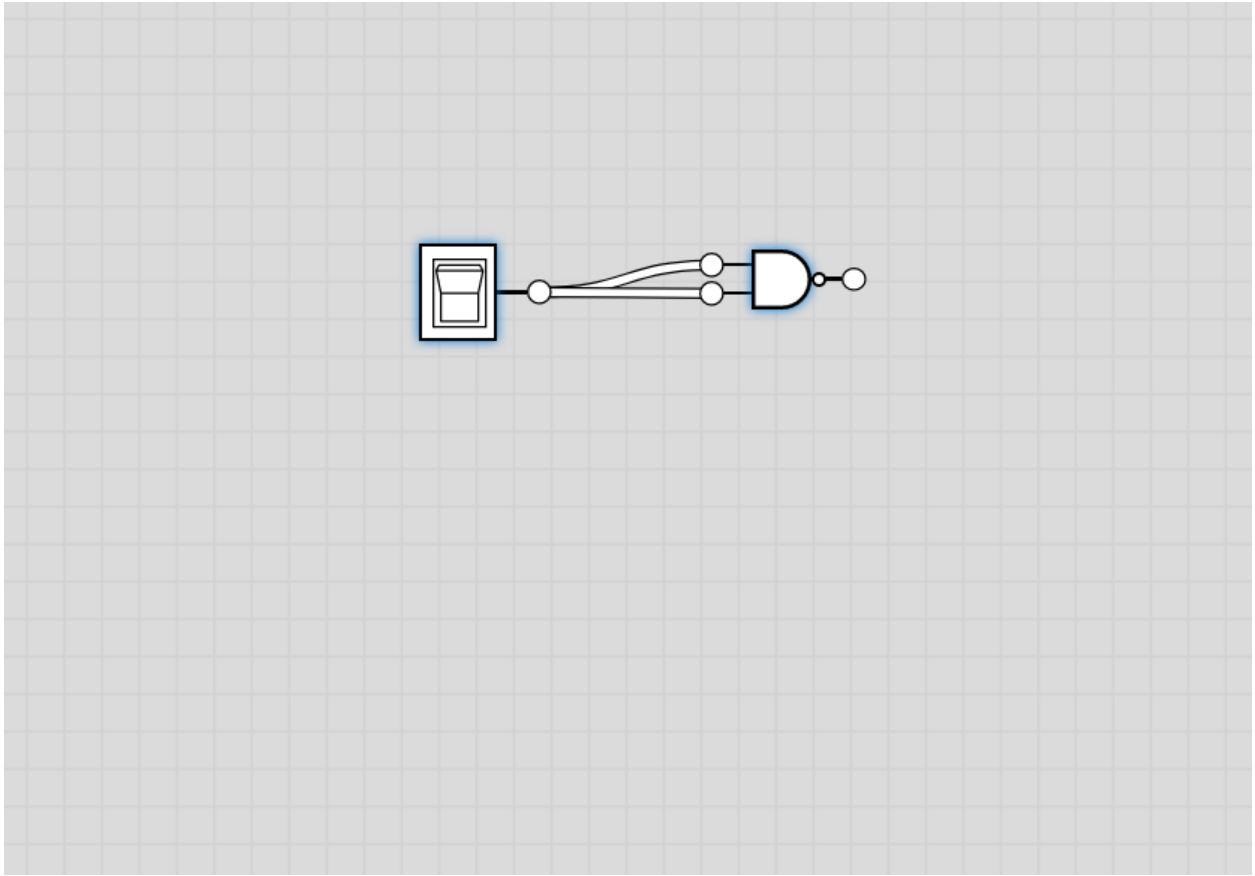
0	0	1
0	1	0
1	0	0
1	1	0

AND Gate:



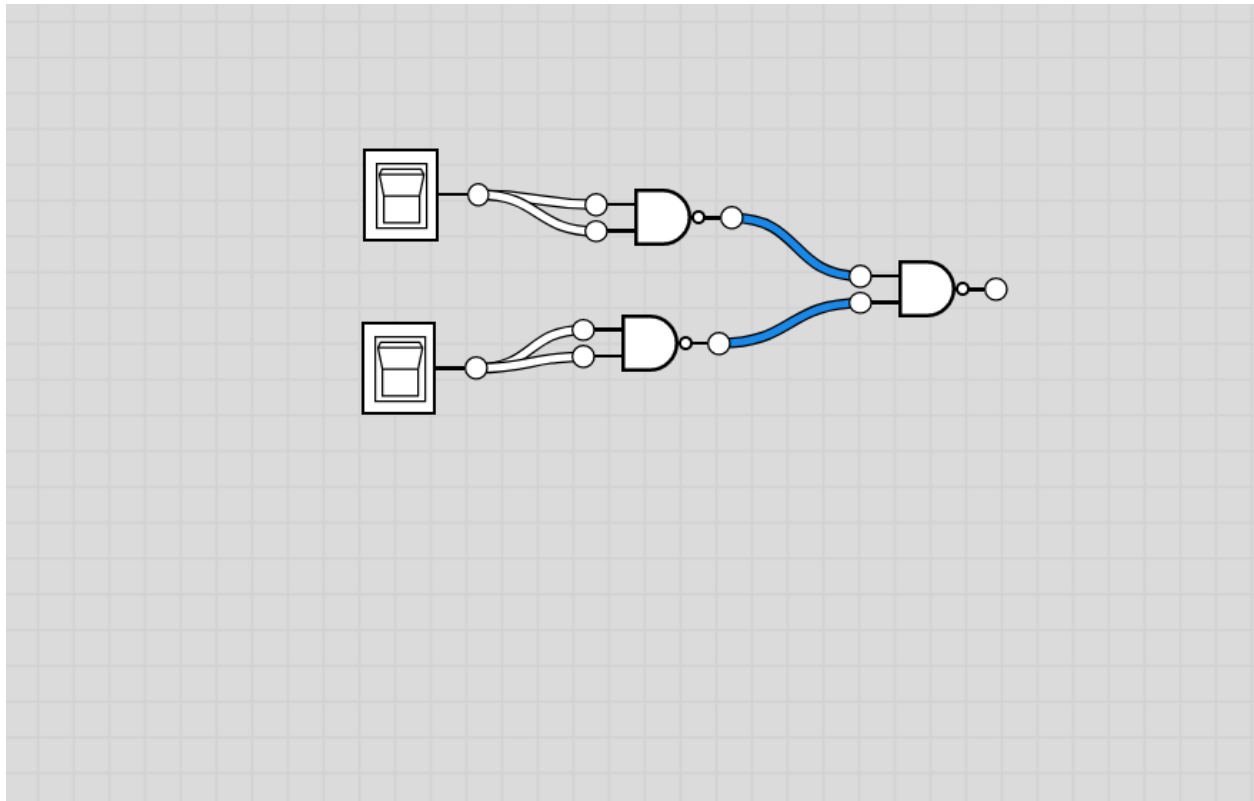
0	0	0
0	1	0
1	0	0
1	1	1

NOT Gate:



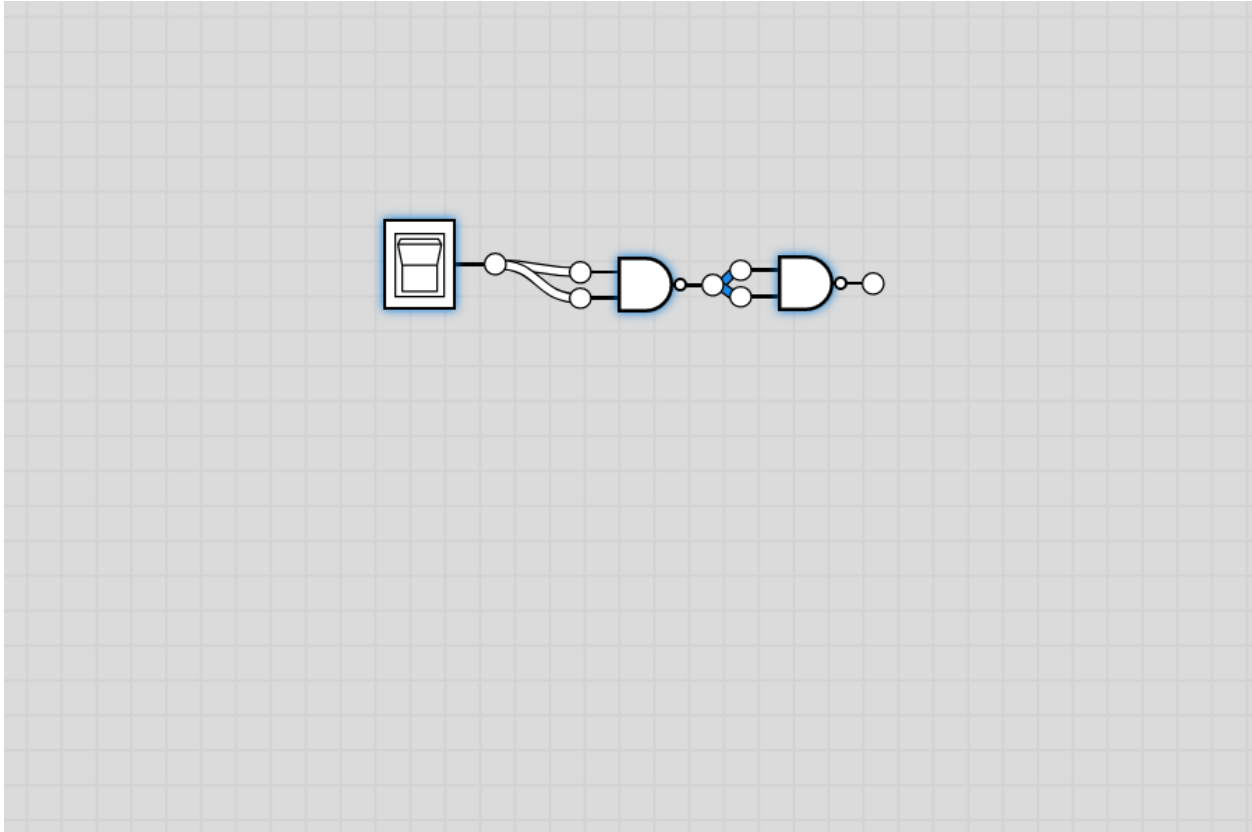
0	1
1	0

OR Gate:



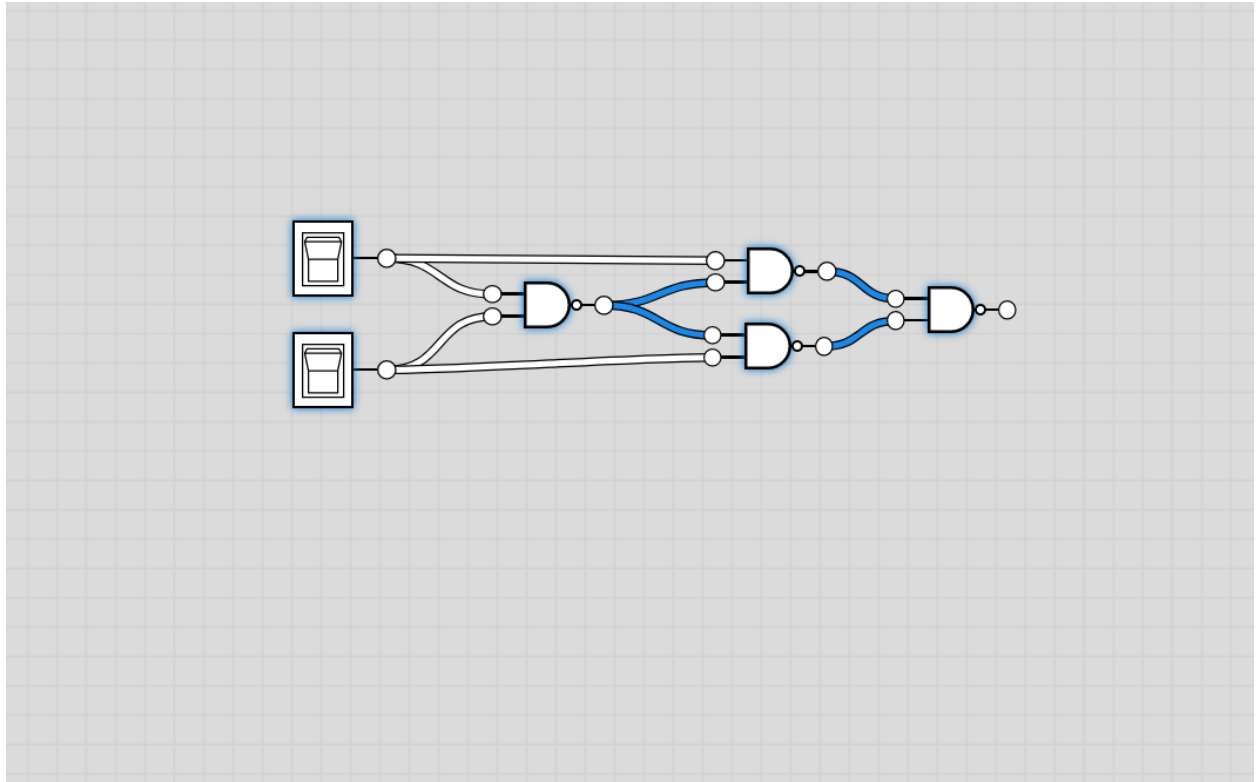
0	0	0
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1	0	1
1	1	1

BUFFER:



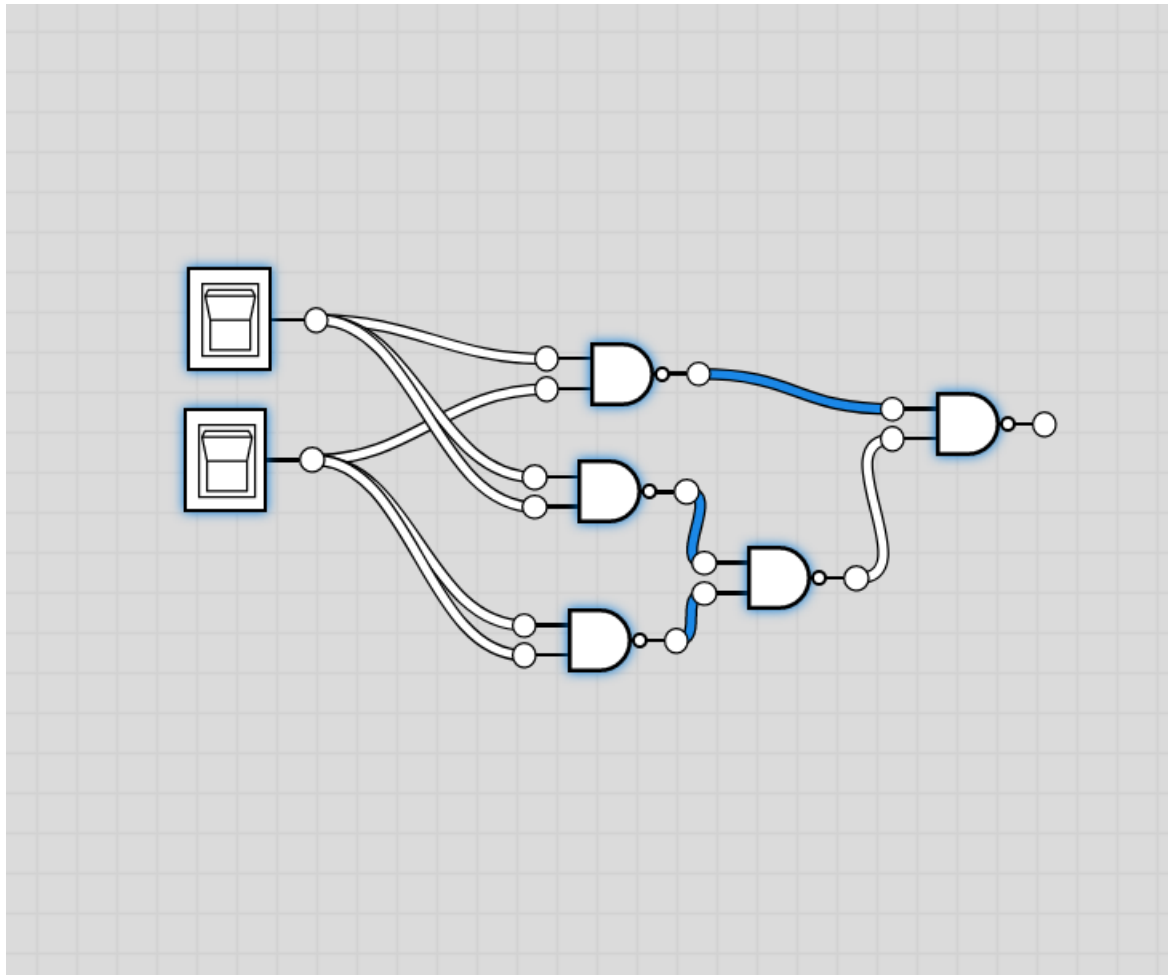
0	0
1	1

Exclusive-OR:



0	0	0
0	1	1
1	0	1
1	1	0

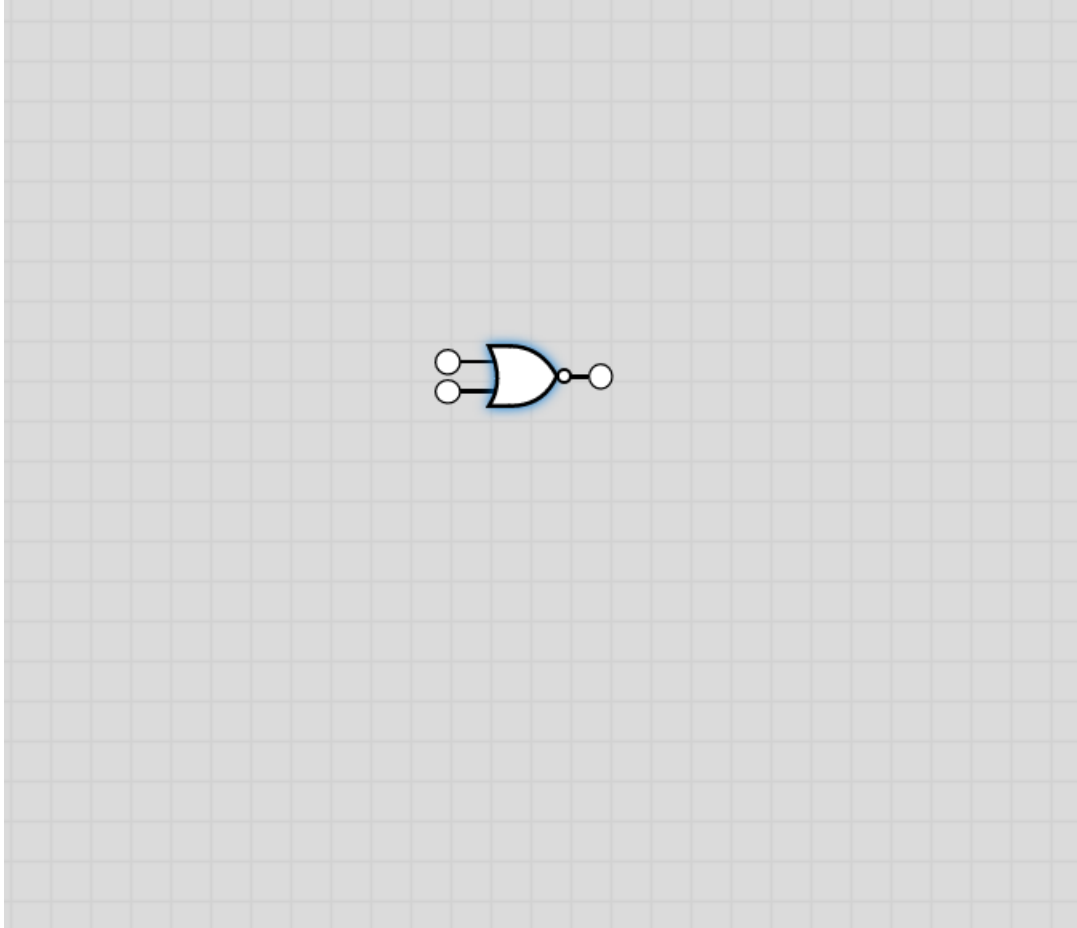
Exclusive-NOR:



0	0	1
0	1	0
1	0	0
1	1	1

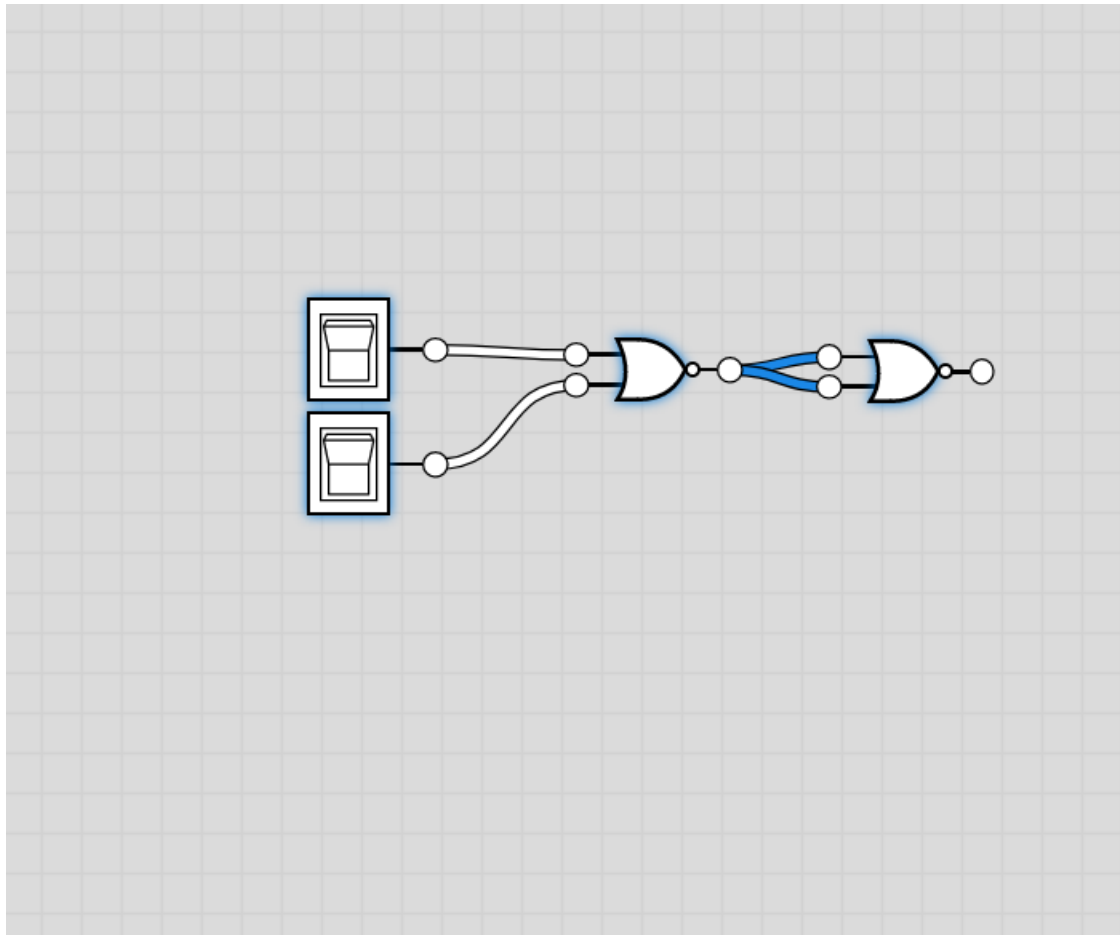
Using NOR Gate Only:

NOR Gate:



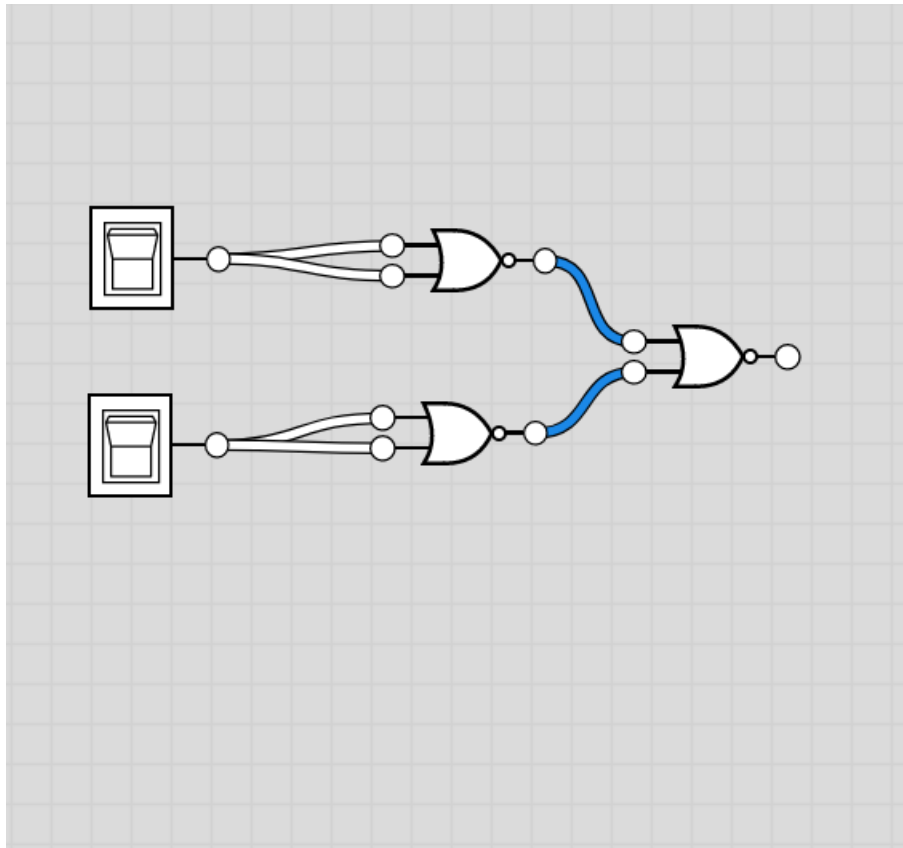
0	0	1
0	1	0
1	0	0
1	1	0

OR Gate:



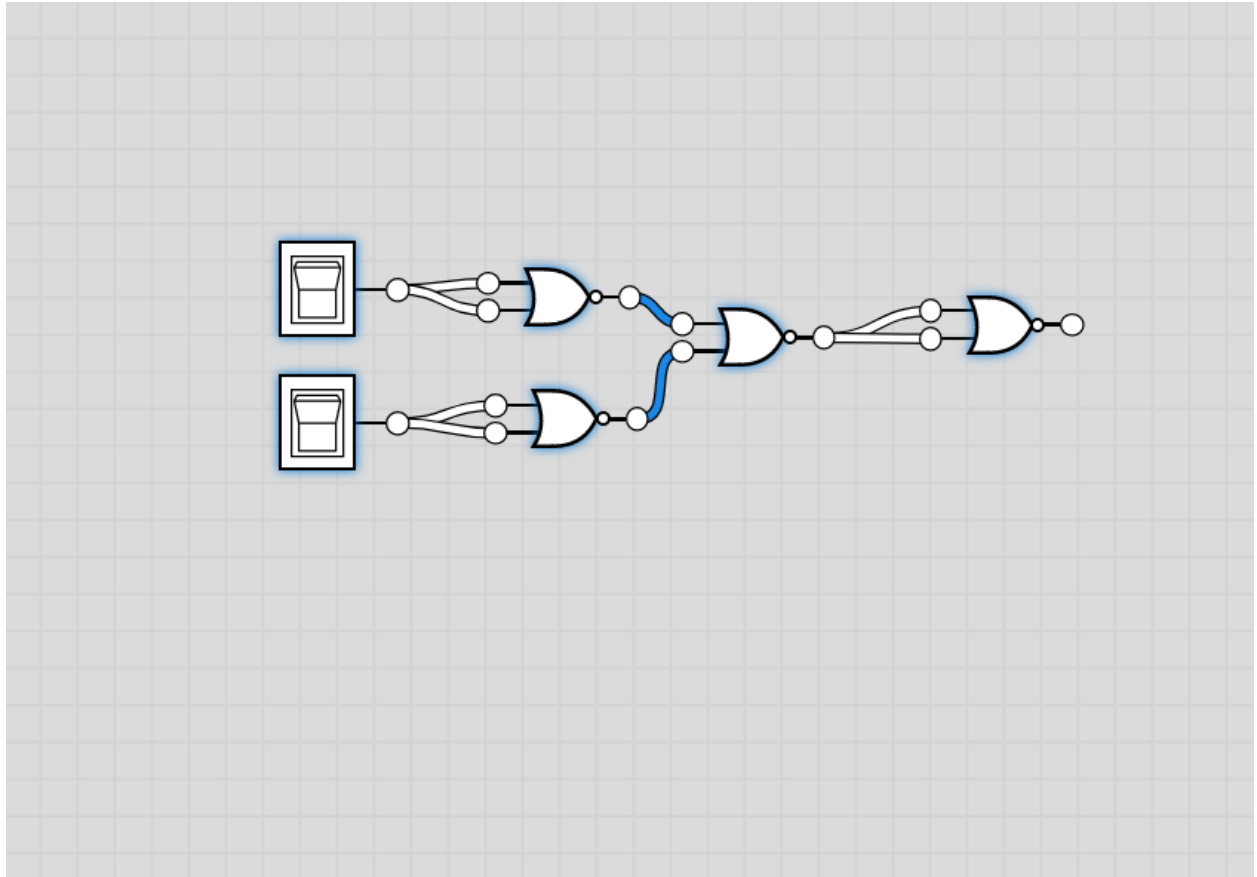
0	0	0
0	1	1
1	0	1
1	1	1

AND Gate:



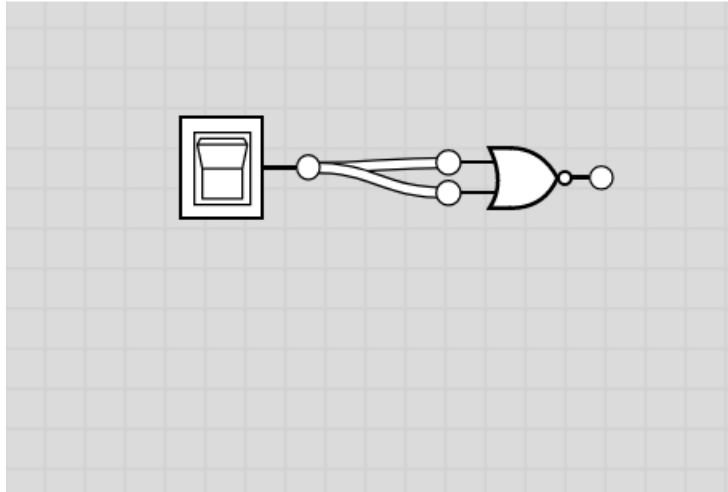
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1	0	0
1	1	1

NAND Gate:



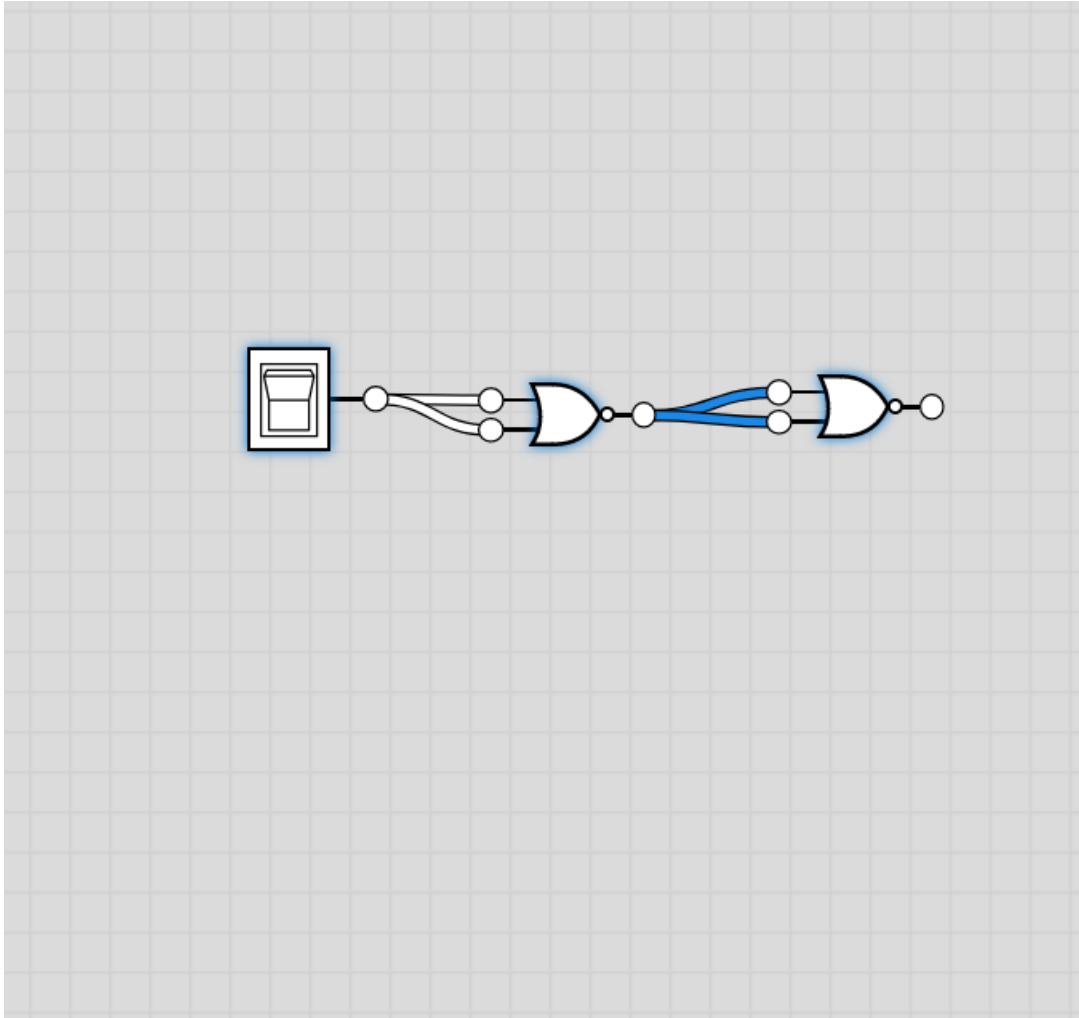
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0	1	1
1	0	1
1	1	0

NOT Gate:



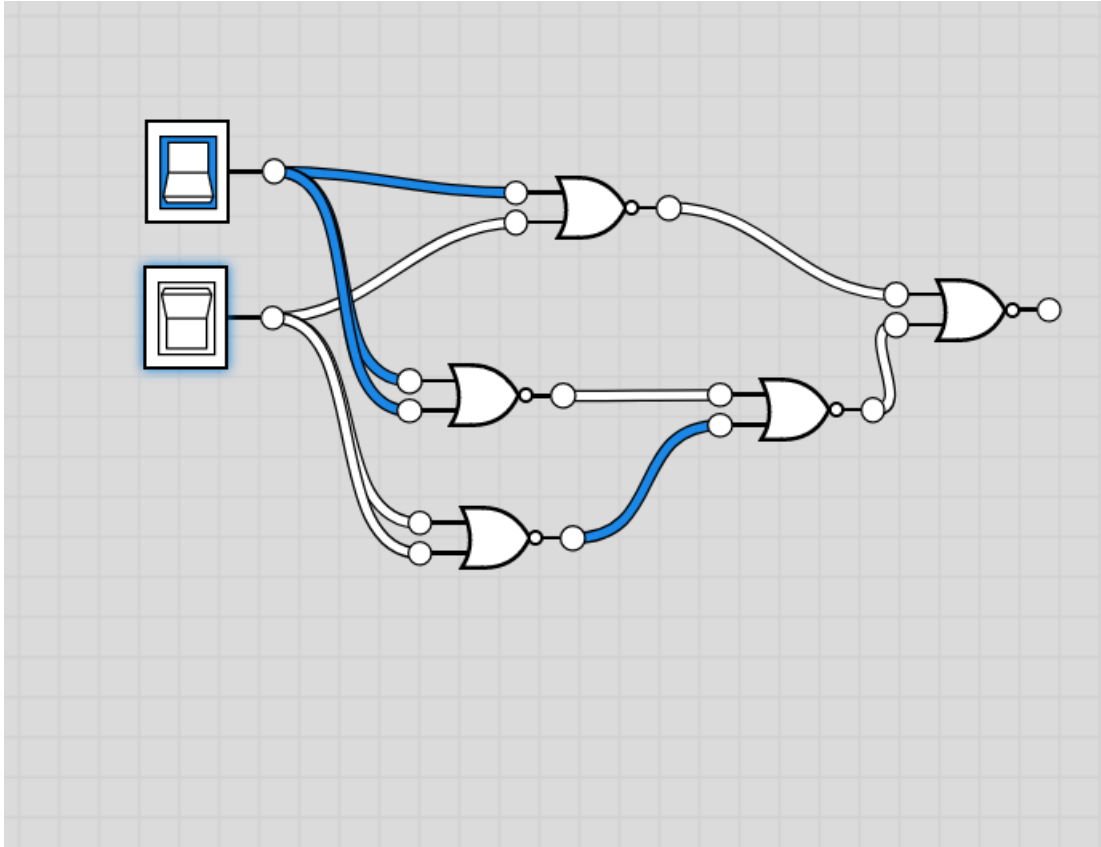
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1	0

Buffer:



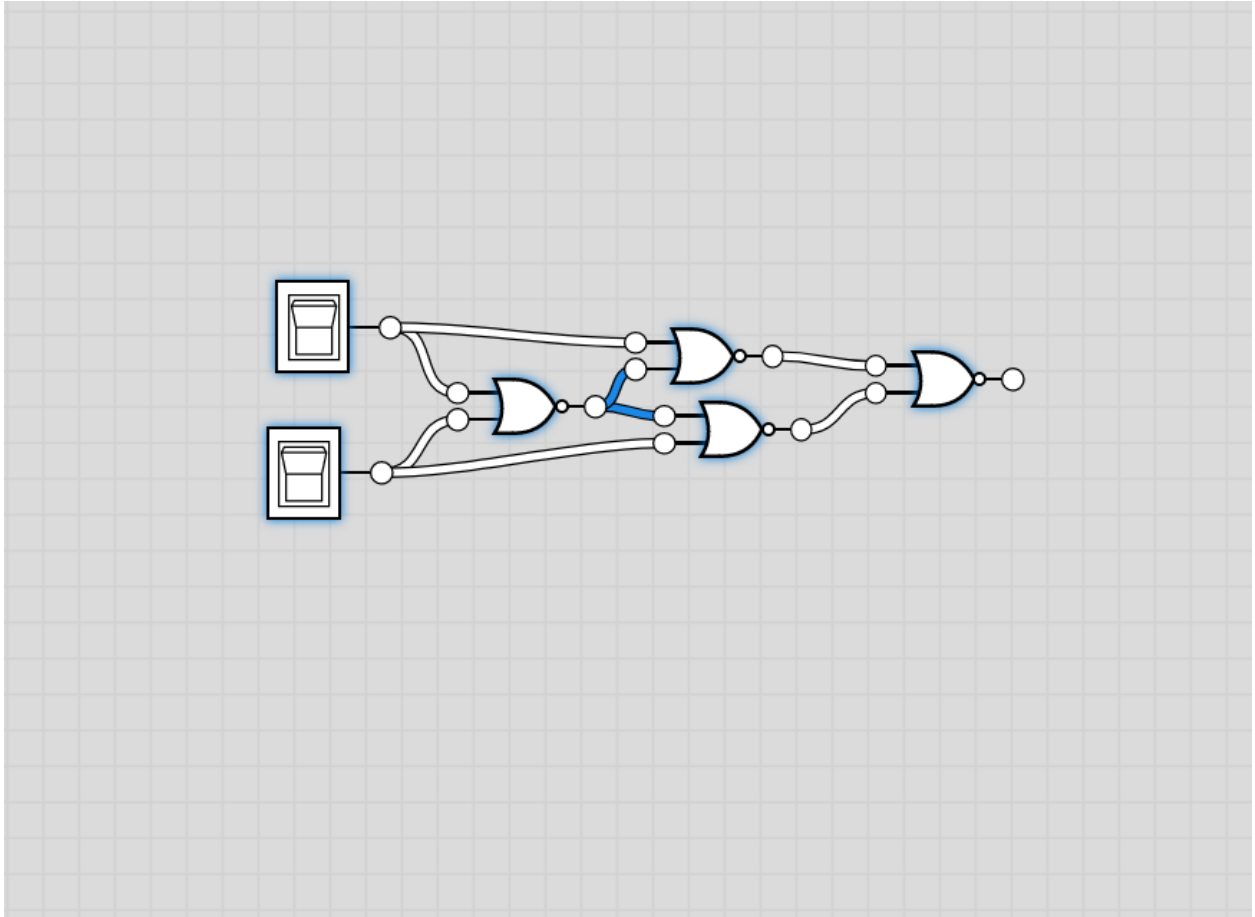
0	0
1	1

Exclusive-OR:



0	0	1
0	1	0
1	0	0
1	1	1

Exclusive-NOR:



0	0	0
0	1	0
1	0	0
1	1	0