Layer	Kicad file name or layer name	ALPWAN file name
Top Solder Paste	xxxx-F_Paste.gbr for the component-side solder paste	TSP-XXXXX-XXX.gbr
Top Silk Screen	xxxx-F_SilkS.gbr for the component-side silkscreen markings	TSS-XXXXX-XXX.gbr
Top Solder Mask	xxxx-F_Mask.gbr for the component-side solder mask	TSM-XXXXX-XXX.gbr
Top	xxxx-F_Cu.gbr for the component side	TOP-XXXXX-XXX.gbr
Internal 1	xxxx-In1.Cu.gbr	IL1-XXXXX-XXX.gbr
Internal 2	xxxx-In2.Cu.gbr	IL2-XXXXX-XXX.gbr
Internal n	xxxx-Inn.Cu.gbr	ILn-XXXXX-XXX.gbr
Bottom	xxxx-B_Cu.gbr for the copper side	BOT-XXXXX-XXX.gbr
Bottom Solder Mask	xxxx-B_Mask.gbr for the copper-side solder mask	BSM-XXXXX-XXX.gbr
Bottom Silk Screen	xxxx-B_SilkS.gbr for the copper-side silkscreen markings	BSS-XXXXX-XXX.gbr
Bottom Solder Paste	xxxx-B_Paste.gbr for the copper-side solder paste	BSP-XXXXX-XXX.gbr

GERBER file format: The format used by KiCad Pcbnew is RS274X 4.6, Metric, Leading zero omitted, Abs

Drill File	xxxx.drl	DRL-XXXXX-XXX.exc
Drill Map	xxxx-drl_map.pdf	MDD-XXXXX-XXX.pdf
Drill Report	xxxx-drl.rpt	DRL-XXXXX-XXX.rpt

EXCELLON file format : The format used by KiCad Pcbnew is EXCELLON 3.3, Metric, Leading zero omitted, Abs

PCB Characteristics	see MDD-XXXXX-XXX.pdf	
PCB Stack_Up	see MDD-XXXXX-XXX.pdf	
Controlled Impedance	Kicad PCB Calculator	IMP-XXXXX-XXX.docx
Controlled Impedance	Also available in PDF	IMP-XXXXX-XXX.pdf