



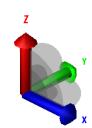
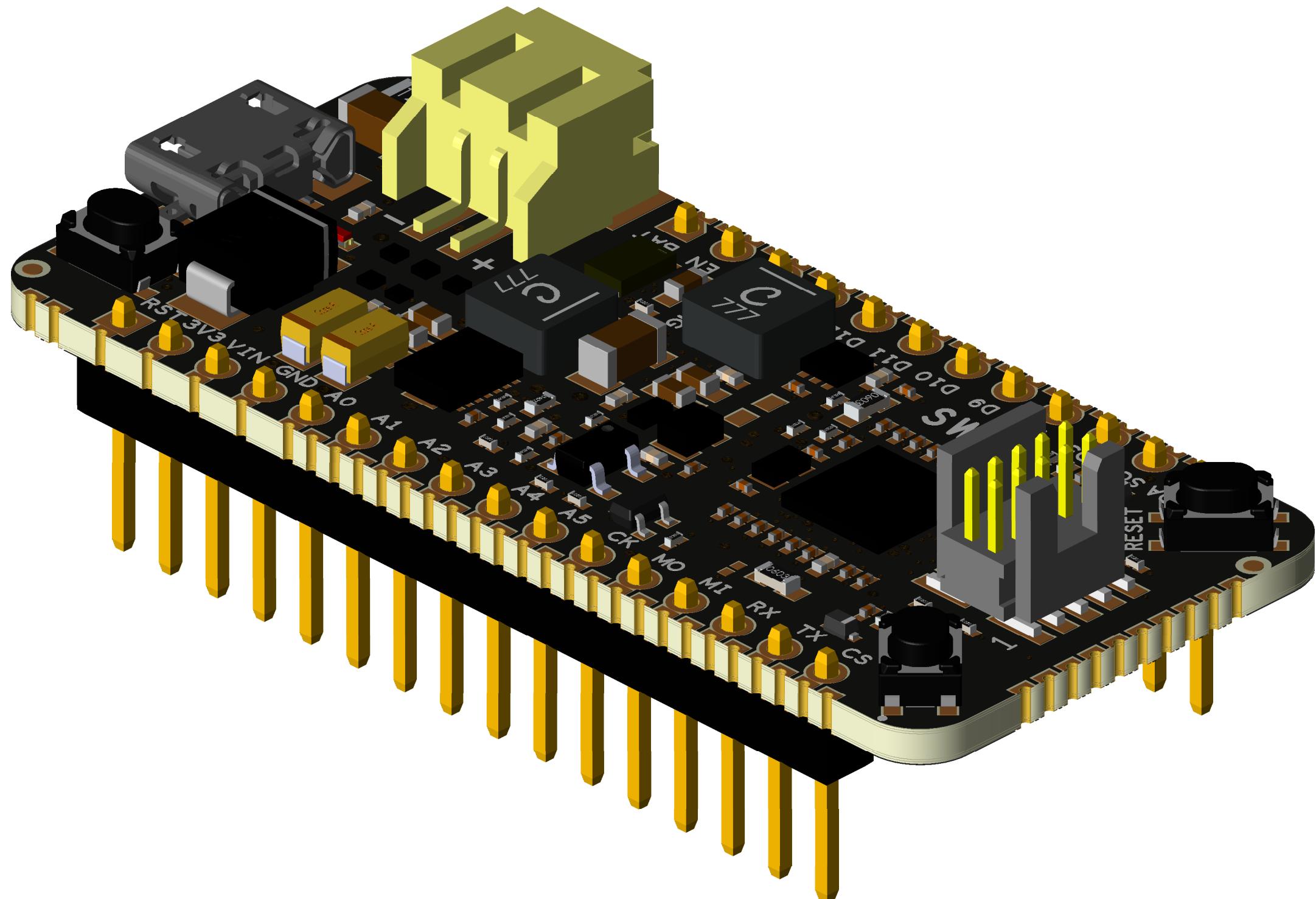
PCB DOCUMENTATION

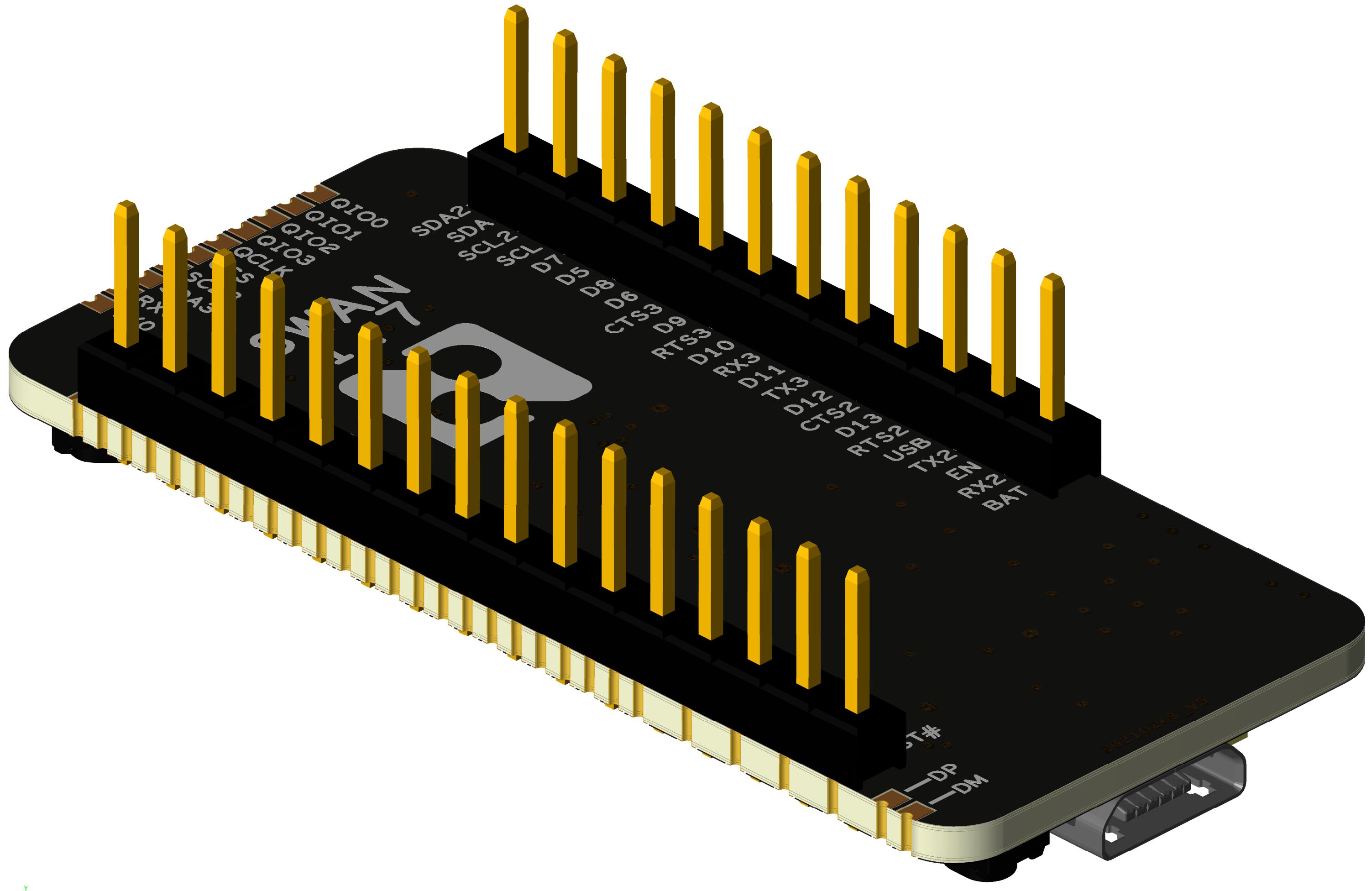
CUSTOMER: BLUES

PROJECT: SWAN-F_V5

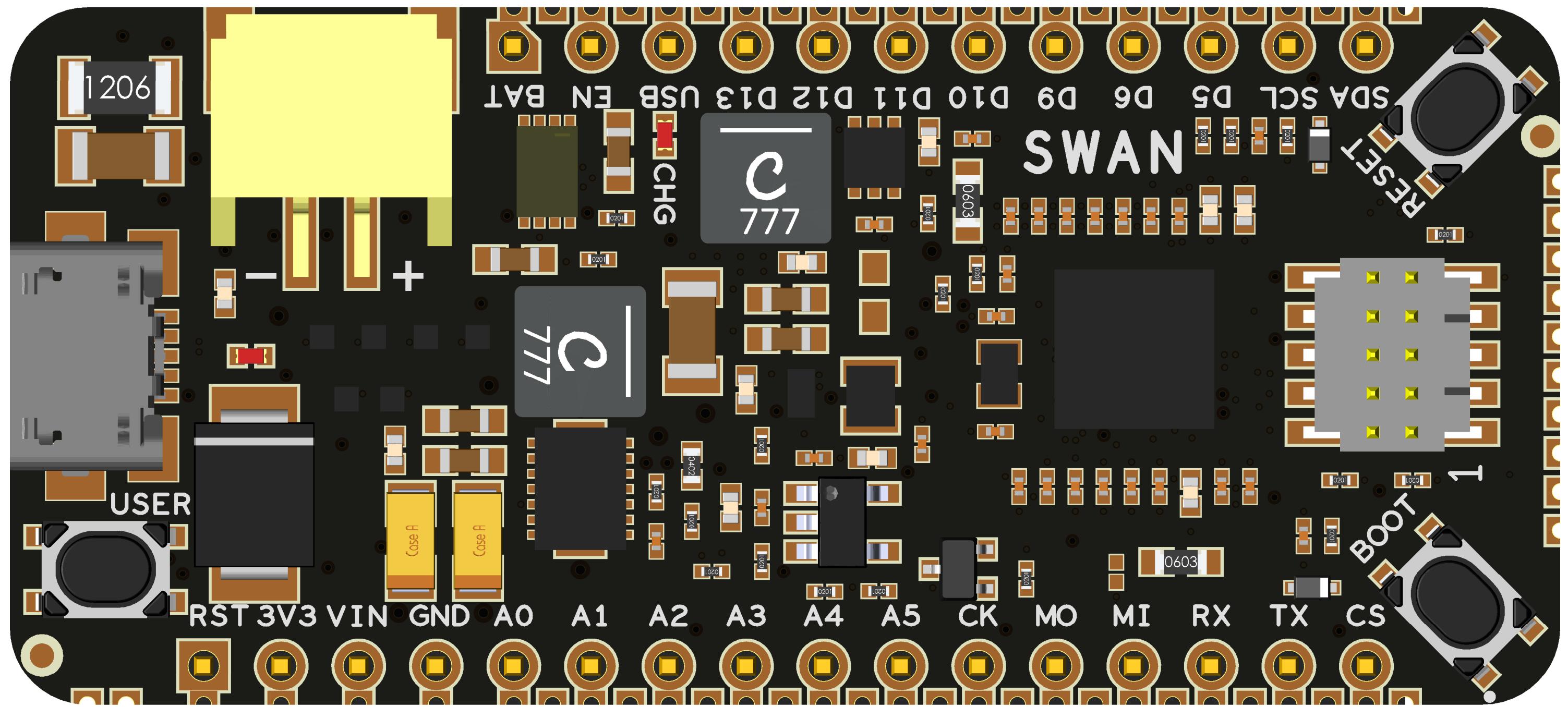
FAE CODE: 2021-0248

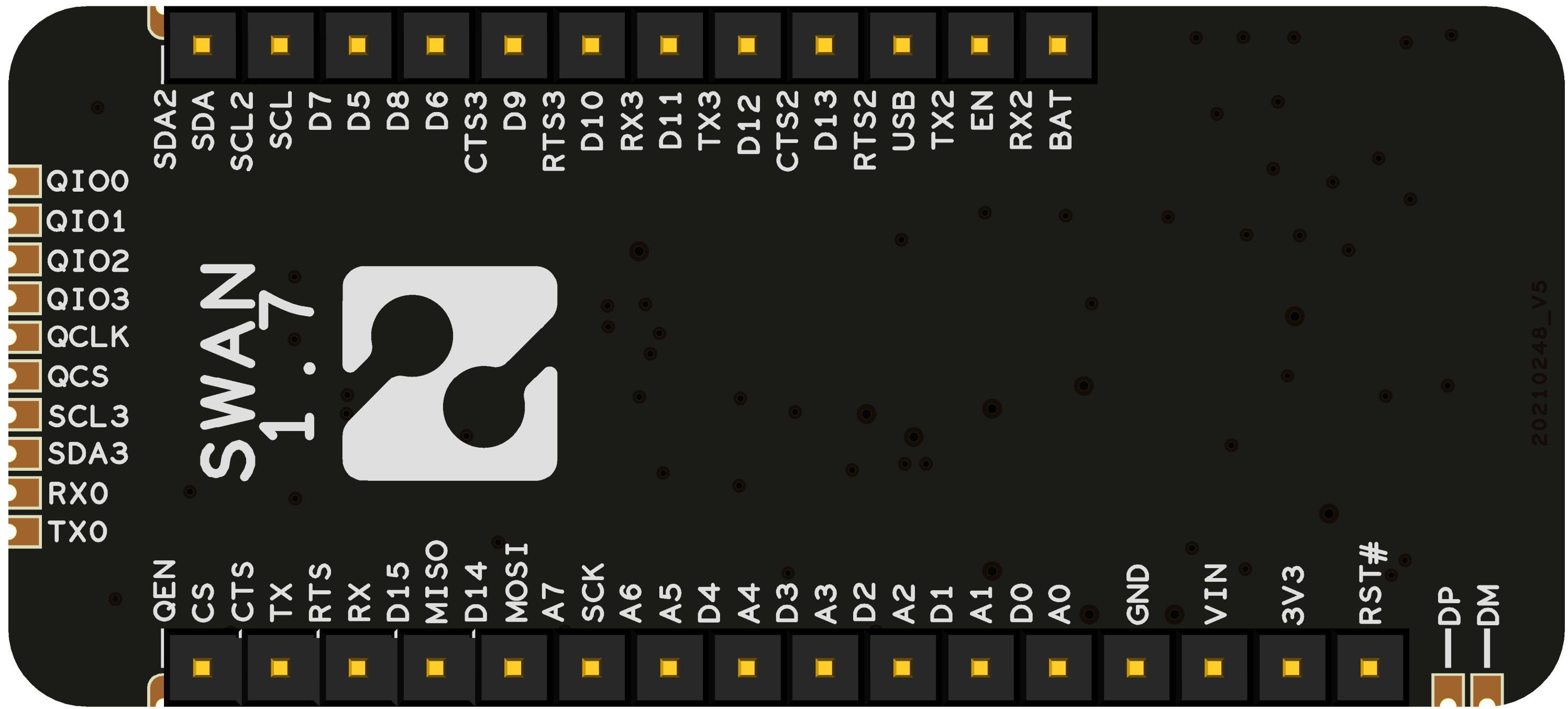
DATE: 15/09/2021

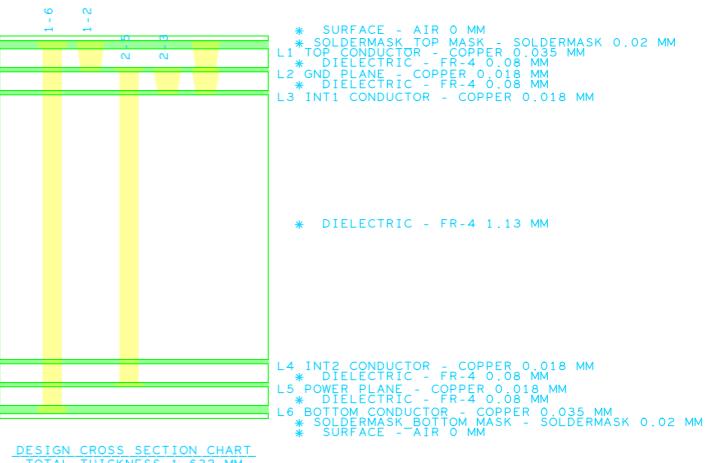
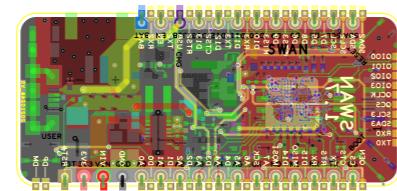




A 3D coordinate system is shown, consisting of three orthogonal axes: X (blue, horizontal), Y (green, diagonal), and Z (red, vertical). The axes are represented by thick, semi-transparent cylinders.







TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

IMPEDANCE CONTROL TABLE

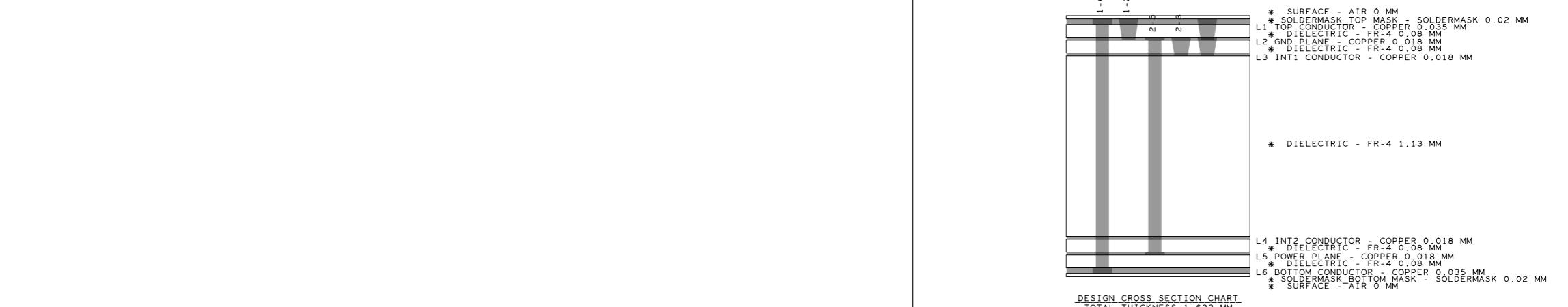
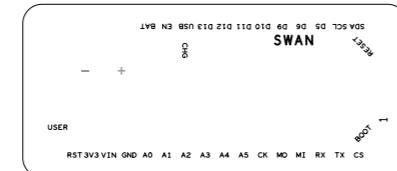
LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLLERANCE
INT1	0.1	0.14	NA	90 ohm	+/- 10%

MANUFACTURING SPECIFICATIONS

BOARD SIZE (XxYxZ)	50.8x22.86x1.6mm	IPC-6012 - IPC-A-600	CLASS 2
BOARD TOLLERANCE (X Y Z)	+/-0.2 +/-0.2 +/-10%	E-TESTING	YES
NO. OF LAYERS	6	UL-MARKING	YES
BASE COPPER OUTSIDE	18um	MICROVIA (hole < 100um)	YES
BASE COPPER INSIDE	18um	BLIND VIA	YES
FINISH	ENIG	BURIED VIA	YES
SOLDER COLOR	MATTE BLACK	VIA FILL/VIA IN PAD	YES - BGA PADS
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.1mm
DIELECTRIC MATERIAL	FR4-TG150	MIN. TRACE SPACING	0.08mm
IMPEDANCE CONTROL	YES	OUTER LAYER MIN. TRACE WIDTH	0.087mm
CTI	175V	INNER LAYER MIN. TRACE WIDTH	0.087mm

	Project name		Board name	
	SWAN		SWAN-F	
	Designer	G. Boschini	Approved	Not approved
	Project Code	2021-0248	Customer	BLUES
	Internal Code	-	Code	-
	Via C. Battisti 136, 24025, Gazzaniga (Bg), Italy Mail: info@fae.technology	Rev. 5	Size Page A3	Scale 1:1 Data 15/09/2021

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TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

IMPEDANCE CONTROL TABLE

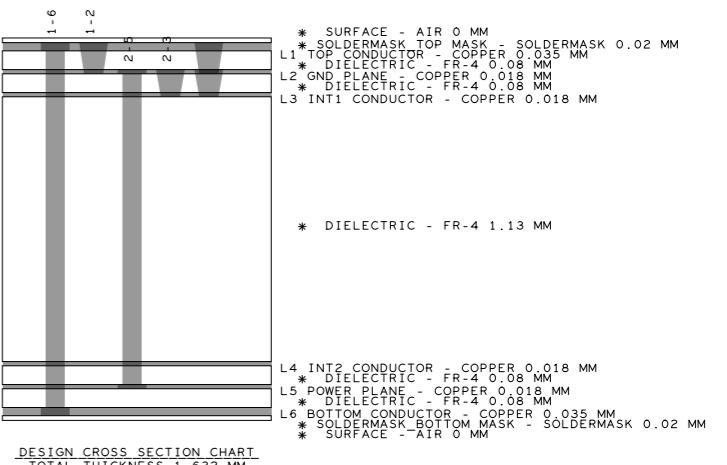
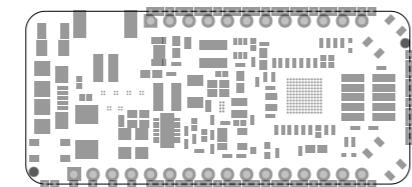
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MANUFACTURING SPECIFICATIONS

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NO. OF LAYERS	6	UL-MARKING	YES
BASE COPPER OUTSIDE	18um	MICROVIA (hole < 100um)	YES
BASE COPPER INSIDE	18um	BLIND VIA	YES
FINISH	ENIG	BURIED VIA	YES
SOLDER COLOR	MATTE BLACK	VIA FILL/VIA IN PAD	YES - BGA PADS
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	5	A3	1:1	15/09/2021

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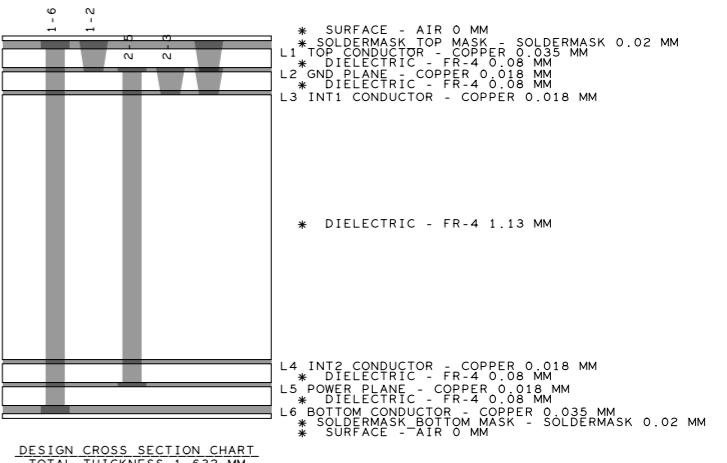
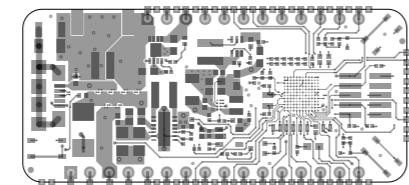
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FINISH	ENIG	BURIED VIA	YES
SOLDER COLOR	MATTE BLACK	VIA FILL/VIA IN PAD	YES - BGA PADS
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.1mm
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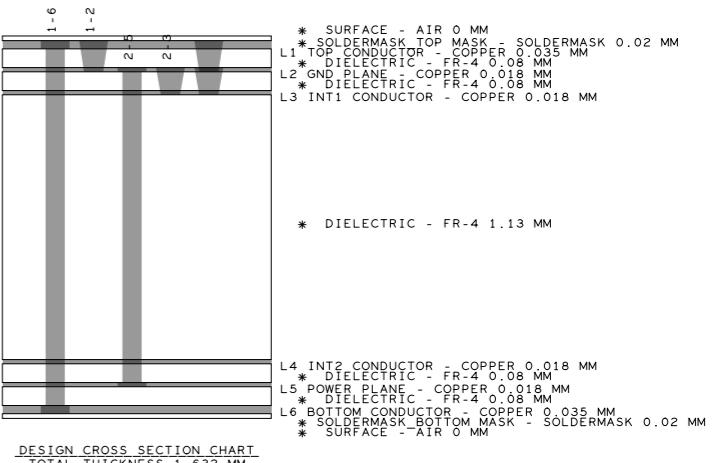
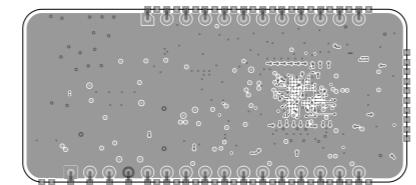
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INT1	0.1	0.14	NA	90 ohm	+/- 10%

MANUFACTURING SPECIFICATIONS

BOARD SIZE (XxYxZ)	50.8x22.86x1.6mm	IPC-6012 - IPC-A-600	CLASS 2
BOARD TOLLERANCE (X Y Z)	+/-0.2 +/-0.2 +/-10%	E-TESTING	YES
NO. OF LAYERS	6	UL-MARKING	YES
BASE COPPER OUTSIDE	18um	MICROVIA (hole < 100um)	YES
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IMPEDANCE CONTROL TABLE

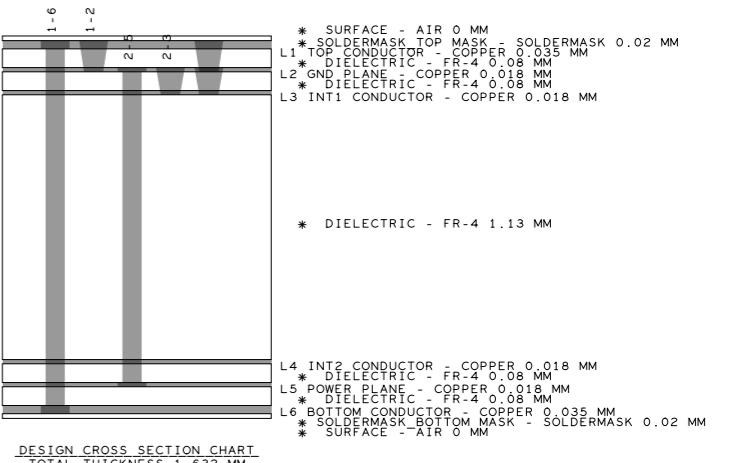
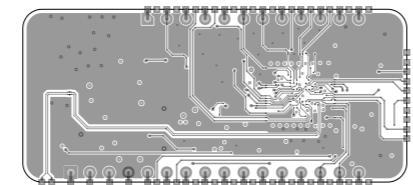
LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLLERANCE
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MANUFACTURING SPECIFICATIONS

BOARD SIZE (XxYxZ)	50.8x22.86x1.6mm	IPC-6012 - IPC-A-600	CLASS 2
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	Designer	G. Boschini	Approved	Not approved
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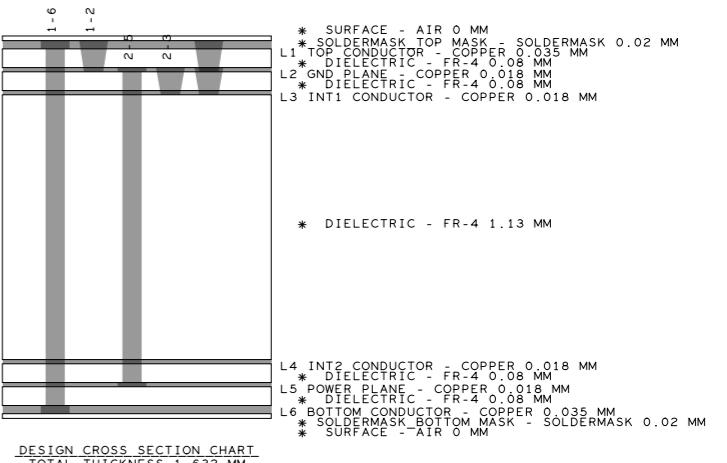
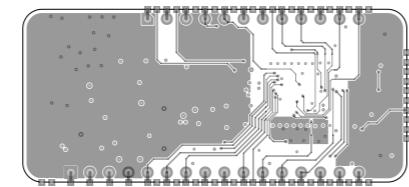
LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLLERANCE
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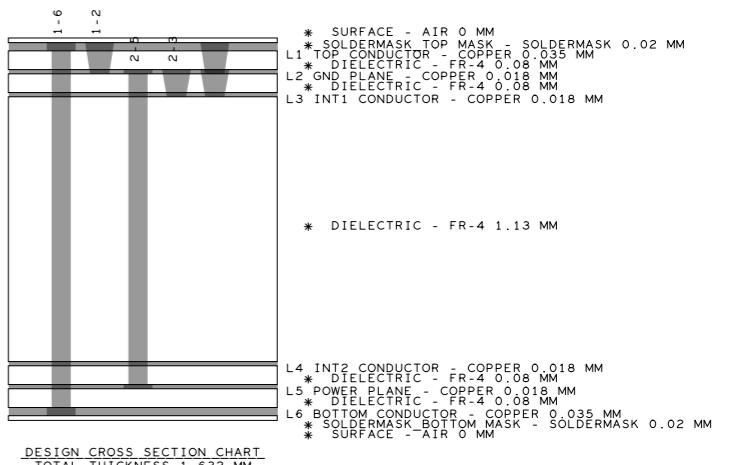
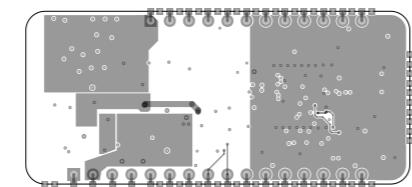
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MANUFACTURING SPECIFICATIONS

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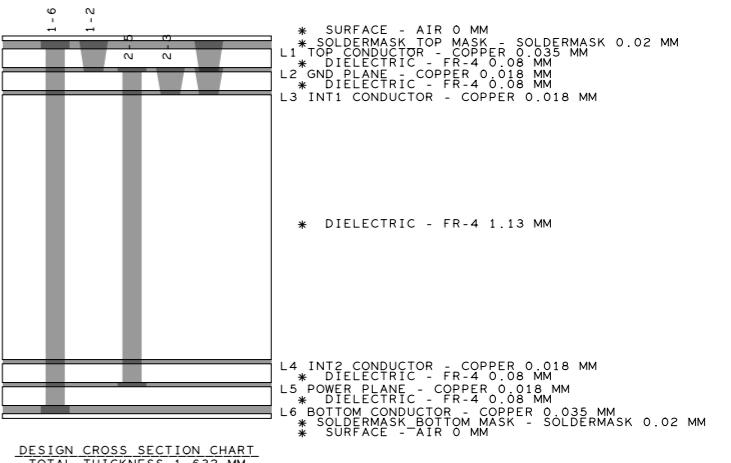
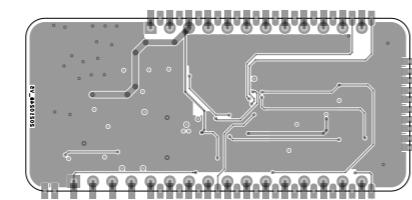
IMPEDANCE CONTROL TABLE

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MANUFACTURING SPECIFICATIONS

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FAE TECHNOLOGY	Project name		Board name
	SWAN		SWAN-F
	Designer G. Boschini		Approved Not approved
	Project Code 2021-0248		Customer BLUES
	Internal Code -		Code -
Via C. Battisti 136, 24025, Gazzaniga (Bg), Italy Mail: info@fae.technology Tel: +39 035738130		Rev. 5	Size Page A3
		Scale 1:1	Data 15/09/2021
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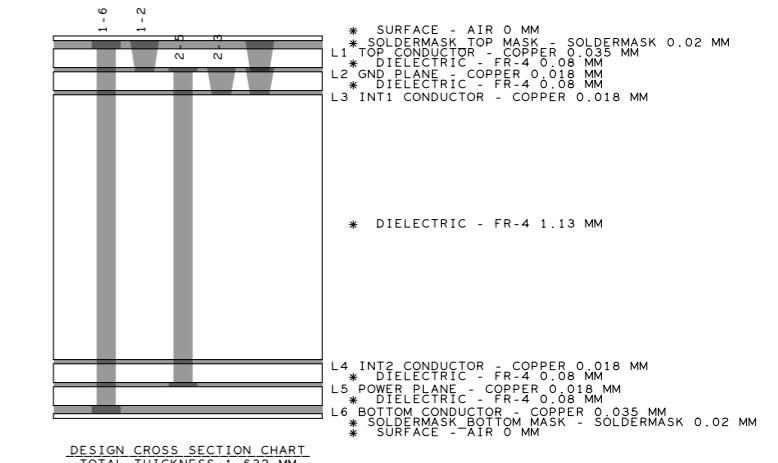
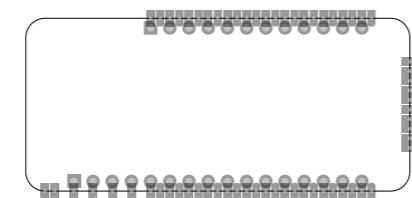
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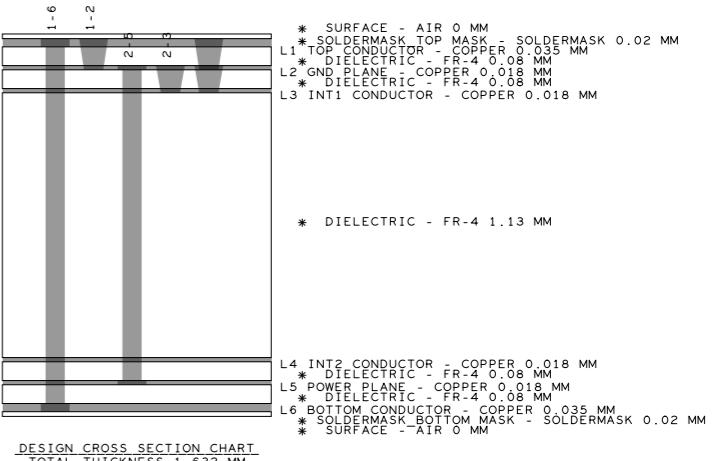
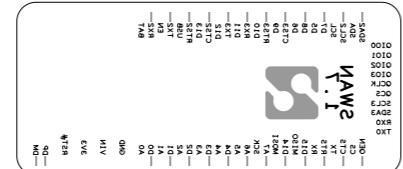
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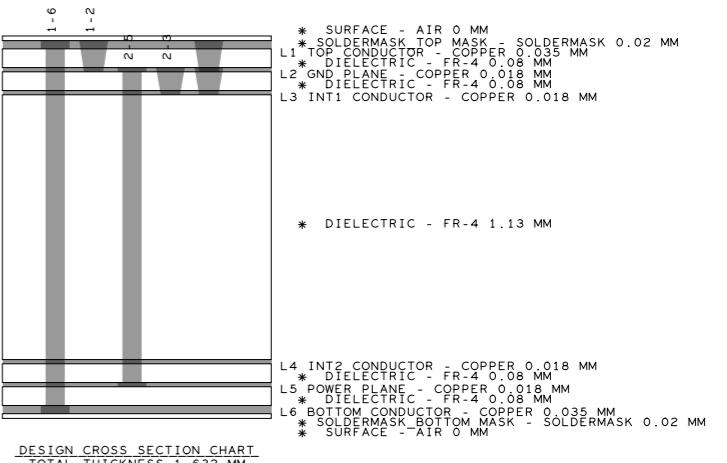
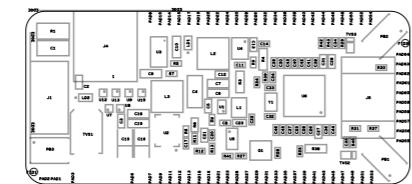
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BASE COPPER INSIDE	18um	BLIND VIA	YES
FINISH	ENIG	BURIED VIA	YES
SOLDER COLOR	MATTE BLACK	VIA FILL/VIA IN PAD	YES - BGA PADS
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.1mm
DIELECTRIC MATERIAL	FR4-TG150	MIN. TRACE SPACING	0.08mm
IMPEDANCE CONTROL	YES	OUTER LAYER MIN. TRACE WIDTH	0.087mm
CTI	175V	INNER LAYER MIN. TRACE WIDTH	0.087mm

FAE TECHNOLOGY	Project name		Board name	
	SWAN		SWAN-F	
	Designer		Approved	Not approved
	G. Boschini			
	Project Code		Customer	BLUES
Internal Code -		Code	-	
Via C. Battisti 136, 24025, Gazzaniga (Bg), Italy Mail: info@fae.technology		Rev.	Size Page	Scale
		5	A3	1:1
		Data	15/09/2021	
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TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

IMPEDANCE CONTROL TABLE

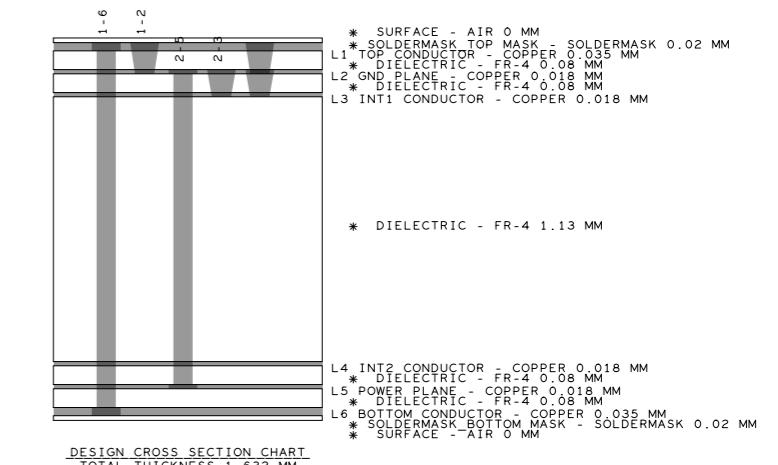
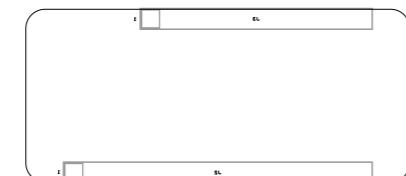
LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLLERANCE
INT1	0.1	0.14	NA	90 ohm	+/- 10%

MANUFACTURING SPECIFICATIONS

BOARD SIZE (XxYxZ)	50.8x22.86x1.6mm	IPC-6012 - IPC-A-600	CLASS 2
BOARD TOLLERANCE (X Y Z)	+/-0.2 +/-0.2 +/-10%	E-TESTING	YES
NO. OF LAYERS	6	UL-MARKING	YES
BASE COPPER OUTSIDE	18um	MICROVIA (hole < 100um)	YES
BASE COPPER INSIDE	18um	BLIND VIA	YES
FINISH	ENIG	BURIED VIA	YES
SOLDER COLOR	MATTE BLACK	VIA FILL/VIA IN PAD	YES - BGA PADS
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.1mm
DIELECTRIC MATERIAL	FR4-TG150	MIN. TRACE SPACING	0.08mm
IMPEDANCE CONTROL	YES	OUTER LAYER MIN. TRACE WIDTH	0.087mm
CTI	175V	INNER LAYER MIN. TRACE WIDTH	0.087mm

	Project name		Board name	
	SWAN		SWAN-F	
	Designer	G. Boschin	Approved	Not approved
	Project Code	2021-0248	Customer	BLUES
	Internal Code	-	Code	-
	Via C. Battisti 136, 24025, Gazzaniga (Bg), Italy Mail: info@fae.technology	Rev. 5	Size Page A3	Scale 1:1 Data 15/09/2021

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TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

IMPEDANCE CONTROL TABLE

LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLLERANCE
INT1	0.1	0.14	NA	90 ohm	+/- 10%

MANUFACTURING SPECIFICATIONS

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SOLDER COLOR	MATTE BLACK	VIA FILL/VIA IN PAD	YES - BGA PADS
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.1mm
DIELECTRIC MATERIAL	FR4-TG150	MIN. TRACE SPACING	0.08mm
IMPEDANCE CONTROL	YES	OUTER LAYER MIN. TRACE WIDTH	0.087mm
CTI	175V	INNER LAYER MIN. TRACE WIDTH	0.087mm

FAE TECHNOLOGY	Project name		Board name	
	SWAN		SWAN-F	
	Designer		Approved	Not approved
	G. Boschin			
	Project Code		Customer	BLUES
	Internal Code -		Code	-
Via C. Battisti 136, 24025, Gazzaniga (Bg), Italy		Rev.	Size Page	Scale
Mail: info@fae.technology		5	A3	1:1
Tel: +39 035738130		Data	15/09/2021	
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DRILL CHART: TOP to BOTTOM

ALL UNITS ARE IN MILLIMETERS

FIGURE	FINISHED SIZE	TOLERANCE	TYPE	QTY
.	0.2	+0.075/-0.075	PLATED	55
.	0.3	+0.075/-0.075	PLATED	10
*	0.5	+0.075/-0.075	PLATED	64
O	1.0	+0.075/-0.075	PLATED	11
△	1.0	+0.075/-0.075	PLATED	17

DRILL CHART: TOP to GND

ALL UNITS ARE IN MILLIMETERS

FIGURE	FINISHED SIZE	TOLERANCE	TYPE	QTY
.	0.1	+0.075/-0.075	PLATED	211

DRILL CHART: GND to INT1

ALL UNITS ARE IN MILLIMETERS

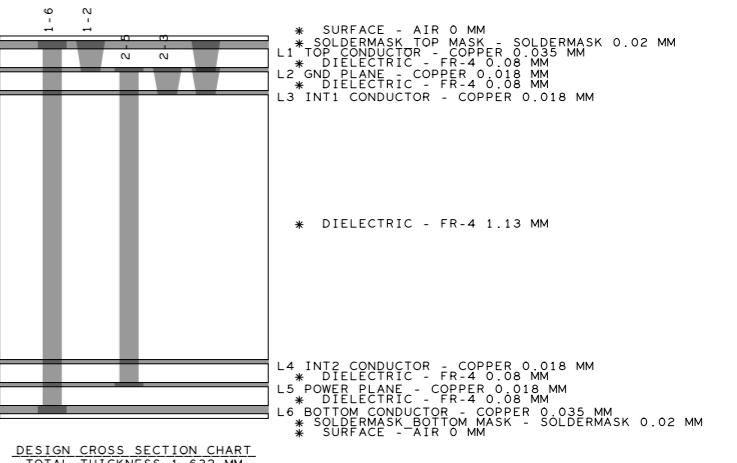
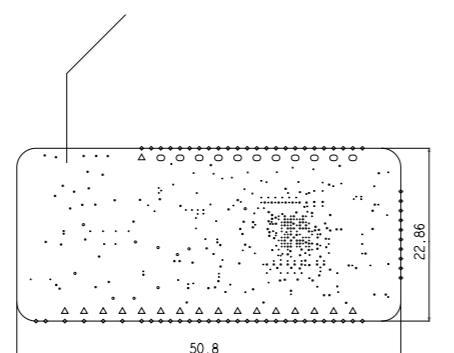
FIGURE	FINISHED SIZE	TOLERANCE	TYPE	QTY
.	0.1	+0.075/-0.075	PLATED	79

DRILL CHART: GND to POWER

ALL UNITS ARE IN MILLIMETERS

FIGURE	FINISHED SIZE	TOLERANCE	TYPE	QTY
.	0.2	+0.075/-0.075	PLATED	71

Put PCB data code and/or manufacturer logo
on Bottom Side



TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

IMPEDANCE CONTROL TABLE

LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLERANCE
INT1	0.1	0.14	NA	90 ohm	+/- 10%

MANUFACTURING SPECIFICATIONS

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	Project name	SWAN		Board name	SWAN-F	
	Designer	G. Boschini		Approved	Not approved	
	Project Code	2021-0248		Customer	BLUES	
	Internal Code	-		Code	-	
	Via C. Battisti 136, 24025, Gazzaniga (Bg), Italy Mail: info@fae.technology	Rev.	Size Page	Scale	Data	15/09/2021
			5	A3	1:1	

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