

Module name

Notecarrier-XS

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Design considerations

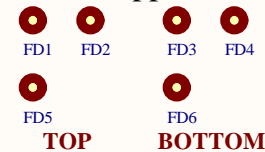
SOFTWARE CONFIG NOTE:

- Contains important notes for software development.
- Things like: pin configuration, timing requirements,
IC configuration etc.

DESIGN NOTE:

- Hardware notes

Board support



PCB1

PCB

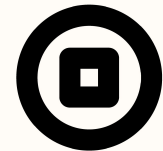
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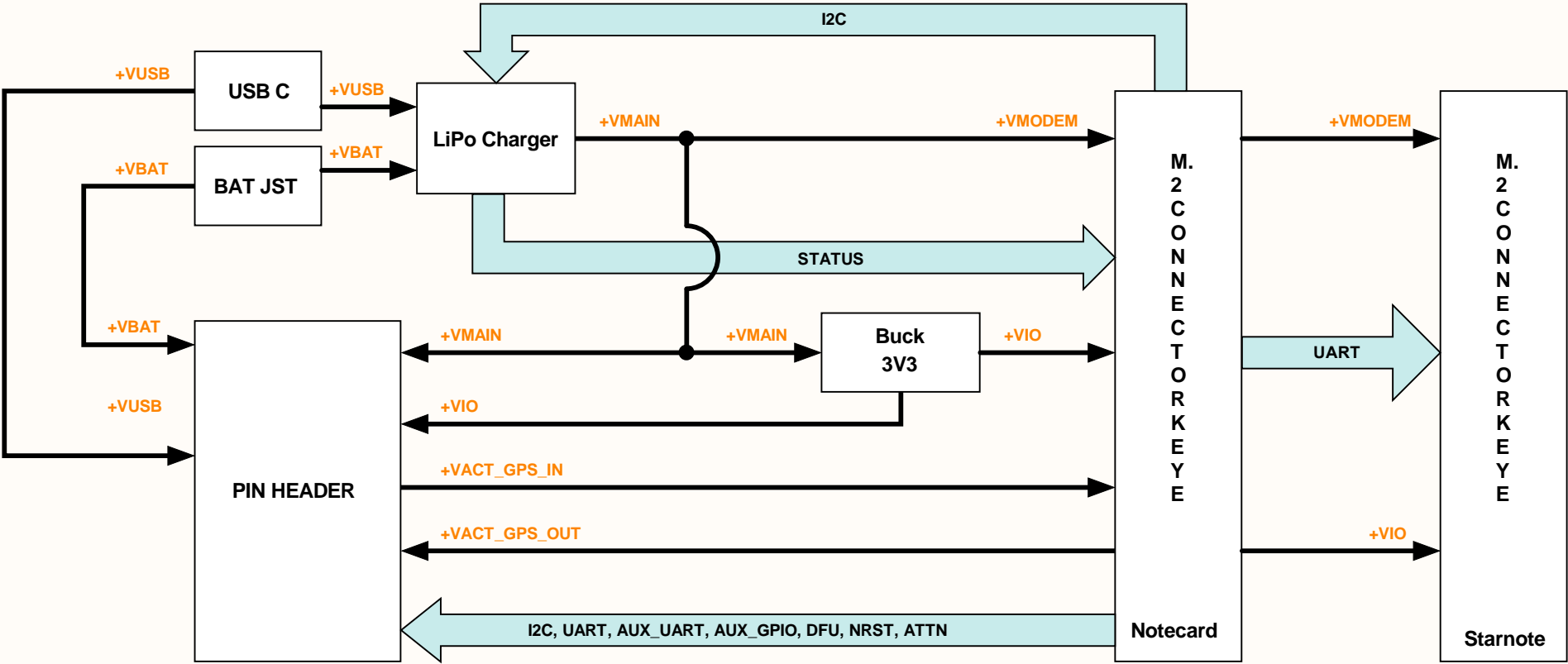
DOC1

HW-E

100426

Title: 01_COVER.SchDoc			
Project: 100426_notecarrier-xs.PrjPCB			
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author: M. Marsic		Acceptor: J. Wiedey	
Sheet 1 of 5		Rev: 11	Format: A4

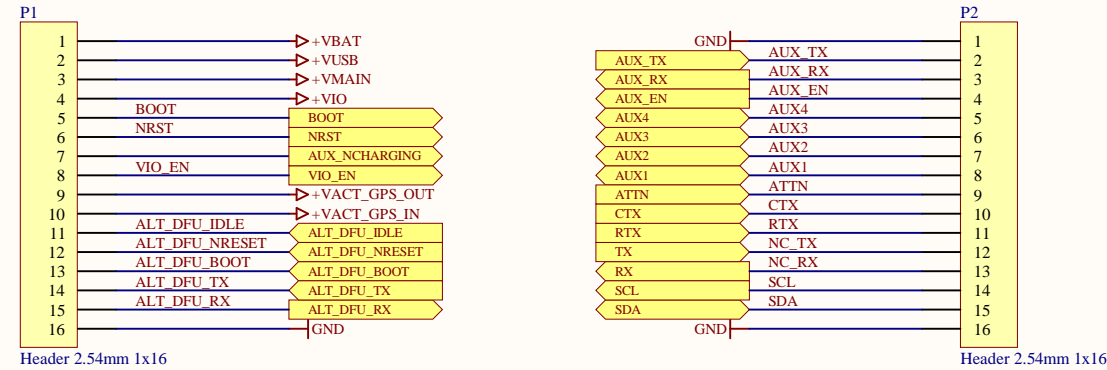




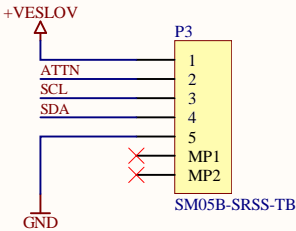
Title: 02_BLOCK-DIAGRAM.SchDoc			
Project: 100426_notecarrier-xs.PrjPCB			
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author:	M. Marsic	Acceptor:	J. Wiedey
Sheet 2 of 5		Rev: 11	Format: A4



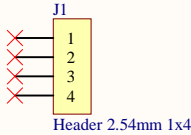
I/O Pin Headers & Castellations



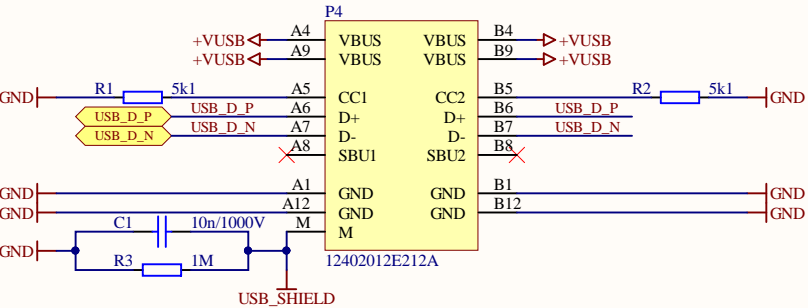
ESLOV Connector



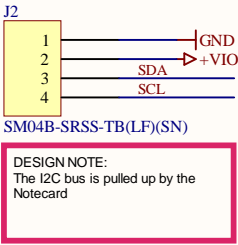
Mech Support Connector



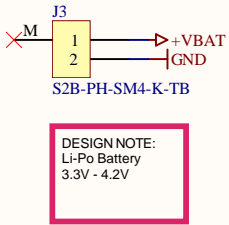
USB C Connector



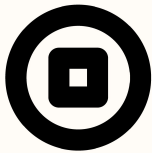
QWIIC Connector



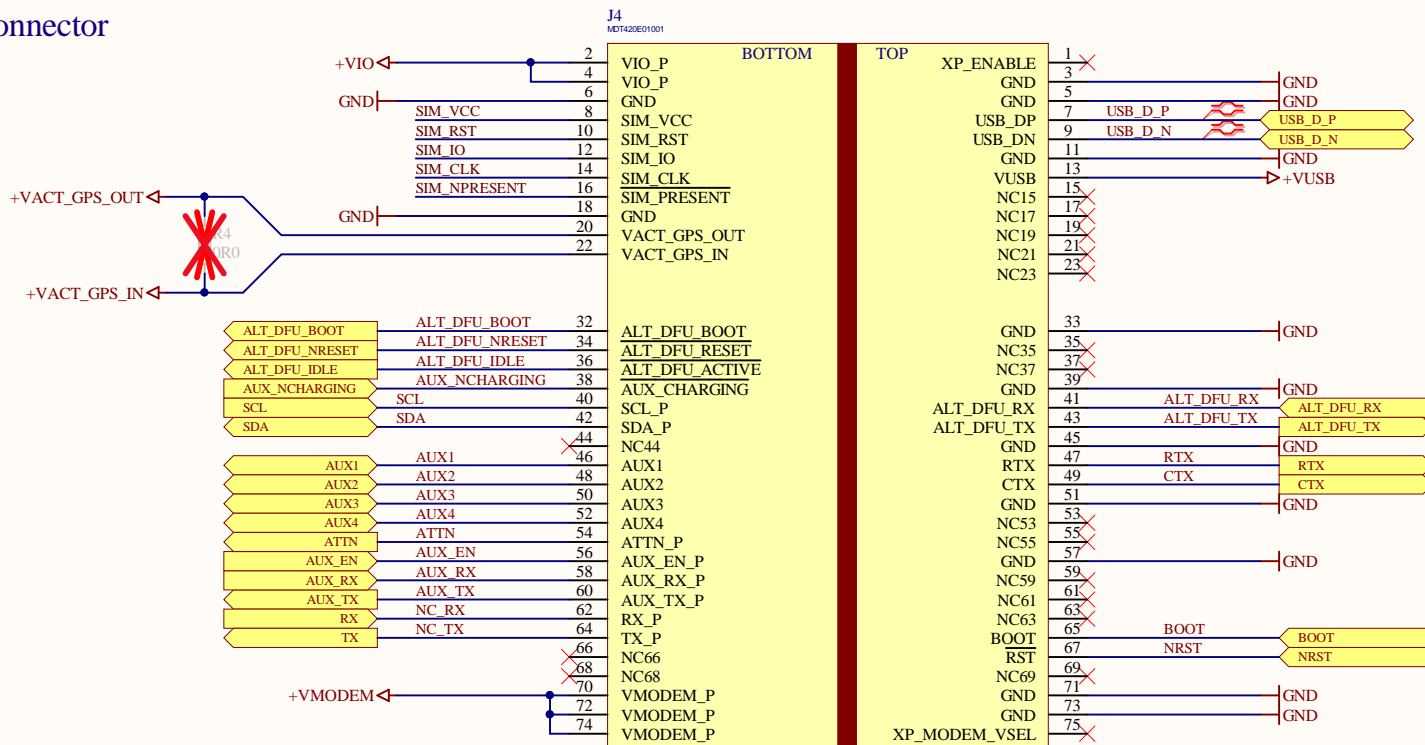
Battery Connector



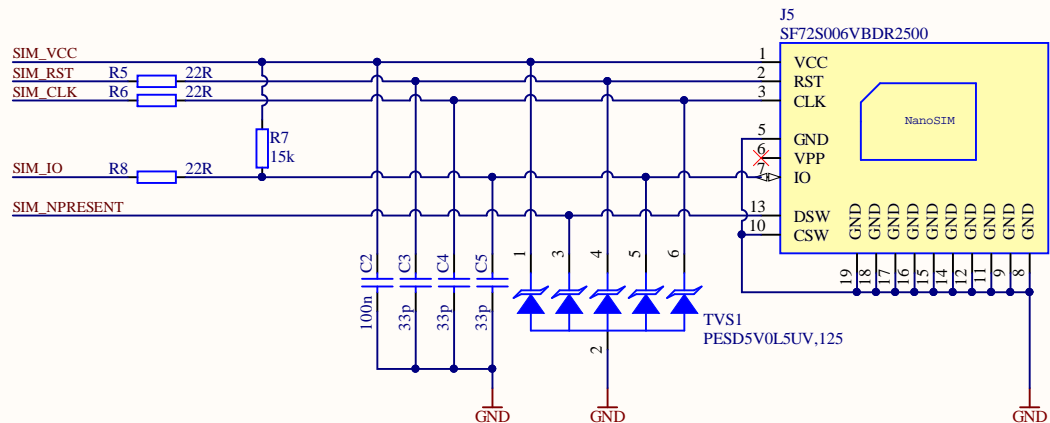
Title: 03_IO-CONNECTION.SchDoc		
Project: 100426_notecarrier-xs.PrjPCB		
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author: M. Marsic	Acceptor: J. Wiedey	
Sheet 3 of 5	Rev: 11	Format: A4



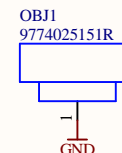
Notecard Connector



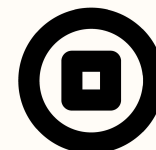
SIM Card



Notecard Support



Title: 04_NOTECARD-CONNECTION.SchDoc		
Project: 100426_notecarrier-xs.PrjPCB		
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author: M. Marsic	Acceptor: J. Wiedey	
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Battery Charger w/ Power Path

DESIGN NOTE:

Charger:
- Default charging voltage: 4.2V
- Default charging current: 320mA
- Default termination current: 20mA
- Default pre-charge current: 20mA
- No battery thermal protection

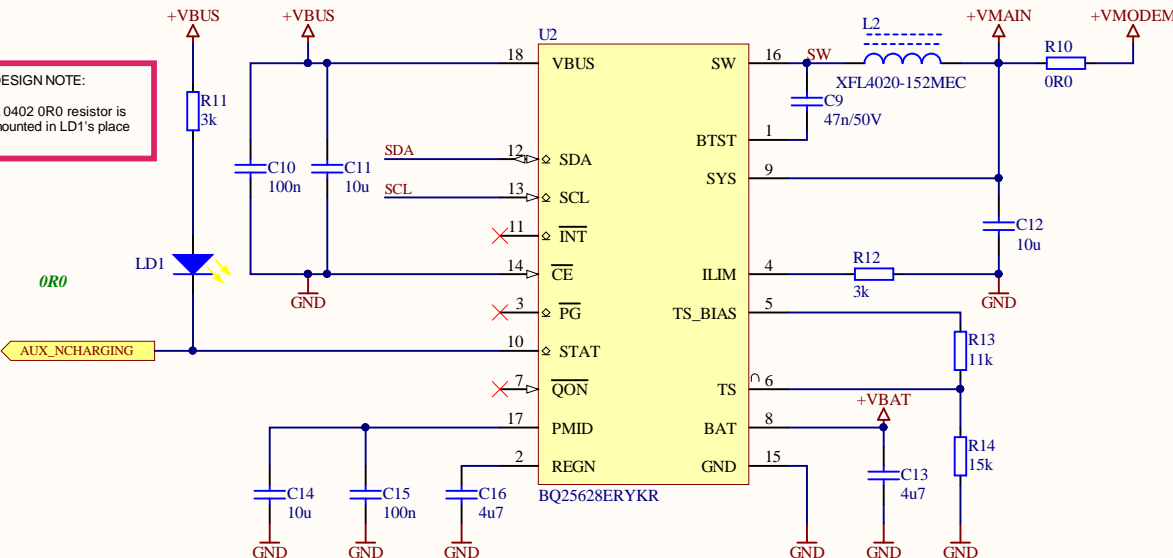
DESIGN NOTE:

Power Path:
- Input current limit: 3.5A
- 26mOhm reverse blocking transistor
- 15mOhm battery transistor

Buck Regulator:
- 1.5MHz frequency
- Input Voltage: 5V
- Output Voltage: 3.3-4.2V
- Peak current: 2.5A

DESIGN NOTE:

A 0402 0R0 resistor is mounted in LD1's place

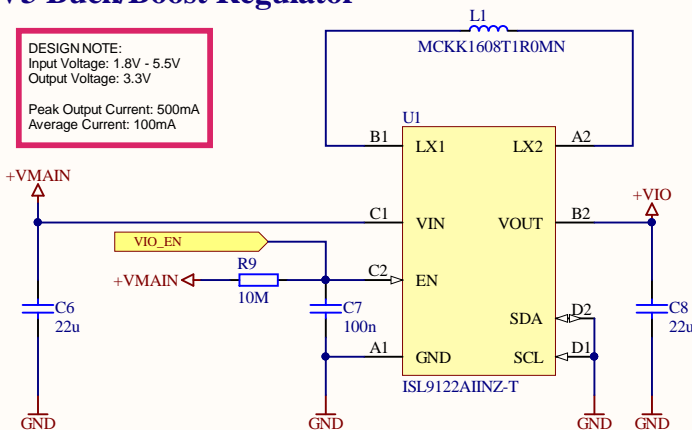


3V3 Buck/Boost Regulator

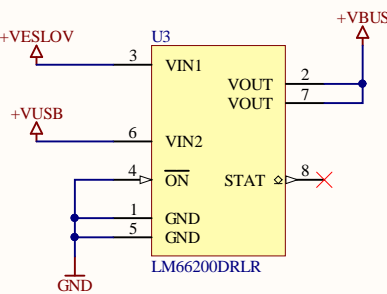
DESIGN NOTE:

Input Voltage: 1.8V - 5.5V
Output Voltage: 3.3V

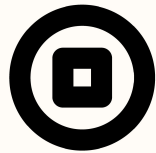
Peak Output Current: 500mA
Average Current: 100mA



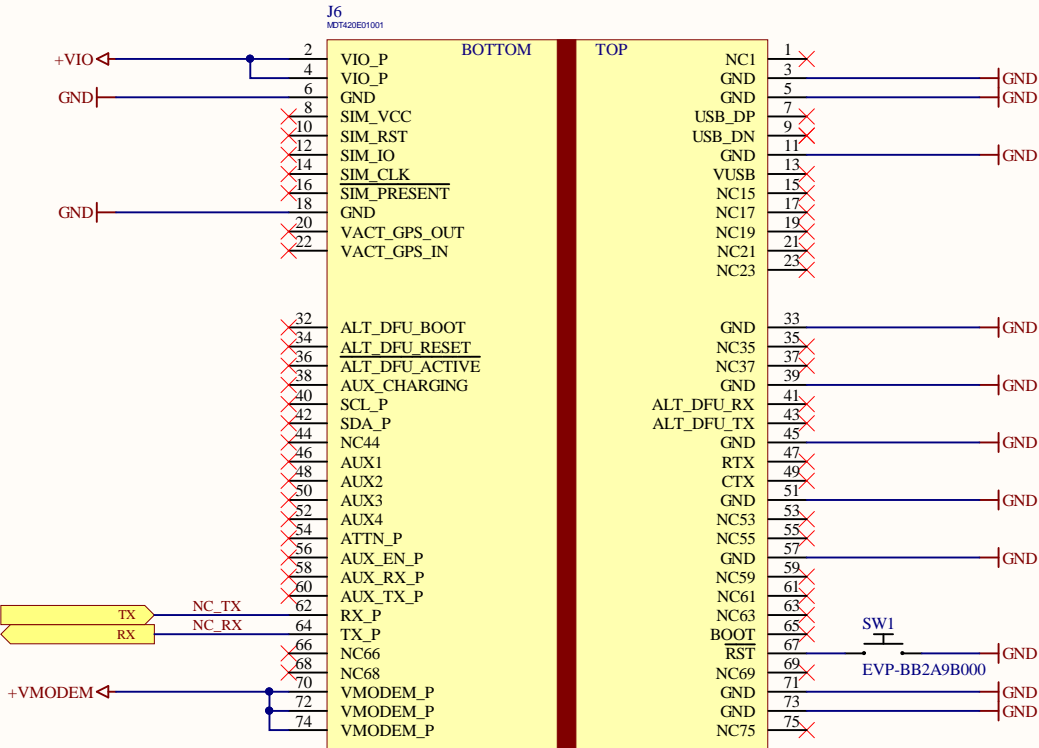
VBUS ORing



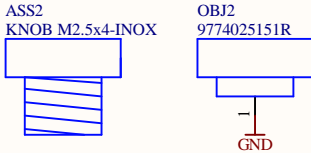
Title: 05_POWER.SchDoc			
Project: 100426_notecarrier-xs.PrjPCB			
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author:	M. Marsic	Acceptor:	J. Wiedey
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Starnote Connector



Starnote Support



Title: 06_STARNOTE-CONNECTION.SchDoc		
Project: 100426_notecarrier-xs.PrjPCB		
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author: M. Marsic	Acceptor: J. Wiedey	
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A

A

B

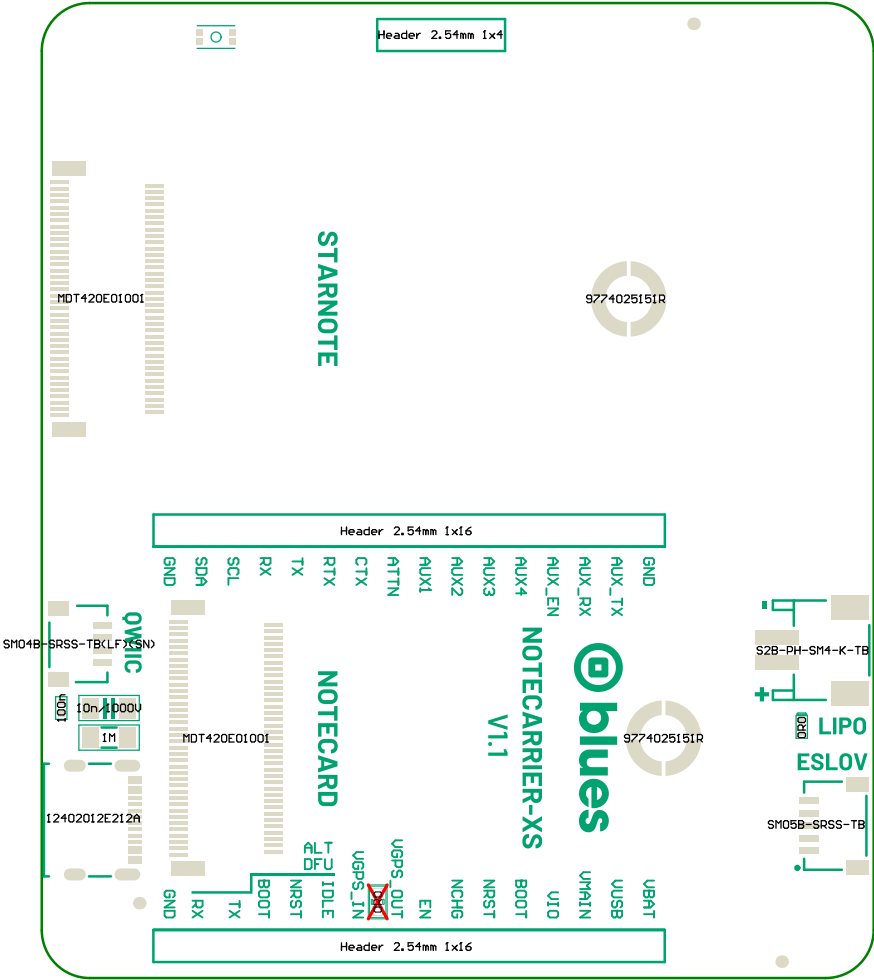
B

C

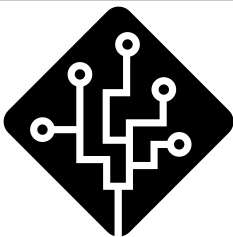
C

D

D

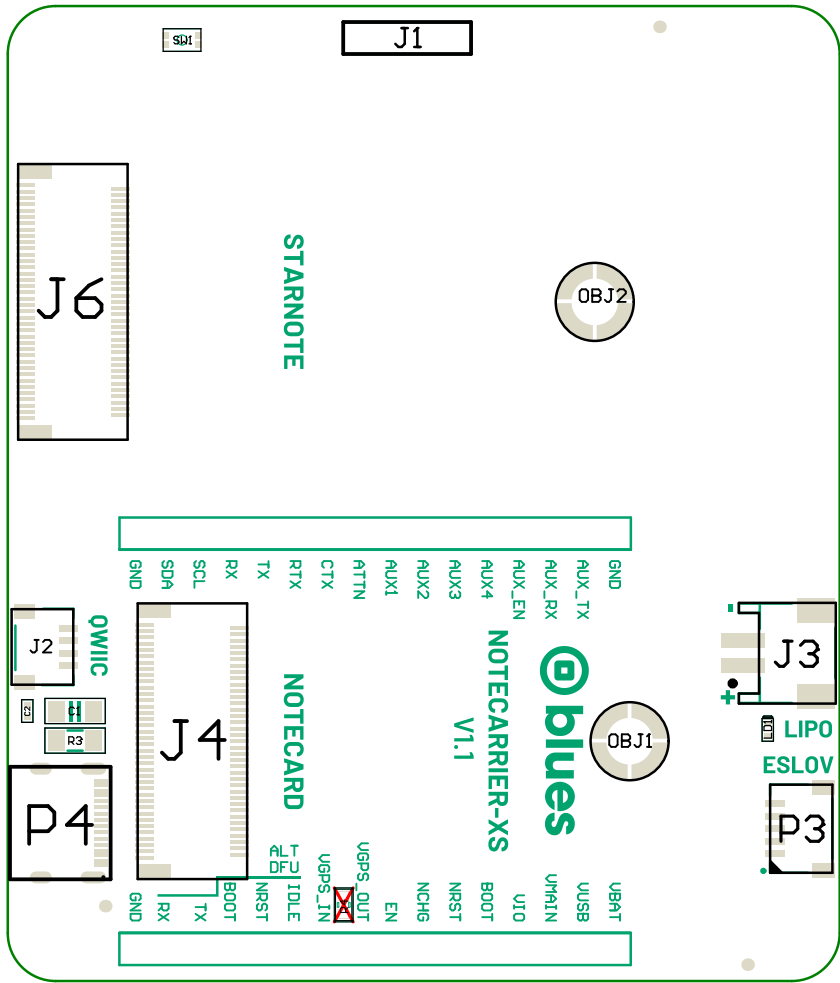


Mount pin headers on bottom!

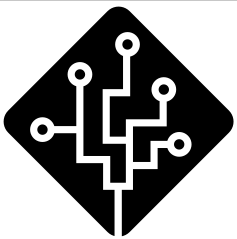


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AUTHOR: ACCEPTOR:	SIGNATURE	TITLE: Assembly drawing comments - TOP	
	M. Marsic		
	J. Wiedey	BOM NO.: 3000-789-001	
DESIGN DOCUMENT NAME: 100426 Notecarrier-XS Rev. 11			FORMAT: A4



Mount pin headers on bottom!



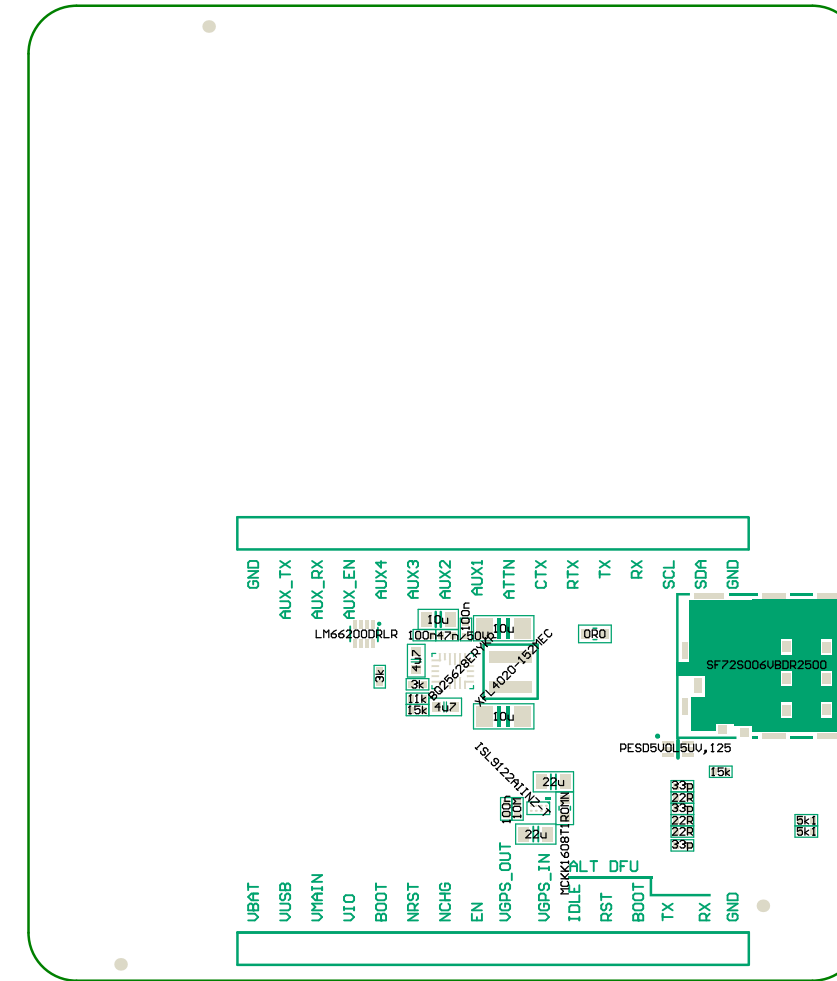
Byte Lab Grupa d.o.o.
Medarska 69/1
10000 Zagreb
Croatia

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AUTHOR:	M. Marsic	
ACCEPTOR:	J. Wiedey	
		BOM NO.: 3000-789-001
DESIGN DOCUMENT NAME: 100426 Notecarrier-XS Rev. 11		FORMAT: A4

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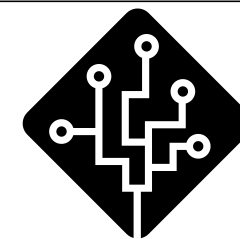
A

B



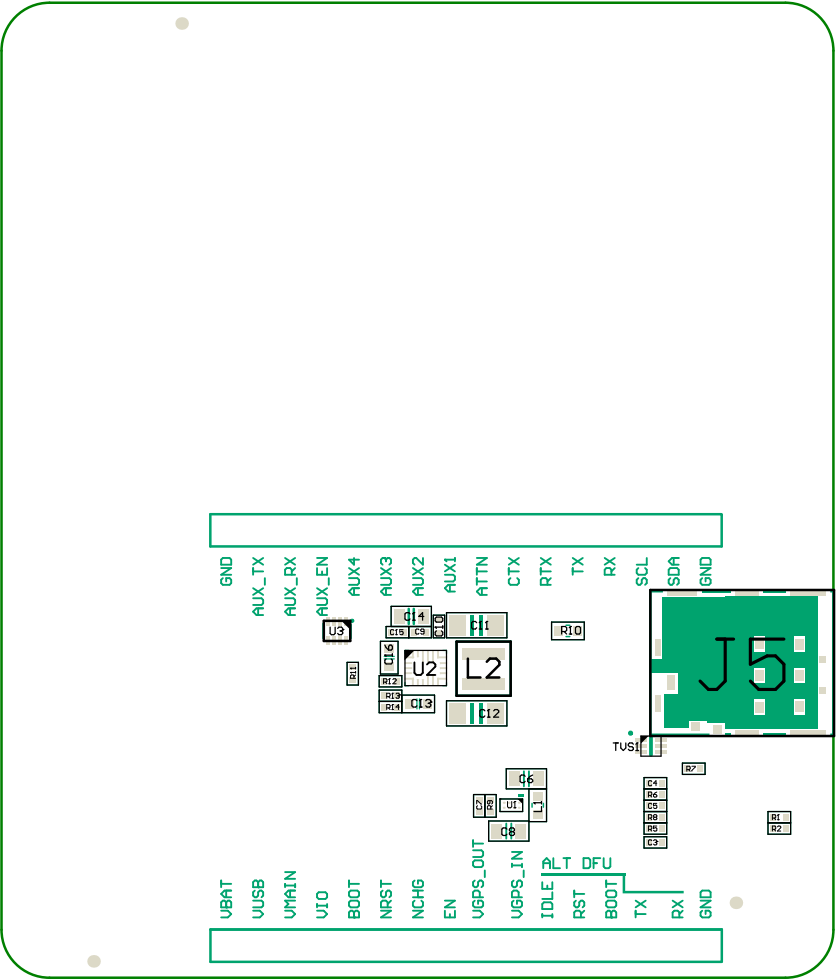
Mount pin headers on bottom!

C

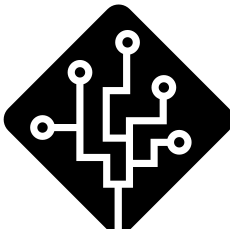


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	SIGNATURE	TITLE: Assembly drawing comments - BOTTOM
AUTHOR:	M. Marsic	
ACCEPTOR:	J. Wiedey	
		BOM NO.: 3000-789-001
DESIGN DOCUMENT NAME: 100426 Notecarrier-XS Rev. 11		FORMAT: A4



Mount pin headers on bottoml

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	AUTHOR:	M. Marsic	
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