

Module name

Notecarrier-X

Index	Description
.....	.....
01	Cover
02	Block Diagram
03	IO Connectors
04	Notecard Connector
05	Power

Design considerations

SOFTWARE CONFIG NOTE:

- Contains important notes for software development.  
- Things like: pin configuration, timing requirements,  
IC configuration etc.

DESIGN NOTE:

- Hardware notes

Board support

FD1

FD2

FD3

FD4

FD5

FD6

TOP

BOTTOM

PCB1

PCB

2200-918

DOC1

HW-E

100379

Title:  
01\_COVER.SchDoc

Project:  
100379\_notecarrier-x.PrjPCB

Company:  
Byte Lab Grupa d.o.o.

Medarska 69/1, 10000 Zagreb, Croatia  
www.byte-lab.com


Author: M. Marsic

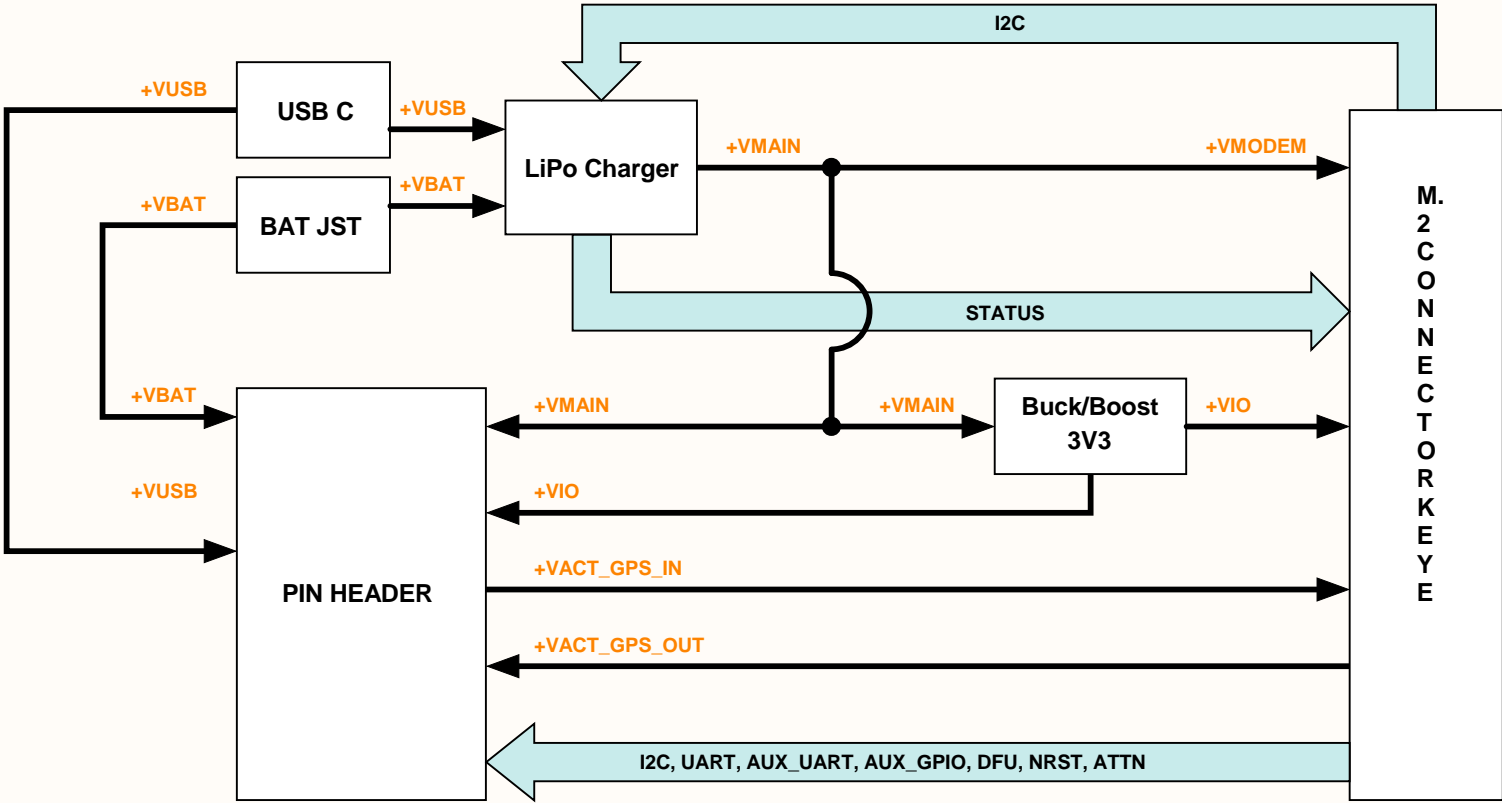
Acceptor: J. Wiedey

Sheet 1 of 5

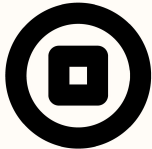
Rev: 11

Format: A4

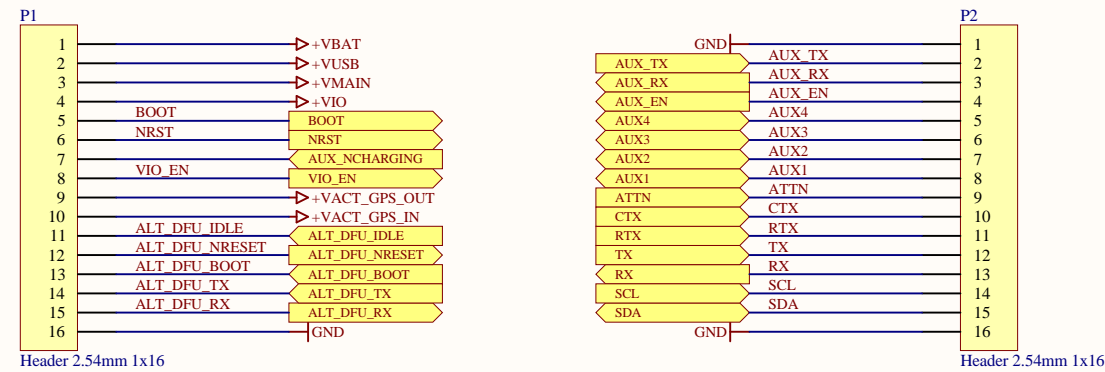




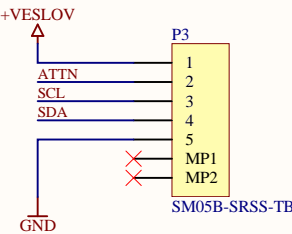
Title: 02_BLOCK-DIAGRAM.SchDoc		
Project: 100379_notecarrier-x.PrjPCB		
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author:	M. Marsic	Acceptor: J. Wiedey
Sheet 2 of 5		Rev: 11      Format: A4



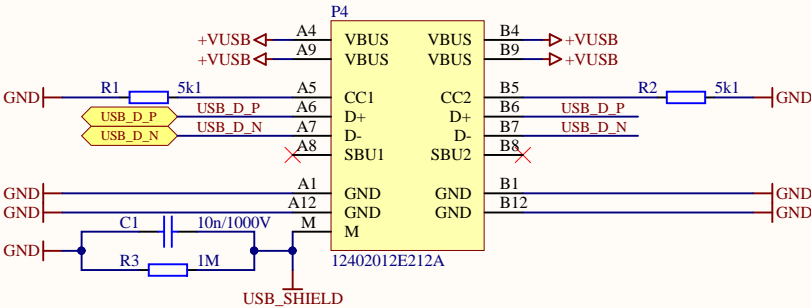
I/O Pin Headers



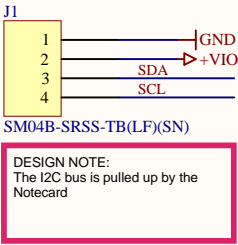
ESLOV Connector



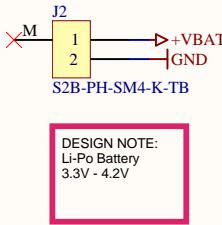
USB C Connector



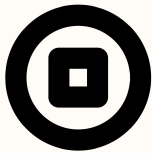
QWIIC Connector



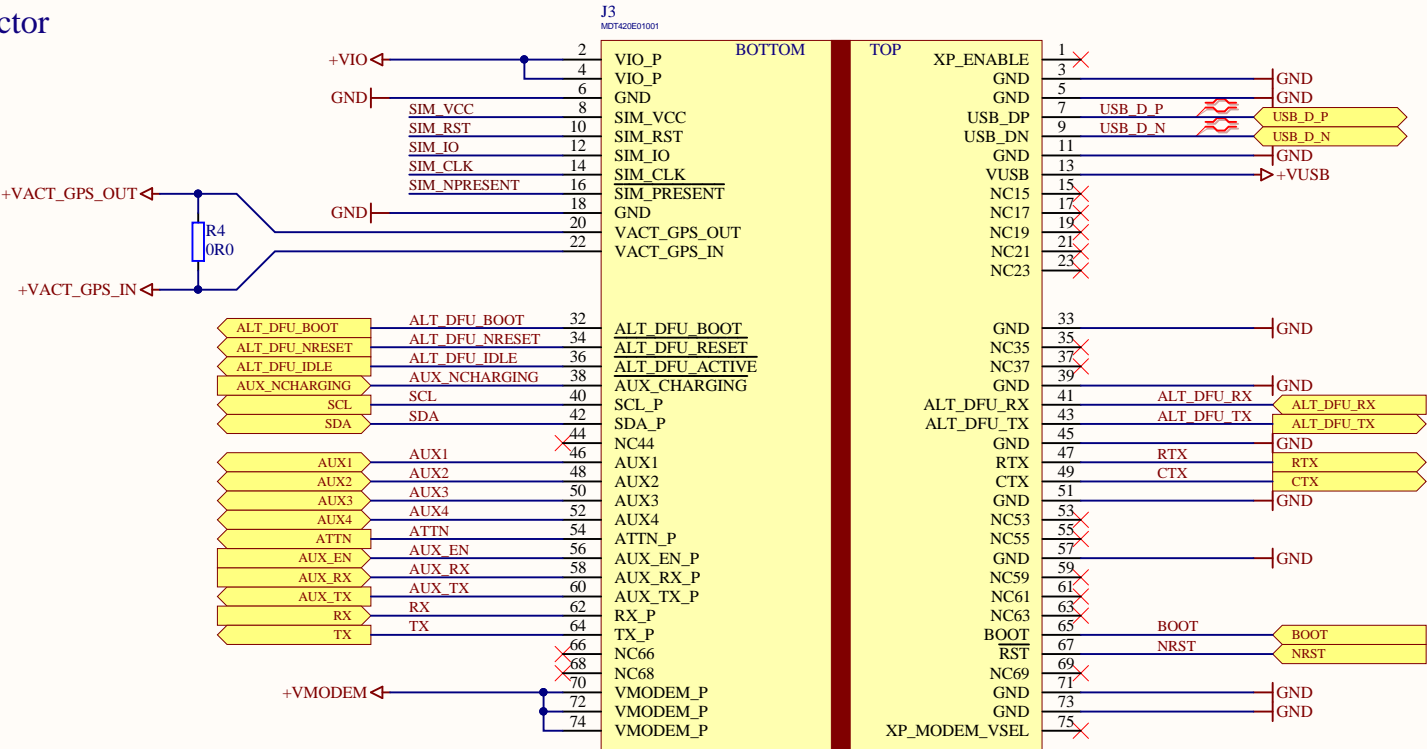
Battery Connector



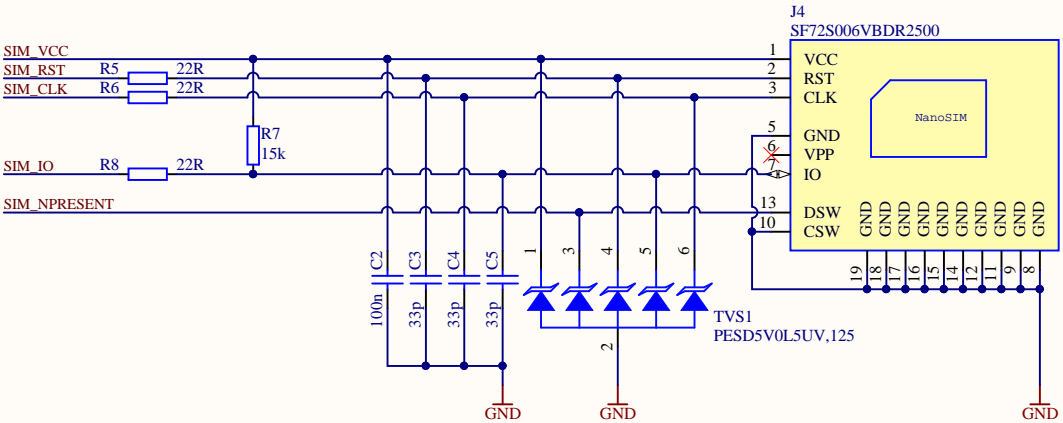
Title: 03_IO-CONNECTION.SchDoc		
Project: 100379_notecarrier-x.PrjPCB		
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author: M. Marsic	Acceptor: J. Wiedey	
Sheet 3 of 5	Rev: 11	Format: A4



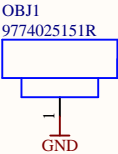
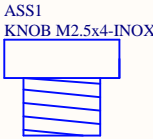
Notecard Connector



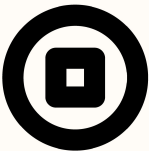
SIM Card



Notecard Support



Title: 04_NOTECARD-CONNECTION.SchDoc		
Project: 100379_notecarrier-x.PrjPCB		
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author: M. Marsic	Acceptor: J. Wiedey	
Sheet 4 of 5	Rev: 11	Format: A4



## Battery Charger w/ Power Path

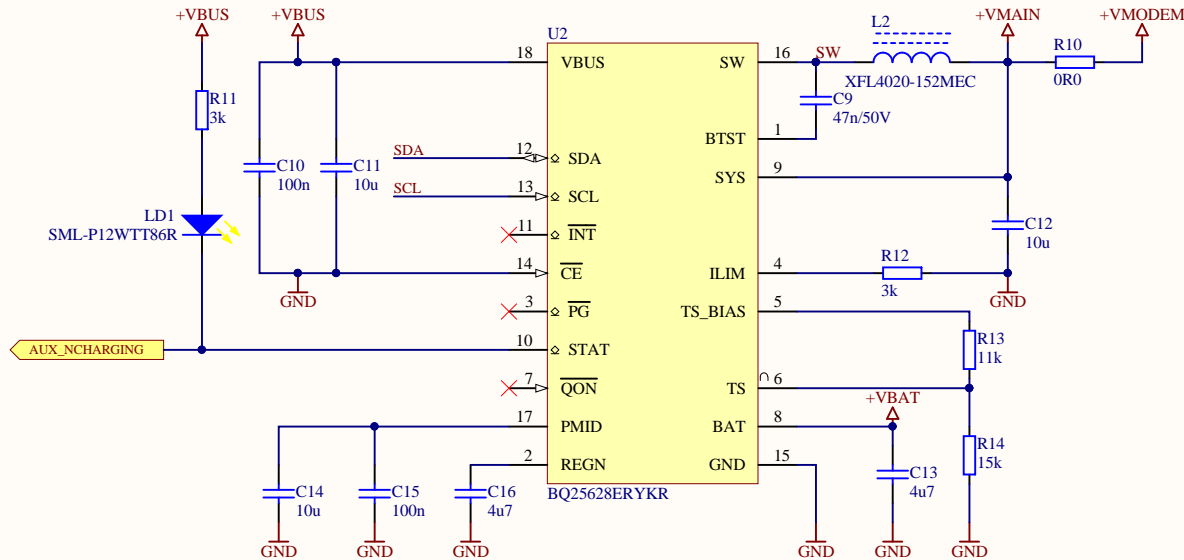
### DESIGN NOTE:

Charger:  
- Default charging voltage: 4.2V  
- Default charging current: 320mA  
- Default termination current: 20mA  
- Default pre-charge current: 20mA  
- No battery thermal protection

### DESIGN NOTE:

Power Path:  
- Input current limit: 3.5A  
- 26mOhm reverse blocking transistor  
- 15mOhm battery transistor

Buck Regulator:  
- 1.5MHz frequency  
- Input Voltage: 5V  
- Output Voltage: 3.3-4.2V  
- Peak current: 2.5A

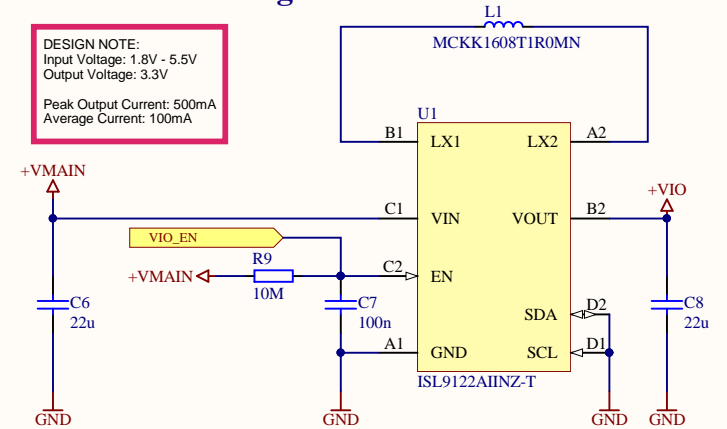


## 3V3 Buck/Boost Regulator

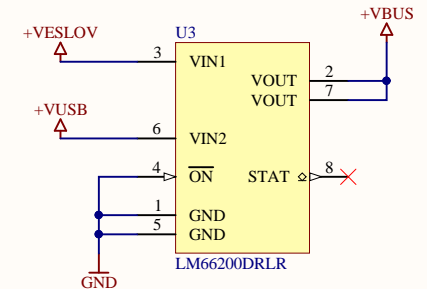
### DESIGN NOTE:

Input Voltage: 1.8V - 5.5V  
Output Voltage: 3.3V

Peak Output Current: 500mA  
Average Current: 100mA



## VBUS ORing



Title: <b>05_POWER.SchDoc</b>			
Project: 100379_notecarrier-x.PrjPCB			
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author:	M. Marsic	Acceptor:	J. Wiedey
Sheet 5 of 5		Rev: 11	Format: A4

