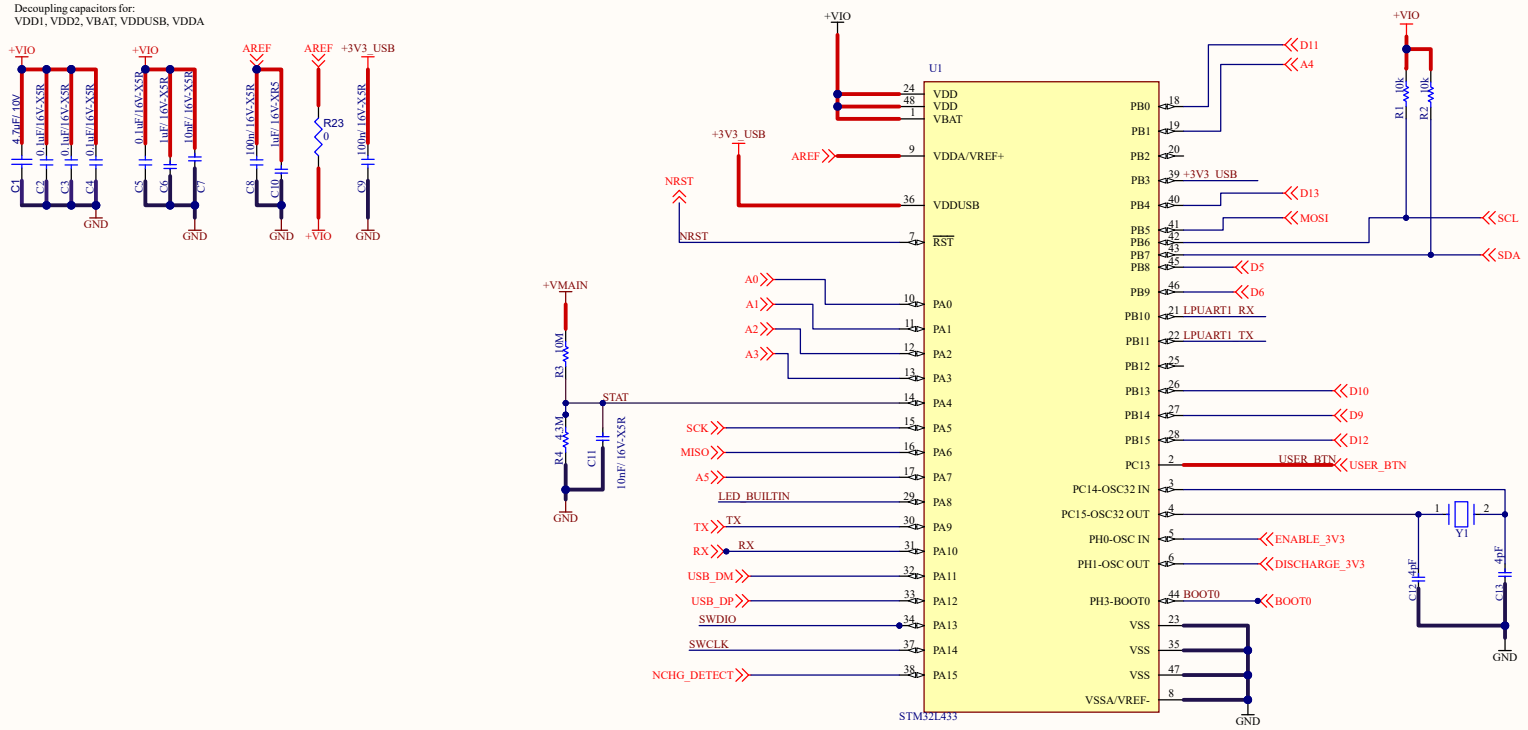
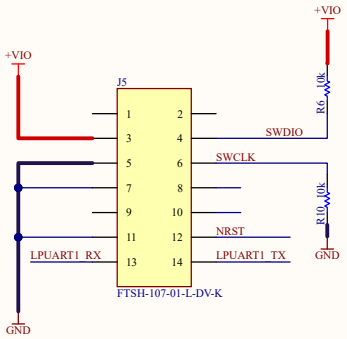
Revision	Date	Author	Description
1	11/01/2023	S.KRAVITZ	First Draft
2	11/14/2023	S.KRAVITZ	Second Draft
3	1/3/2024	S.KRAVITZ	Third Draft - removed VIN, combined ideal diodes, all components on top Copied original project, now on Cygnet Feather v2
4	1/12/2024	S.KRAVITZ	PCB layout complete and ready for review
5	2/15/2024	S.KRAVITZ	Post design review, many changes
6	2/27/2024	S.KRAVITZ	PCB layout complete and ready for review
7	3/18/2024	S.KRAVITZ	Add "do not populate/ mount (DNM)" note to U4.8
8	4/4/2024	S.KRAVITZ	Change PU to PD and vv SWDIO/ SWCLK. Change PNs and footprints for J1, L1
9	4/15/2024	S.KRAVITZ	PU to +VIO change SWDIO
10	6/2/2024 - 7/11/2024	S.KRAVITZ	Various changes after manufacturing review

blues

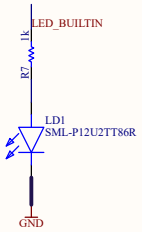
Title: CYGNET REVISION NOTE		
Size: A	Number:	Revision:
Date: 8/29/2024		Sheet of
File: C:\Users\..\CYGNET REVISION v1.2.SchDoc		Drawn By:



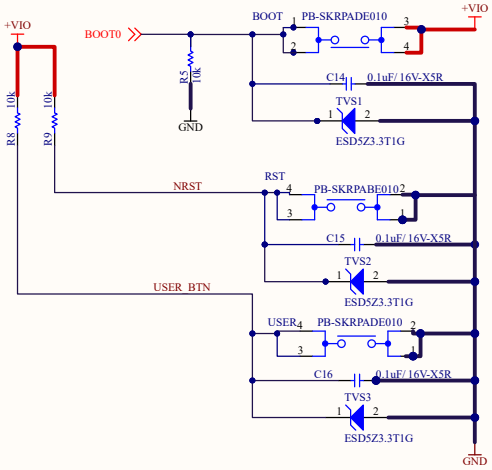
DEBUG CONNECTOR



USER LED (active high)



BUTTONS



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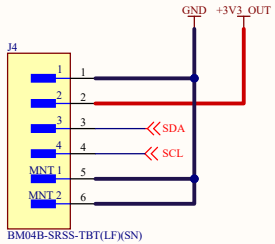

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Date: 8/29/2024		Sheet of
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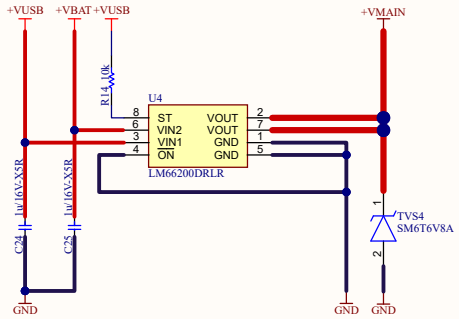
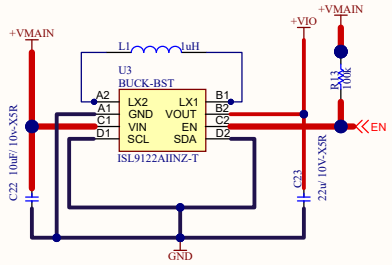
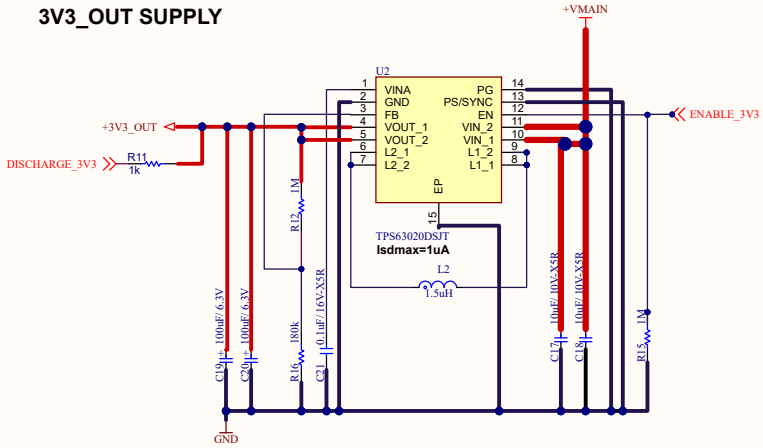
MAIN SUPPLY

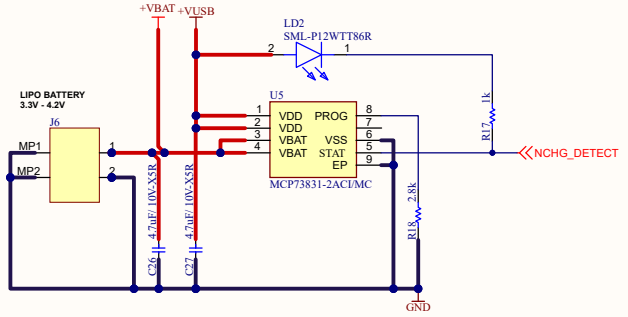




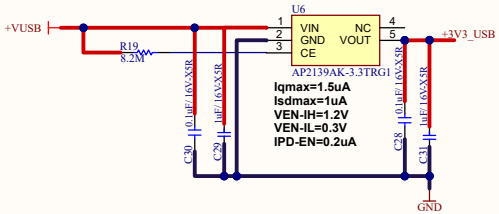
3V3_OUT SUPPLY



BATTERY CHARGER



USB LDO



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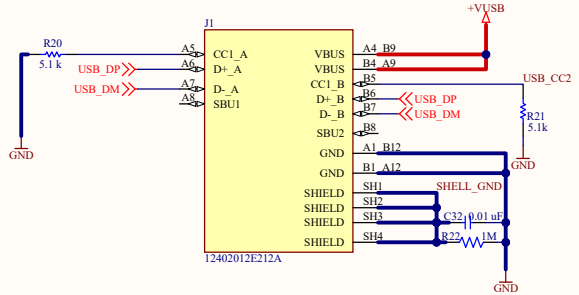
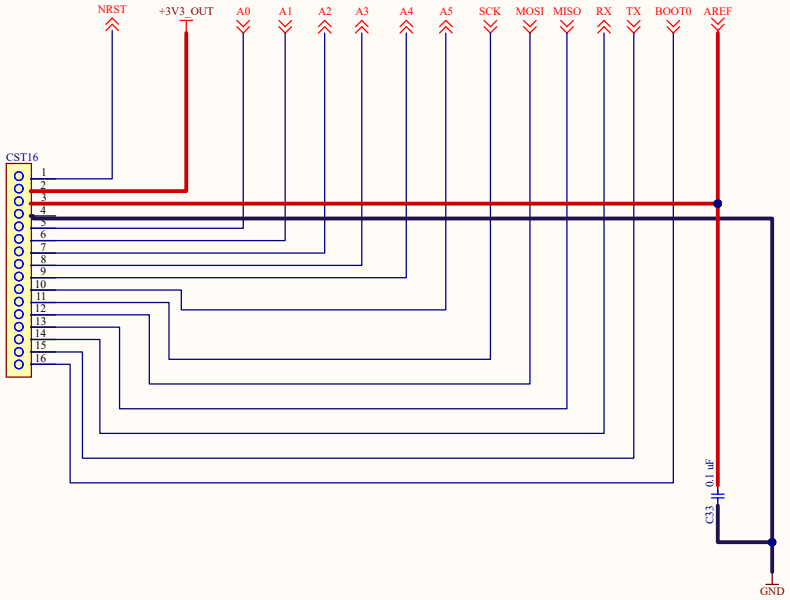
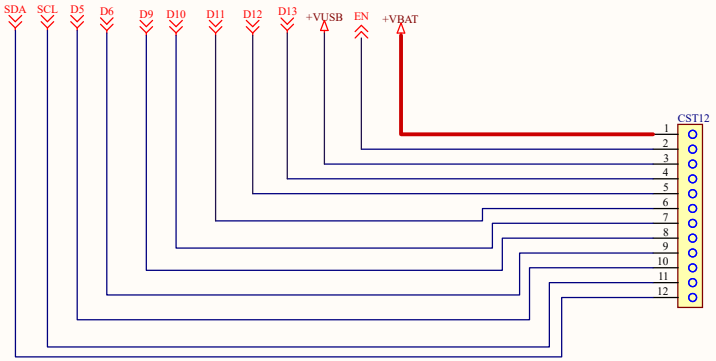
USB-C CONNECTOR







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