

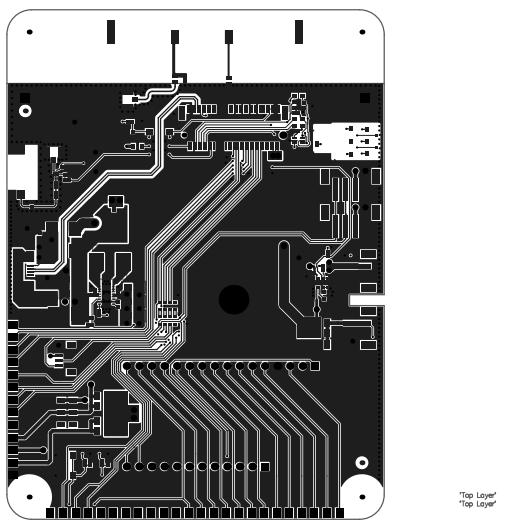
TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

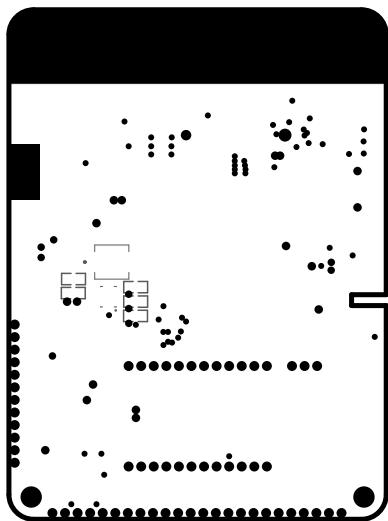
IMPEDANCE CONTROL TABLE					
LAYER	TRACE [MM]	SPACING [MM]	IMPEDANCE SINGLE-ENDED	IMPEDANCE DIFFERENTIAL	TOLERANCE
TOP	0.31	0.45	50 ohm	NA	+/- 10%
TOP	0.26	0.2	NA	90 ohm	+/- 10%

MANUFACTURING SPECIFICATIONS			
BOARD SIZE (XxYxZ)	76.25x103.00x1.6mm	IPC-6012 – IPC-A-600	CLASS 2
BOARD TOLERANCE (X/Y/Z)	+/-0.2 I +/-0.2 I +/-10%	E-TESTING	YES
NO/OF LAYERS	4	UL-MARKING	NO
BASE COPPER OUTSIDE	17um	MICROVIA (hole < 100um)	NO
BASE COPPER INSIDE	35um	BURD VIA	NO
FINISH	ENIG	BURIED VIA	NO
SOLDER COLOR	GREEN	VIA FILL/VIA IN PAD	NO
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.2mm
DELECTRIC MATERIAL	FR4-TG150	MIN TRACE SPACING	0.15mm
IMPEDANCE CONTROL	YES	OUTER LAYER MINTRACE WIDTH	0.2mm
DRILL	12G	INNER LAYER MINTRACE WIDTH	0.6mm

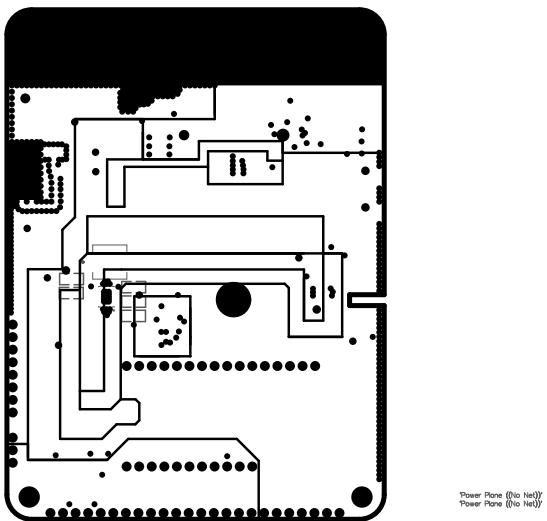
Blues Inc

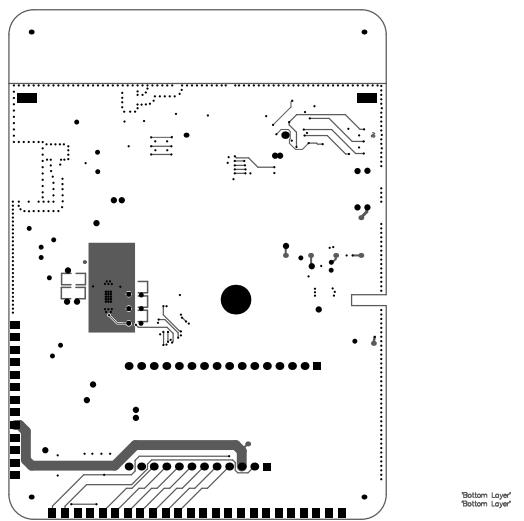
Blues Inc	175V	INNER LAYER MIN. TRACE WIDTH	0.2mm
	Project name		Board name
	Notecarrier 1.7~v6		
	Designed by FAE Technology SpA - Gazzaniga (BG) - Italy - Modified by ToyBuilder Labs		
	Project Code		
Internal Code -		Code -	
Rev.	Size Page A3	Scale 1:1	
Data			

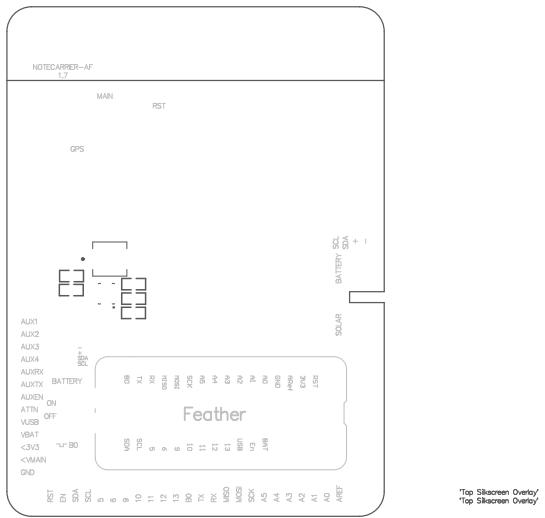


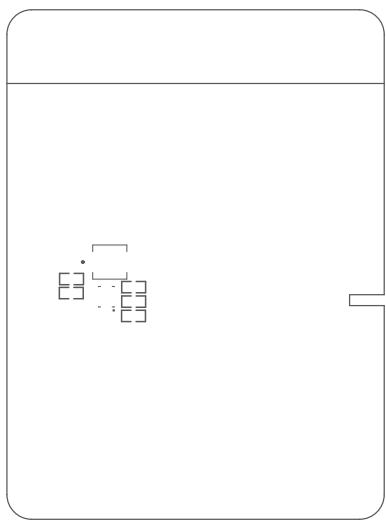


'Ground Plane ((No Net))  
'Ground Plane ((No Net))'

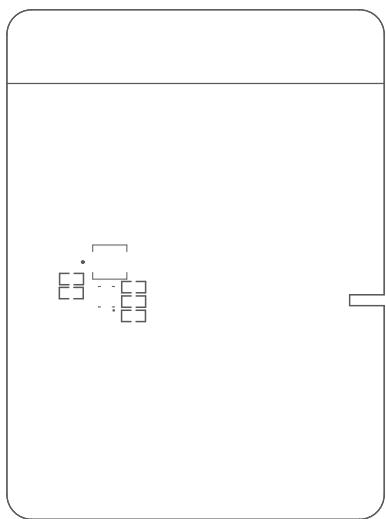








'Bottom Silkscreen Overlay'  
'Bottom Silkscreen Overlay'



'Board Outline'  
'Board Outline'

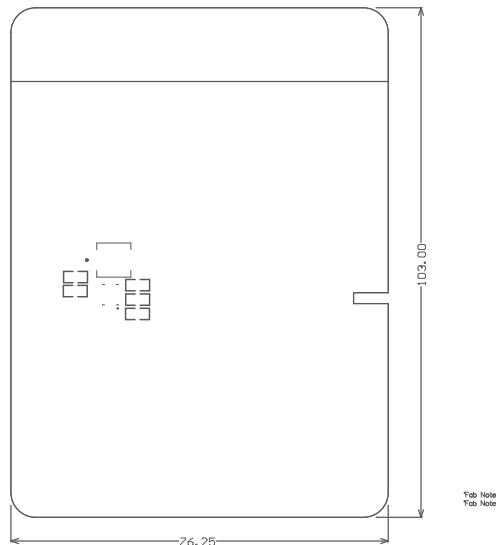
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TOP and BOTTOM thickness layer shown in the image above contains +20um (from IPC-A-600 Class 2) of plating

1000

MANUFACTURING SPECIFICATIONS			
BOARD SIZE (XxYxZ)	76.25x10.0x1.6mm	IPC-6012 - IPC-A-600	CLASS 2
BOARD TOLERANCE (XxYz)	+/-0.2 I +/-.2 I +/-10%	E-TESTING	YES
NO/LAYERS	4	UL-MARKING	NO
BASE COPPER OUTSIDE	17um	MICROVIA (hole < 100um)	NO
BASE COPPER INSIDE	35um	BUND VIA	NO
FINISH	ENIG	BURIED VIA	NO
SOLDER COLOR	GREEN	VIA FILL/VIA IN PAD	NO
SILKSCREEN COLOR	WHITE	MIN. VIA SIZE	0.2mm
Dielectric Material	FR4-TG150	MNLTRACE SPACING	0.15mm
Impedance Control	YES	OUTER LAYER MIN TRACE WIDTH	0.2mm
CtI	175W	INNER LAYER MIN TRACE WIDTH	0.2mm

V INNER LAY

### RACE WIDTH

Designed by FAE Technology SpA - Gazzaniga (BG) - Italy - Modified by ToyBuilder Labs

Project Code

Internal Code =  Code

Rev. Size

1

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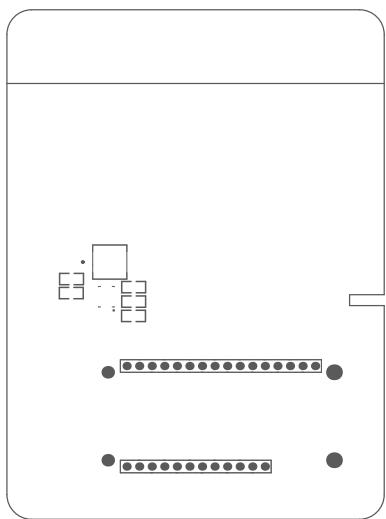
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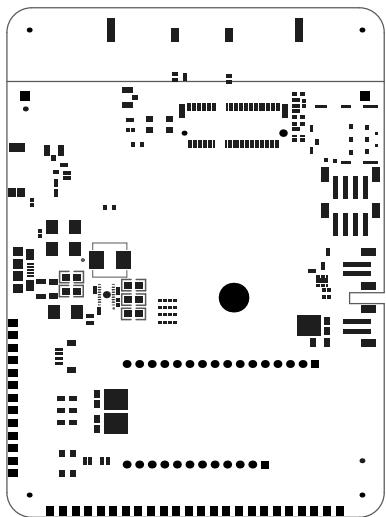
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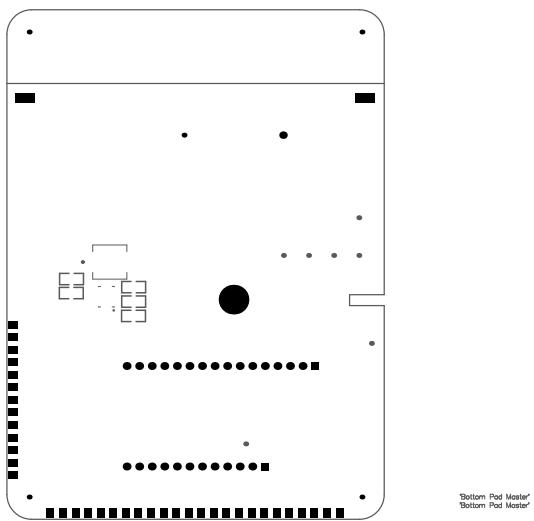
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Mechanical 13  
Mechanical 13

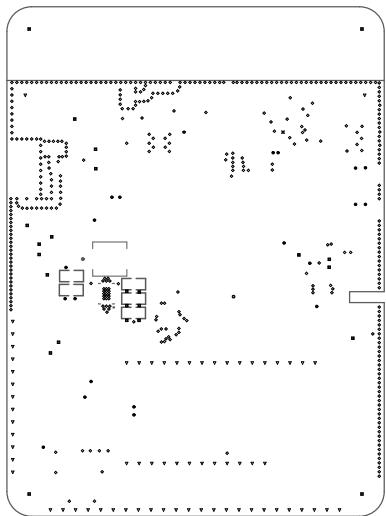


"Top Pod Master"  
"Top Pod Master"

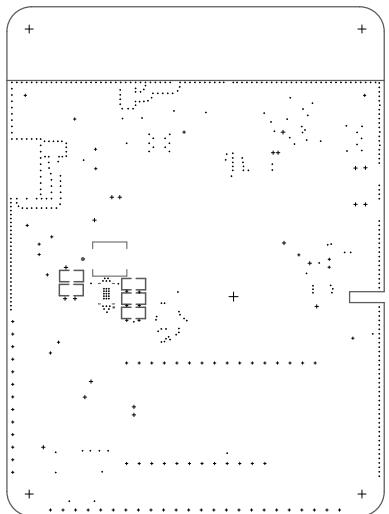


'Bottom Pod Master'  
'Bottom Pod Master'

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)	Hole Length	Routed Path Length
✖	1	43.31mil <1.100mm>	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c100hn10				-	-
✖	1	62.99mil <1.600mm>	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c152hn160				-	-
✖	1	145.67mil <3.700mm>	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c600h370z152				-	-
✖	4	129.92mil <3.300mm>	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c100hn330z10-0	0.00mil <0.000mm>	0.00mil <0.000mm>		-	-
■	20	19.69mil <0.500mm>	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v100n50				-	-
●	20	28.00mil <0.711mm>	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v127n71				-	-
▽	67	39.37mil <1.000mm>	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	v127n71				-	-
◇	396	7.87mil <0.200mm>	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	(Mixed)				-	-
510 Total													



Drill Drawing For {Top Layer - Bottom Layer}  
Drill Drawing For {Top Layer - Bottom Layer}



Drill Guide For (Top Layer - Bottom Layer)  
Drill Guide For (Top Layer - Bottom Layer)