

Module name

Notecarrier-XP

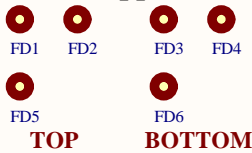
| Index | Description |
|-------|--------------------|
| | |
| 01 | Cover |
| 02 | Block Diagram |
| 03 | IO Connectors |
| 04 | Notecard Connector |
| 05 | Power |

Design considerations

SOFTWARE CONFIG NOTE:
- Contains important notes for software development.
- Things like: pin configuration, timing requirements,
IC configuration etc.

DESIGN NOTE:
- Hardware notes

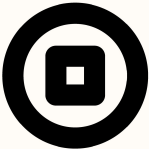
Board support

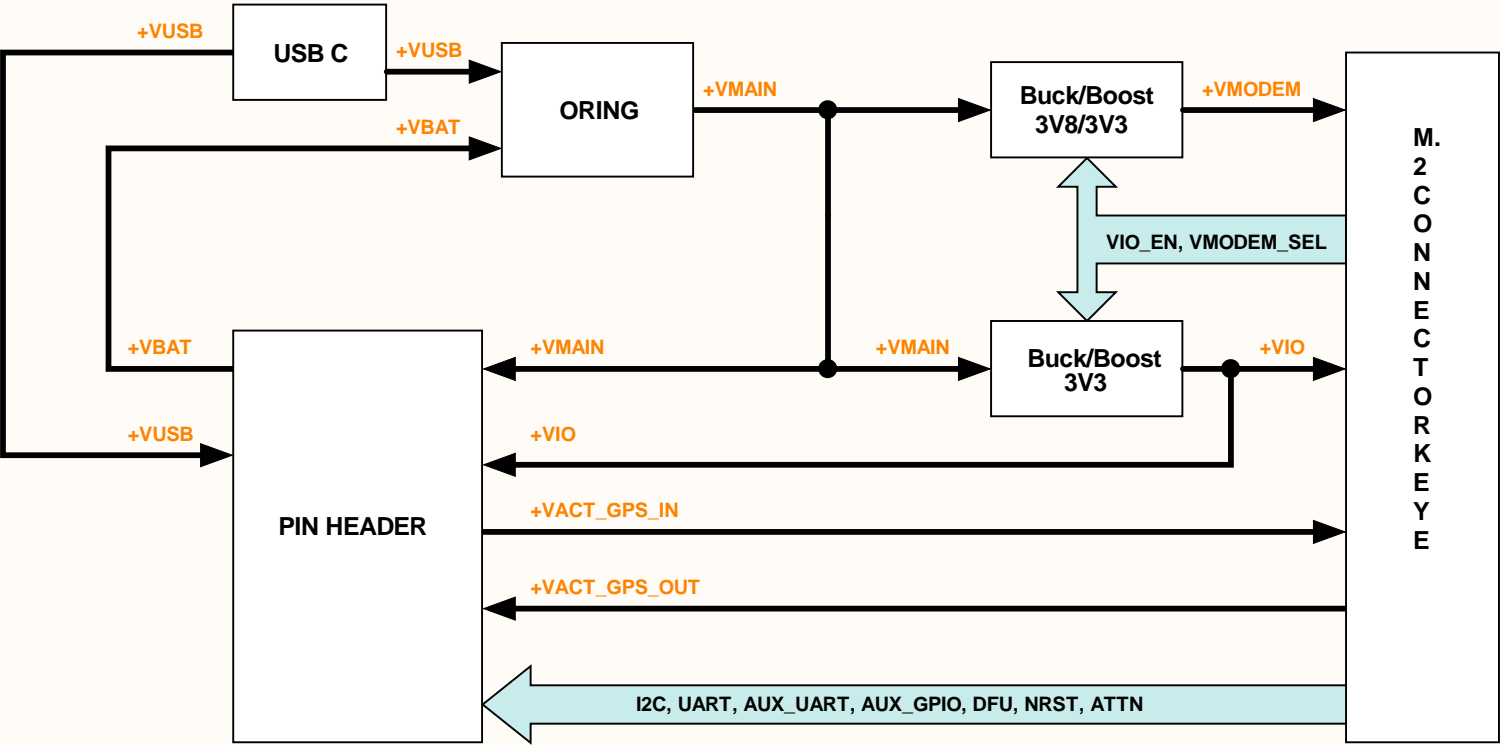


PCB1
PCB
2200-925

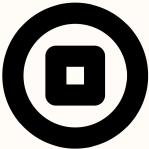
DOC1
HW-E
100380

| | | | |
|--|--|--|------------|
| Title: 01_COVER.SchDoc | | | |
| Project: 100380_notecarrier-xp.PrjPCB | | | |
| Company: Byte Lab Grupa d.o.o. | | Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com | |
| Author: M. Marsic | | Acceptor: J. Wiedey | |
| Sheet 1 of 5 | | Rev: 13 | Format: A4 |

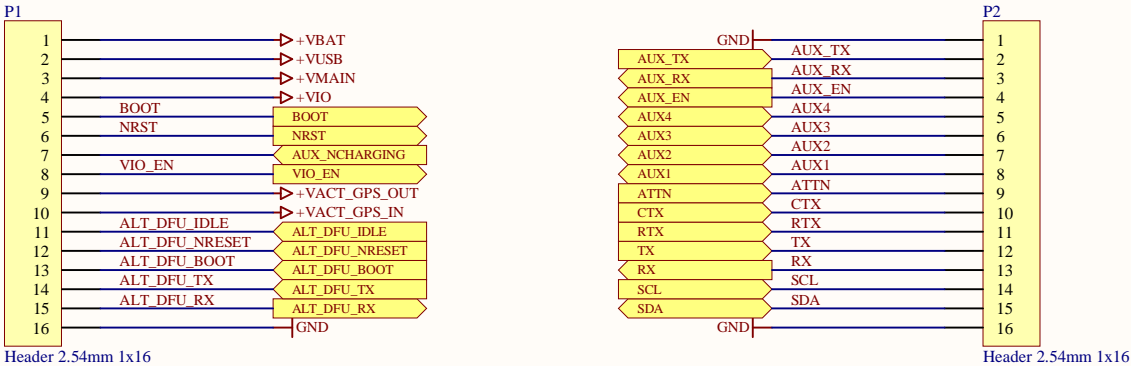




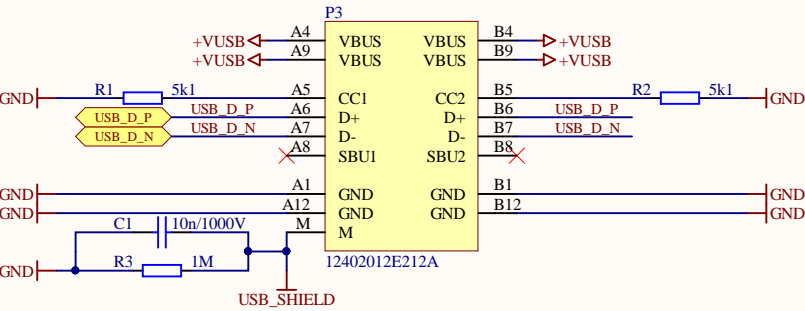
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|--|-----------|--|------------|
| Title: 02_BLOCK-DIAGRAM.SchDoc | | | |
| Project: 100380_notecarrier-xp.PrjPCB | | | |
| Company: Byte Lab Grupa d.o.o. | | Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com | |
| Author: | M. Marsic | Acceptor: | J. Wiedey |
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I/O Pin Headers & Castellations



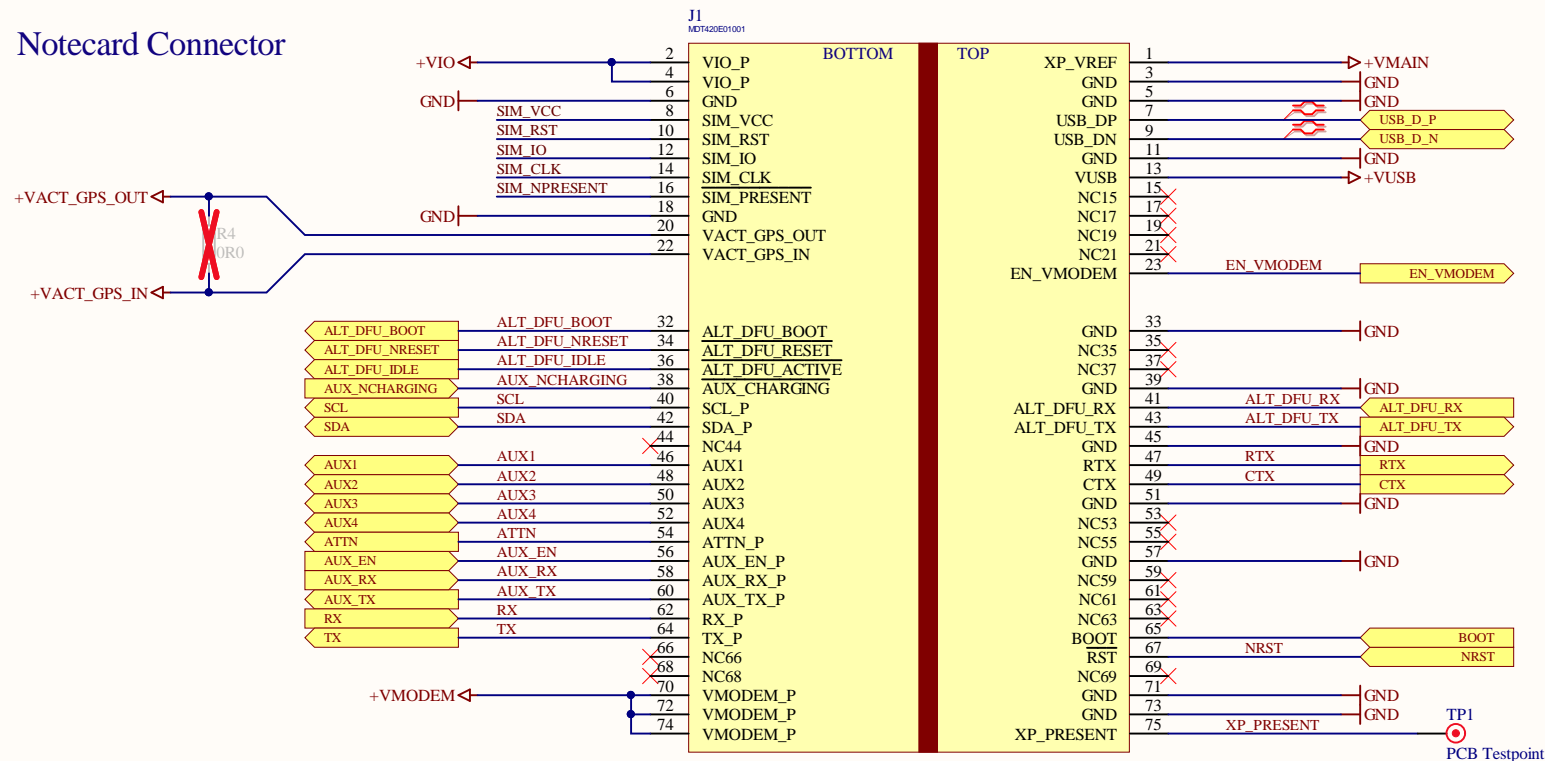
USB C Connector



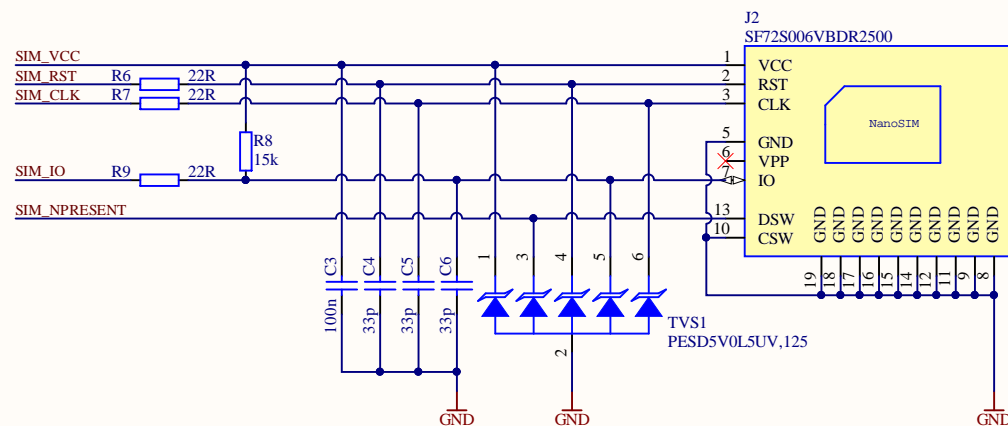
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|--|---------------------|--|
| Title: 03_IO-CONNECTION.SchDoc | | |
| Project: 100380_notecarrier-xp.PrjPCB | | |
| Company: Byte Lab Grupa d.o.o. | | Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com |
| Author: M. Marsic | Acceptor: J. Wiedey | |
| Sheet 3 of 5 | Rev: 13 | Format: A4 |



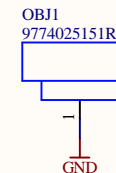
Notecard Connector



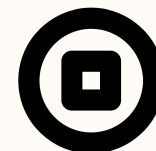
SIM Card



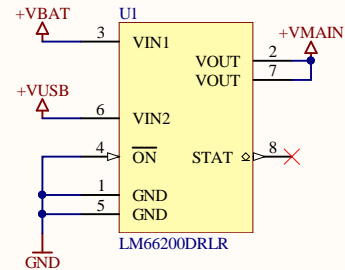
Notecard Support



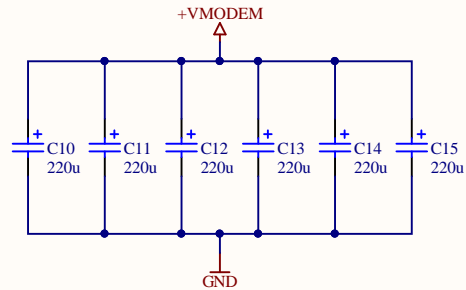
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| Title: 04_NOTECARD-CONNECTION.SchDoc | | |
| Project: 100380_notecarrier-xp.PrjPCB | | |
| Company: Byte Lab Grupa d.o.o. | | Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com |
| Author: M. Marsic | Acceptor: J. Wiedey | |
| Sheet 4 of 5 | Rev: 13 | Format: A4 |



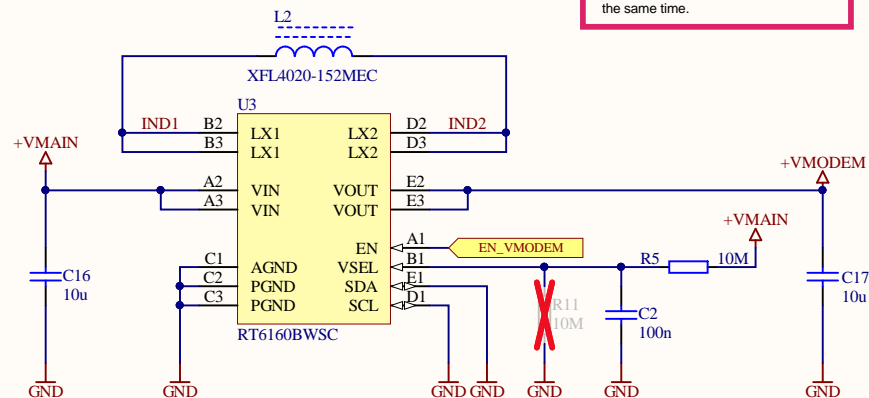
Power ORing



2G TX Capacitor Bank



Modem Buck/Boost Regulator



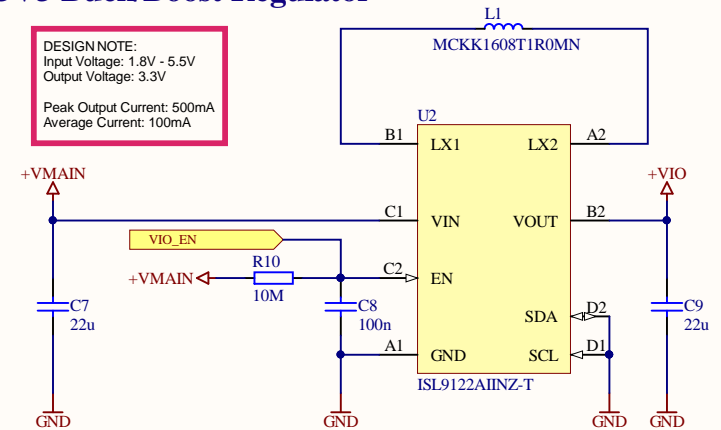
DESIGN NOTE:
R11 and R5 shall not be mounted at
the same time.

DESIGN NOTE:
Input Voltage: 2.2V - 5.5V
Output Voltage:
3.3V; VSEL = L
3.85V; VSEL = H

Peak Output Current: 2A
Average Current: 250mA

3V3 Buck/Boost Regulator

DESIGN NOTE:
Input Voltage: 1.8V - 5.5V
Output Voltage: 3.3V
Peak Output Current: 500mA
Average Current: 100mA



Title:
05_POWER.SchDoc

Project:
100380_notecarrier-xp.PrjPCB

Company:
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www.byte-lab.com

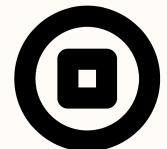
Author: M. Marsic

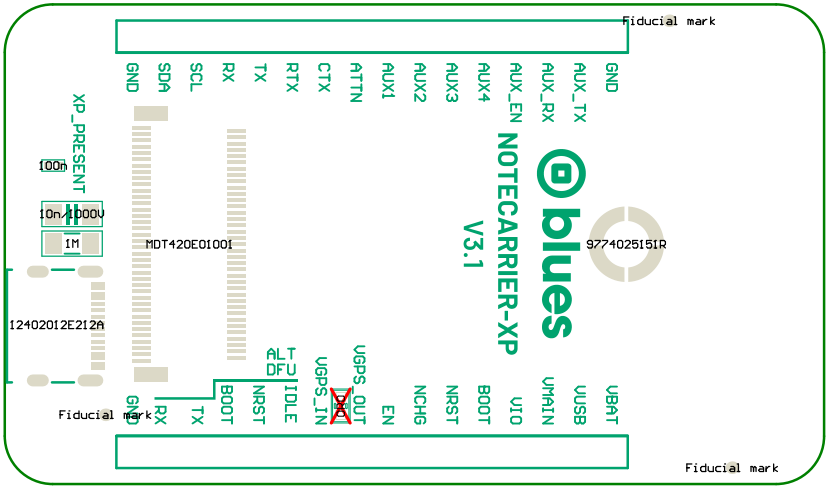
Acceptor: J. Wiedey

Sheet 5 of 5

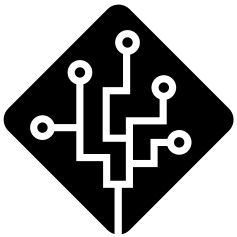
Rev: 13

Format: A4



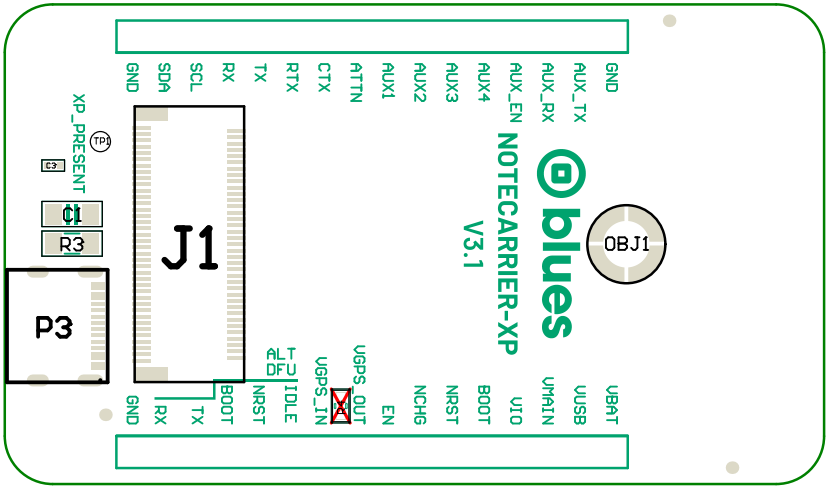


Mount pin headers on bottom!

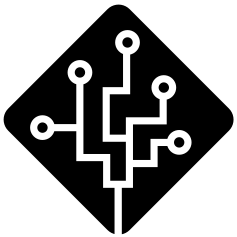


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| | | |
|--|-----------|---|
| | SIGNATURE | TITLE: Assembly drawing comments - TOP |
| AUTHOR: | M. Marsic | |
| ACCEPTOR: | J. Wiedey | |
| | | BOM NO.: 3000-808-001 |
| DESIGN DOCUMENT NAME: 100380 Notecarrier-XP Rev. 13 | | FORMAT: A4 |

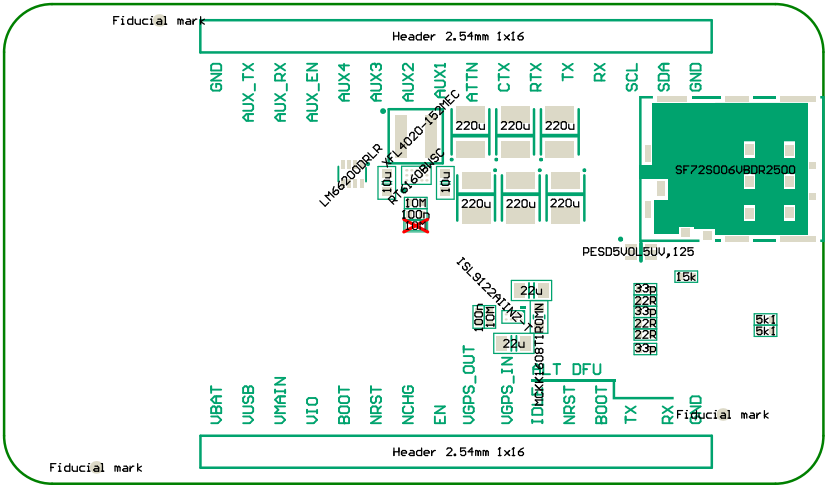


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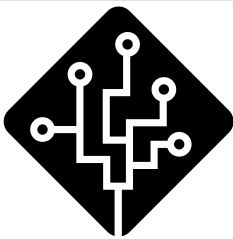


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| AUTHOR: | M. Marsic | |
| ACCEPTOR: | J. Wiedey | |
| | | BOM NO.: 3000-808-001 |
| DESIGN DOCUMENT NAME: 100380 Notecarrier-XP Rev. 13 | | FORMAT: A4 |

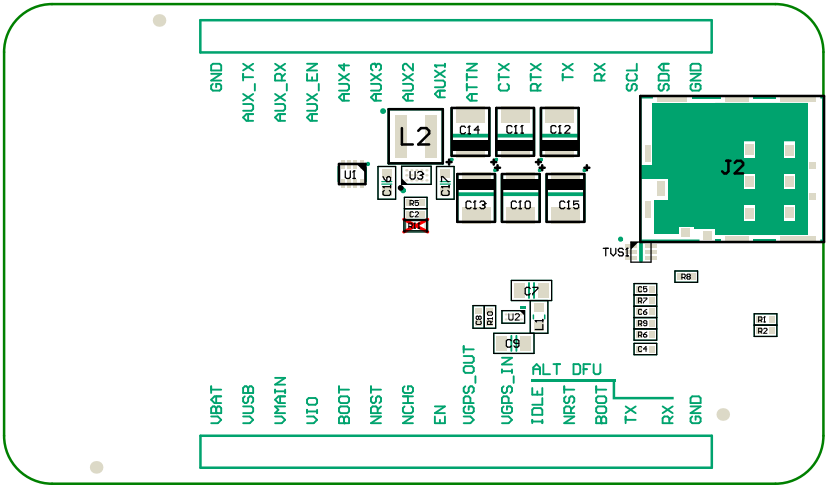


Mount pin headers on bottom!

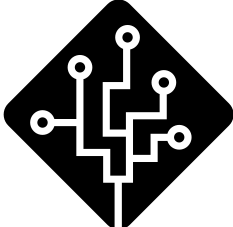


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| | | |
|---|------------------|---|
| | SIGNATURE | TITLE: Assembly drawing comments - BOTTOM |
| AUTHOR: | M. Marsic | |
| ACCEPTOR: | J. Wiedey | |
| | | BOM NO.: 3000-808-001 |
| DESIGN DOCUMENT NAME: 100380 Notecarrier-XP Rev. 13 | | FORMAT: A4 |



Mount pin headers on bottoml

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|--|-----------|-------------------------------|---|--------------|
|  Byte Lab Grupa d.o.o. Medarska 69/1 10000 Zagreb Croatia | | SIGNATURE | Asse mbly drawing designators - BOTTOM | |
| | AUTHOR: | M. Marsic | | |
| | ACCEPTOR: | J. Wiedey | | |
| | | | BOM NO.: | 3000-808-001 |
| DESIGN DOCUMENT NAME: | | 100380 Notecarrier-XP Rev. 13 | | |
| | | FORMAT: A4 | | |