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Module name

Notecarrier F

Index	Description
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02 Block diagram

03 Notecard connection

04 Feather connection

05 I/O

06 Power - Input

07 Power - Rails

REVISION BLOCK

[illegible]

LIST OF AVAILABLE VARIANTS

# VAR	Variant name	Description

REVISION FORMAT EXPLANATION

-	-	202y-mm-dd	Development phase, date only		-	
10	-	202y-mm-dd	Prototyping phase, revision numeric		3000-xxx-xxx	
-	A	202y-mm-dd	Production phase, revision alphanumeric		3000-xxx-xxx	

Design considerations

SOFTWARE CONFIG NOTE:

- Contains important notes for software development.
- Things like: pin configuration, timing requirements, IC configuration etc.

DESIGN NOTE:
- Hardware notes

Board support

FD1 FD2 FD5 FD6
FD3 FD4 FD7 FD8
TOP BOTTOM

DOC1

HW-E

100275

PCB1

PCB

2200-814

Title:
01_COVER.SchDoc

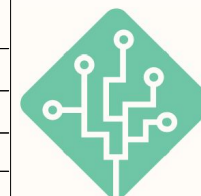
Project:	100275 NOTECARRIER-F.PrjPCB
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Company:	Medarska 69/1, 10000 Zagreb, Croatia
Byte Lab Grupa d.o.o.	www.byte-lab.com

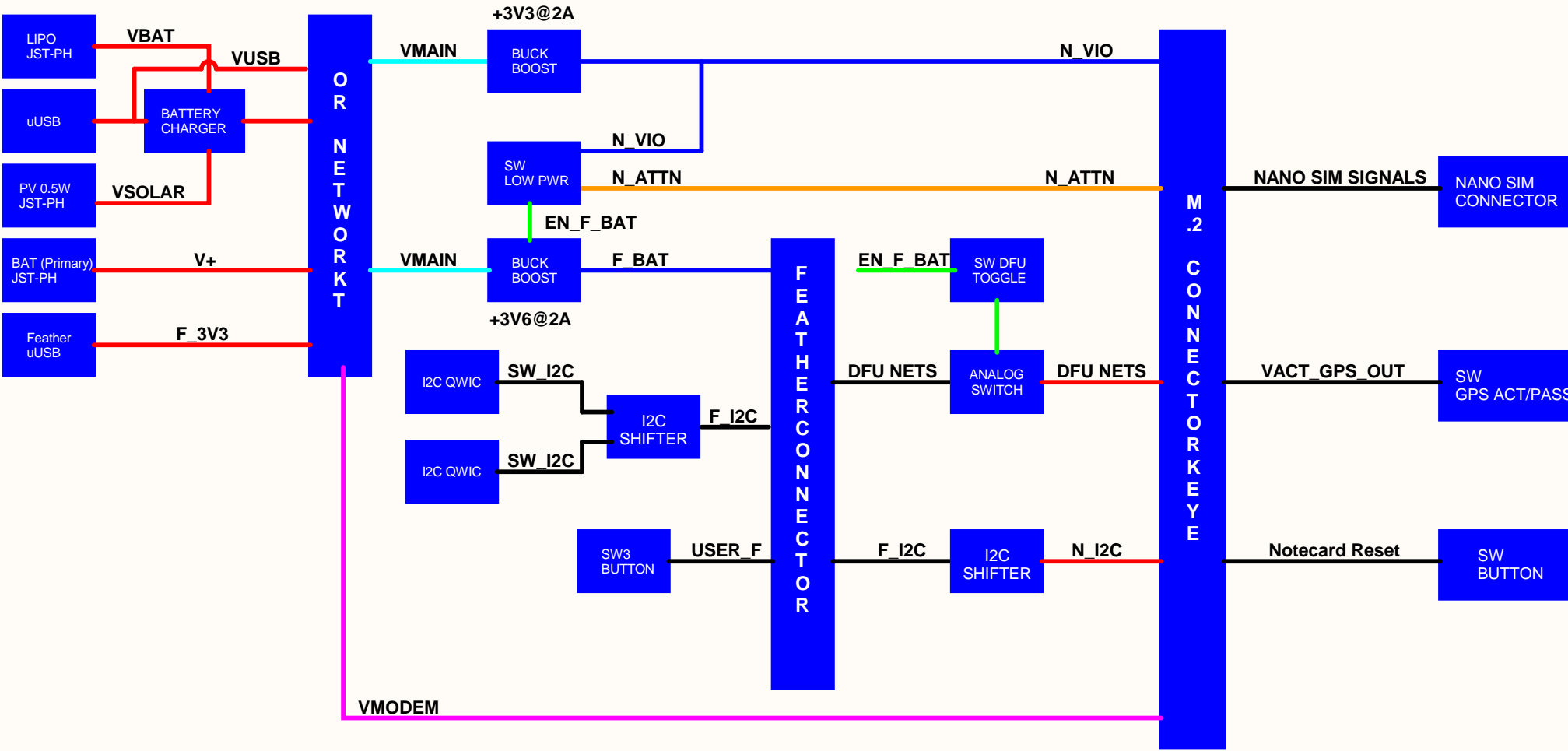
Author: M. Hamin


Acceptor:

T. Zvonc

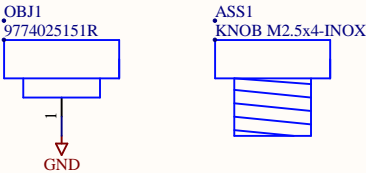


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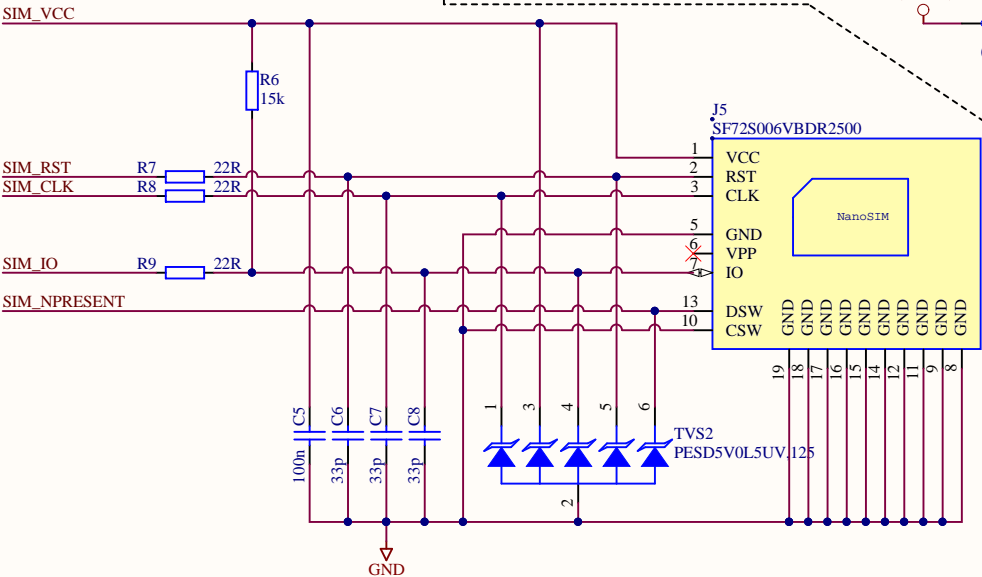


Title: 02_BLOCK-DIAGRAM.SchDoc			
Project: 100275_NOTECARRIER-F.PrjPCB			
Company: Byte Lab Grupa d.o.o.		Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author: M. Hamin	Acceptor: T. Zvonc		
Sheet 2 of 7	Rev: 11	Format: A4	

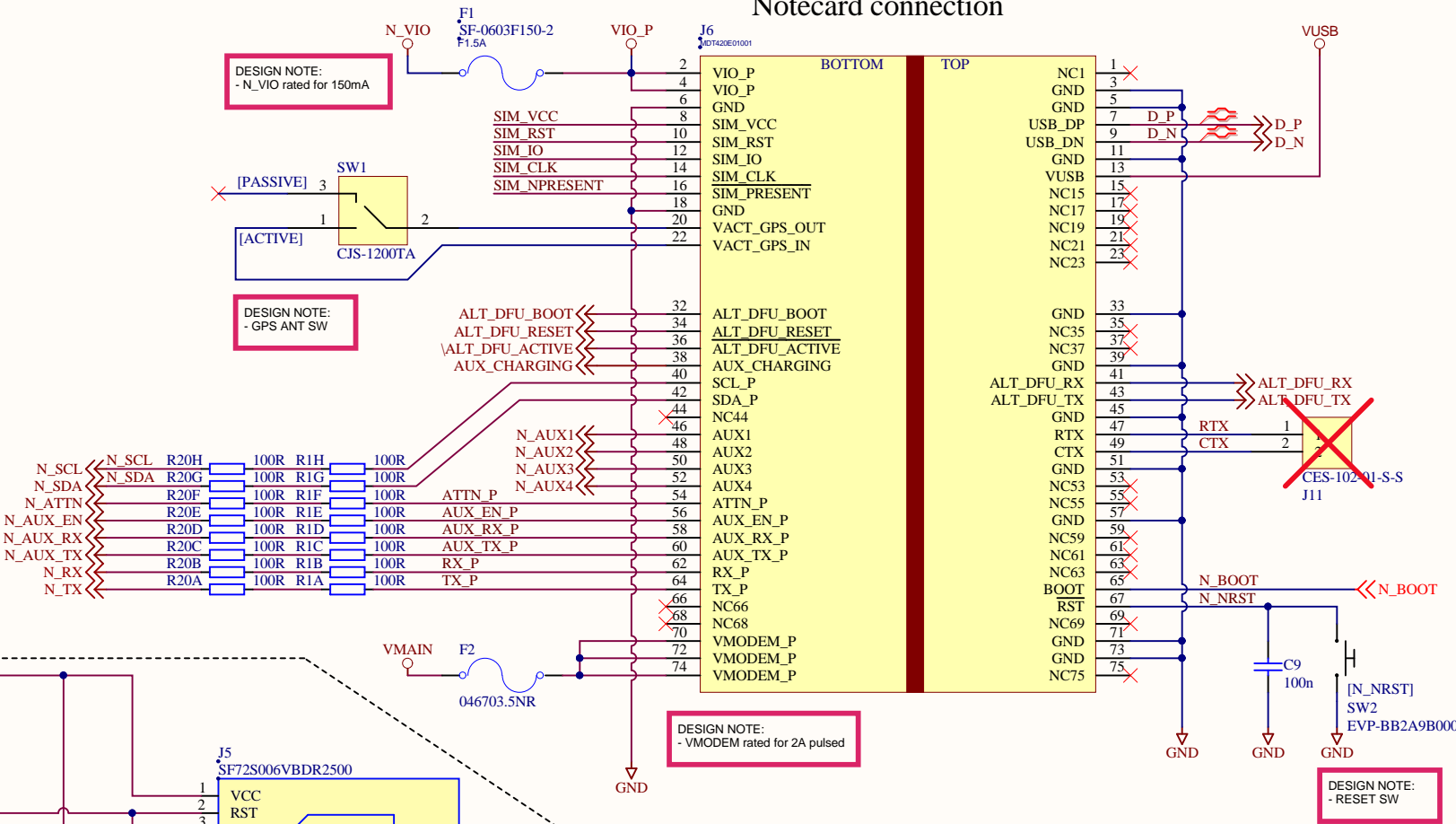
Notecard Fasteners



SIM Card socket



Notecard connection



Title:
03_NOTECARD-CONNECTION.SchDoc

Project:
100275_NOTECARRIER-F.PrjPCB

Company:
Byte Lab Grupa d.o.o.

Medarska 69/1, 10000 Zagreb, Croatia
www.byte-lab.com

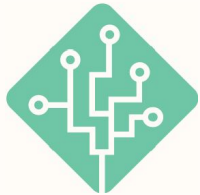
Author: M. Hamin

Acceptor: T. Zvonc

Sheet 3 of 7

Rev: 11

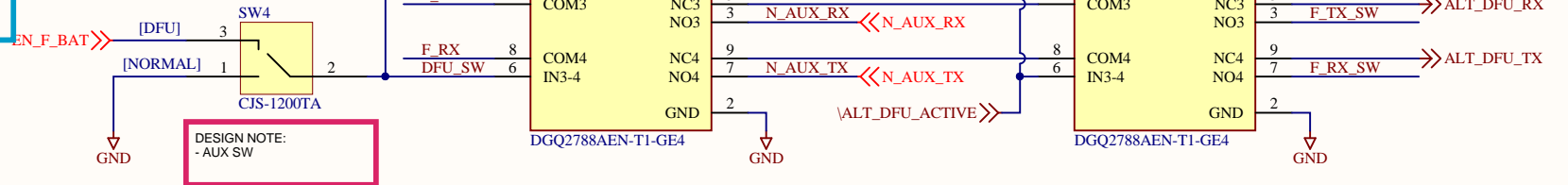
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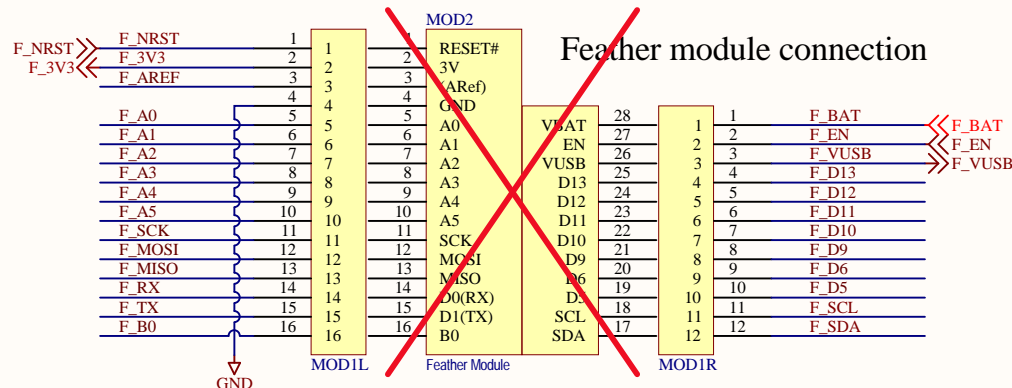
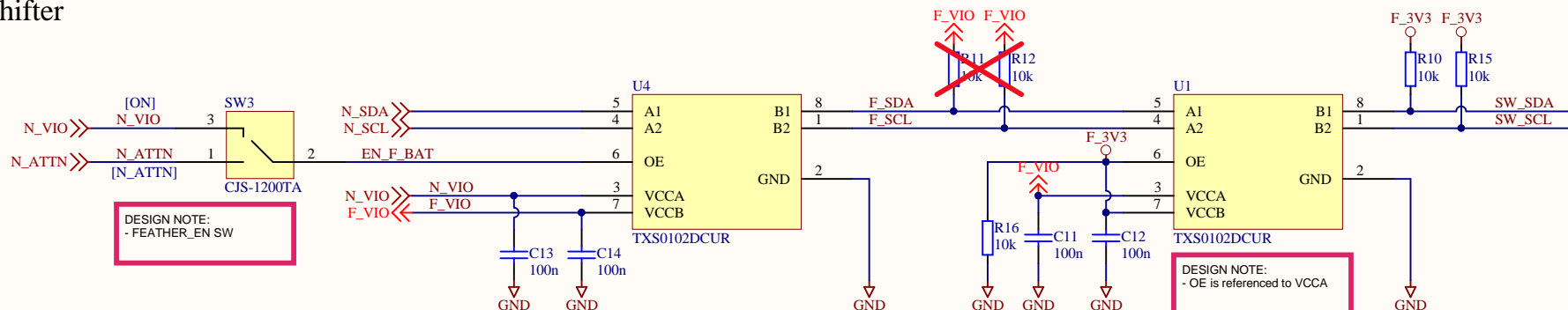
DFU Enable

SOFTWARE CONFIG NOTE:
- Flick switch towards "ON" to enable outboard DFU

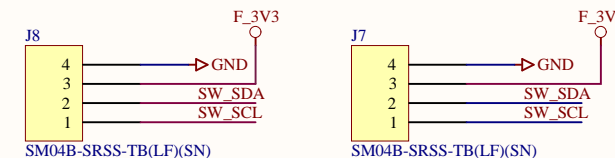
SOFTWARE CONFIG NOTE:
Truth table:
IN1-2, IN3-4
Logic 0 = NC is True/High, DFU = N
Logic 1 = NO is True/High, DFU = Y



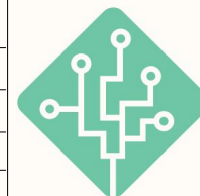
Logic level shifter

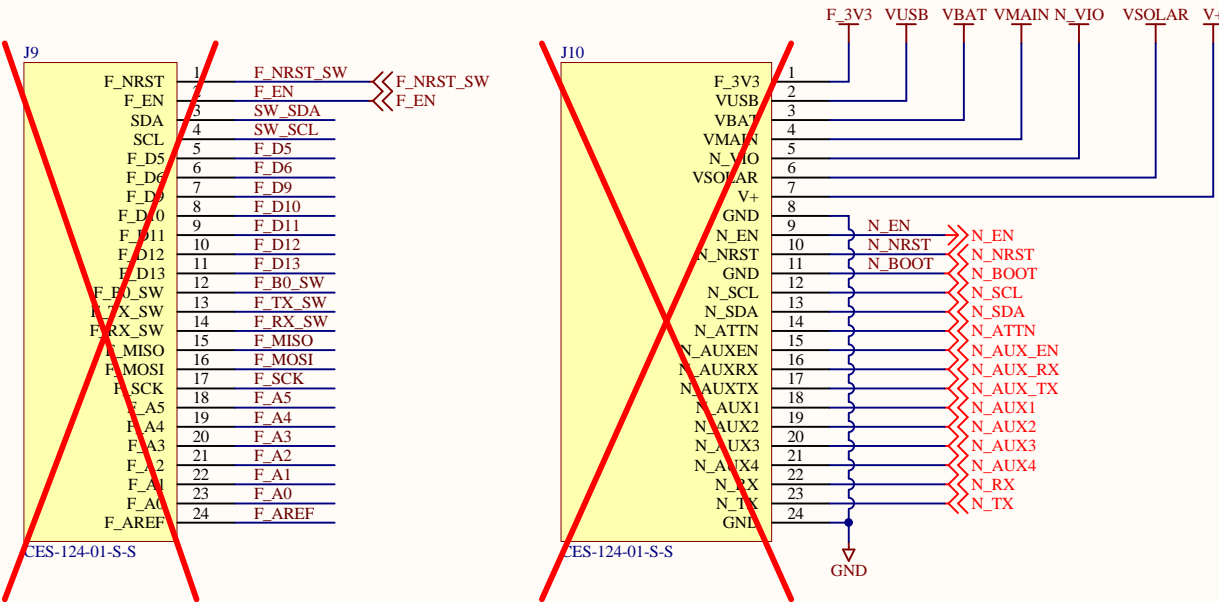


QWIIIC Connection

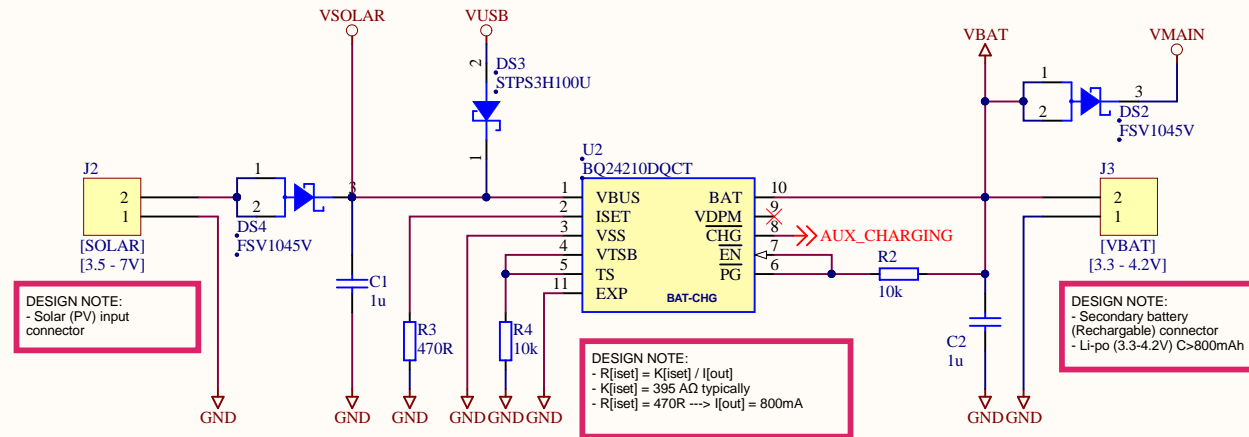


Title: 04_FEATHER-CONNECTION.SchDoc	
Project: 100275_NOTECARRIER-F.PjPCB	
Company: Byte Lab Grupa d.o.o.	
Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author: M. Hamin	Acceptor: T. Zvonc
Sheet 4 of 7	Rev: 11 Format: A4

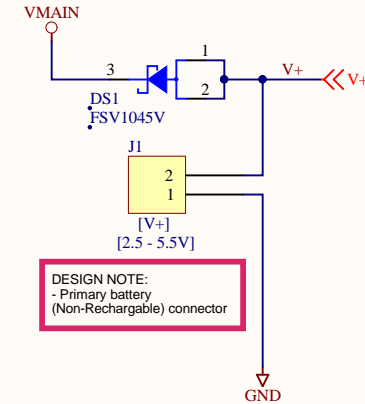




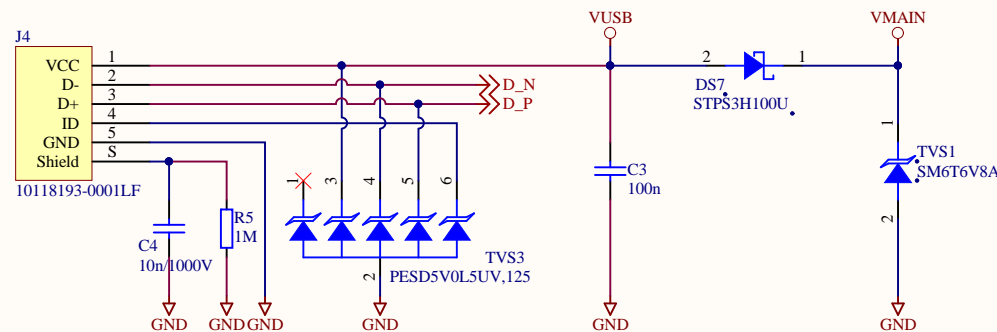
Battery charger



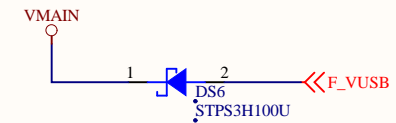
Non-Rechargeable battery



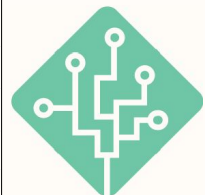
Notecard power input (USB)



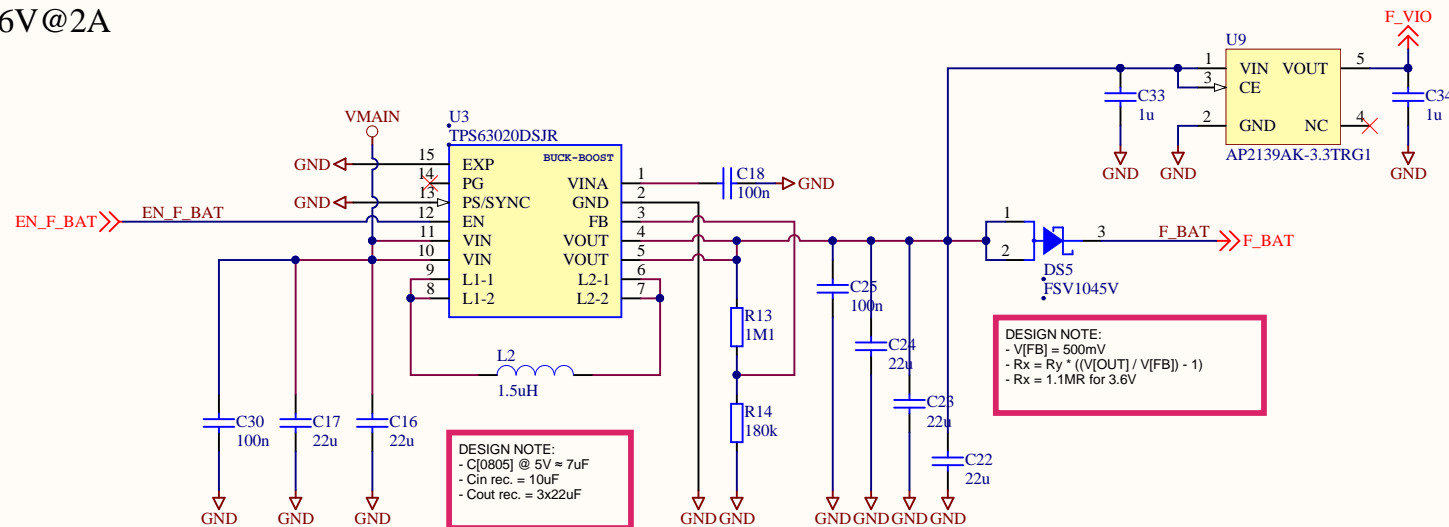
Feather power input (USB)



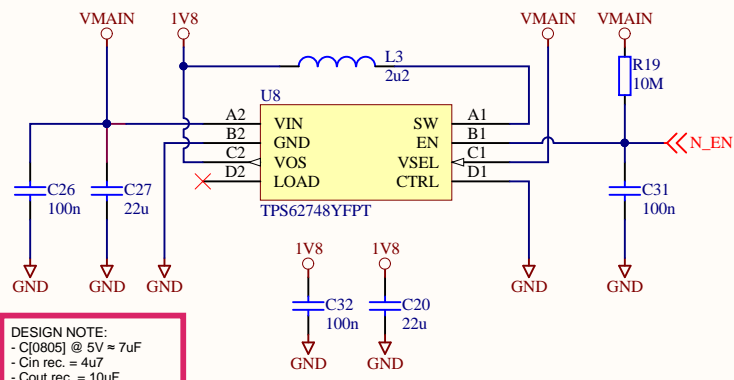
Title: 06_POWER-INPUT.SchDoc	
Project: 100275_NOTECARRIER-F.PrjPCB	
Company: Byte Lab Grupa d.o.o.	
Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com	
Author: M. Hamin	Acceptor: T. Zvonc
Sheet 6 of 7	Rev: 11 Format: A4



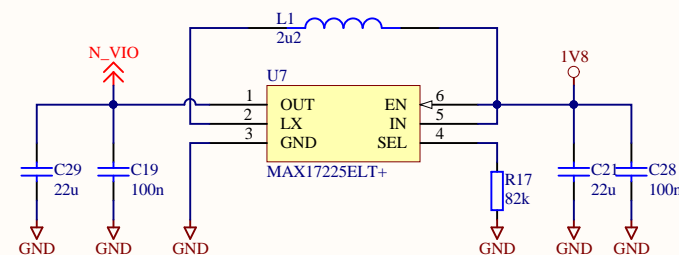
Buck - Boost, 3.6V@2A



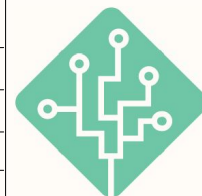
Buck, 1.8V@300mA

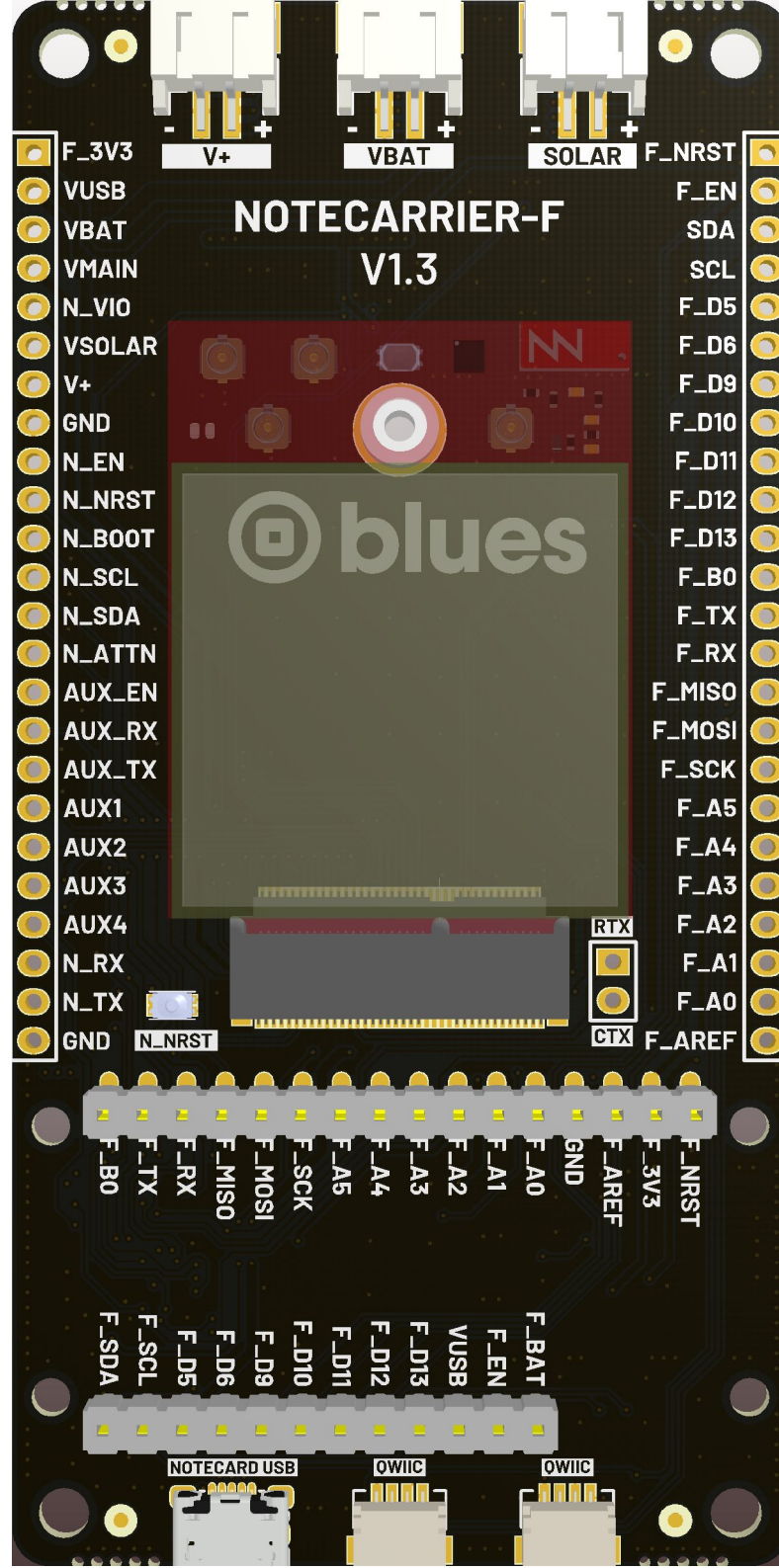


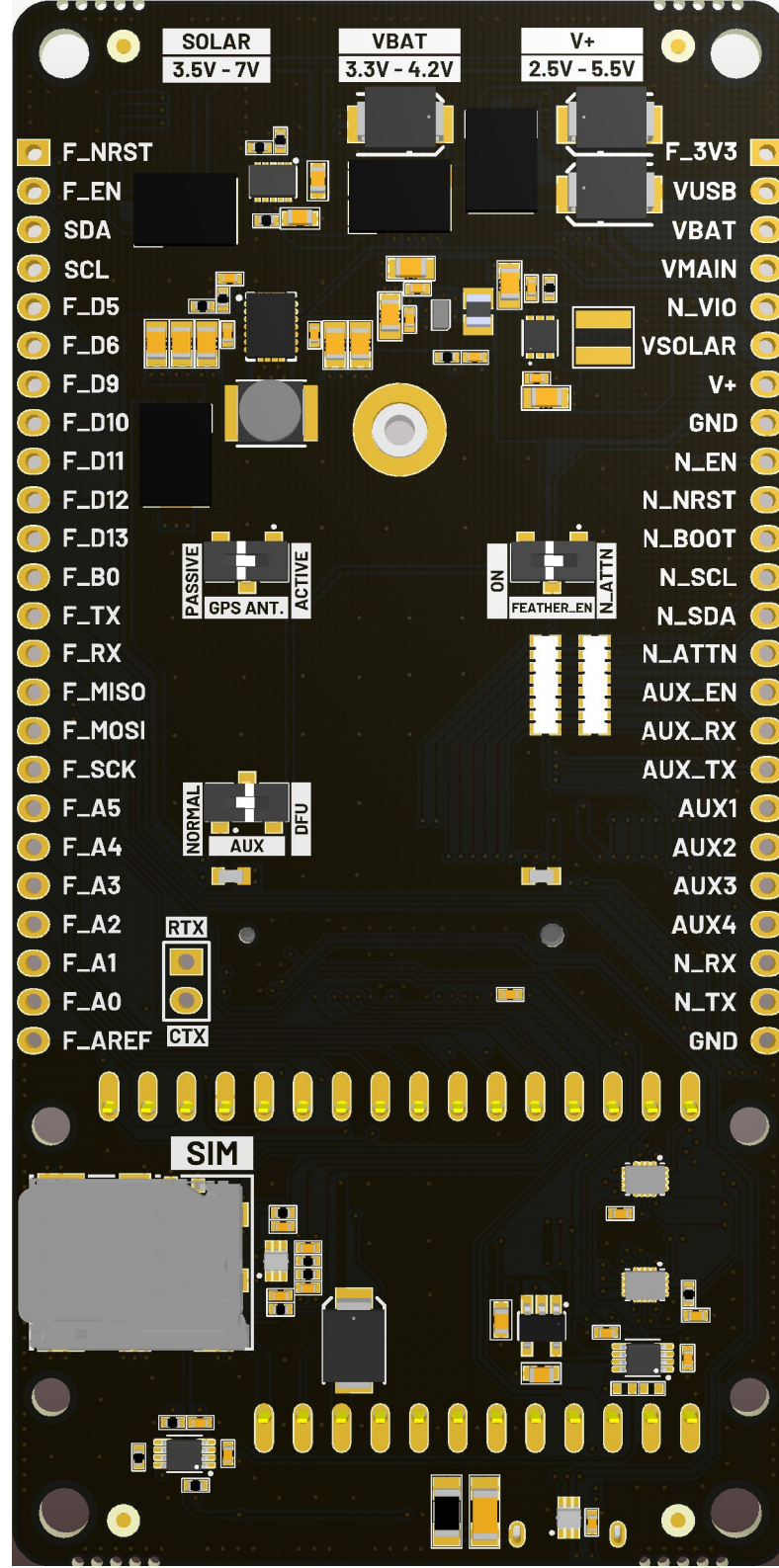
Boost, 3.3V@150mA



Title: 07_POWER-RAILS.SchDoc	
Project: 100275_NOTECARRIER-F.PrjPCB	
Company: Byte Lab Grupa d.o.o.	Medarska 69/1, 10000 Zagreb, Croatia www.byte-lab.com
Author: M. Hamin	Acceptor: T. Zvonc
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SOLAR
3.5V - 7V

VBAT
3.3V - 4.2V

V+
2.5V - 5.5V

F_NRST

F_EN

SDA

SCL

F_D5

F_D6

F_D9

F_D10

F_D11

F_D12

F_D13

F_B0

F_TX

F_RX

F_MISO

F_MOSI

F_SCK

F_A5

F_A4

F_A3

F_A2

F_A1

F_A0

F_AREF

F_3V3

VUSB

VBAT

VMAIN

N_VIO

VSOLAR

V+

GND

N_EN

N_NRST

N_BOOT

N_SCL

N_SDA

N_ATTN

AUX_EN

AUX_RX

AUX_TX

AUX1

AUX2

AUX3

AUX4

N_RX

N_TX

GND

PASSIVE
GPS ANT.
ACTIVE

ON
FEATHER_EN
N_ATTN

NORMAL
AUX
DFU

RTX

CTX

SIM

