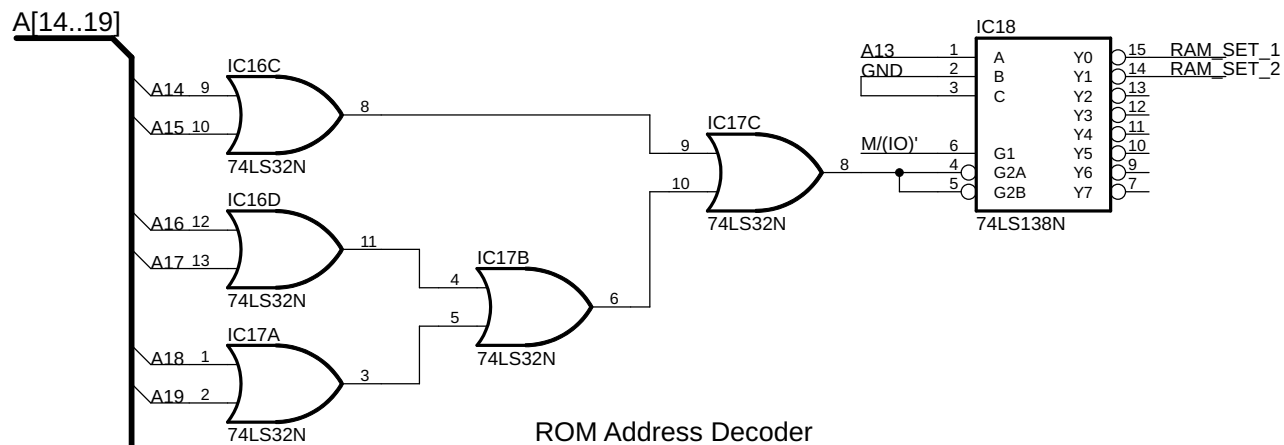
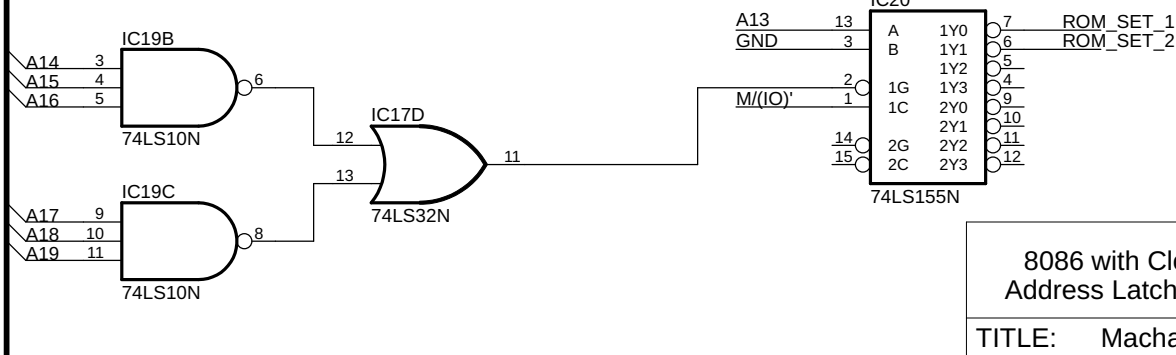


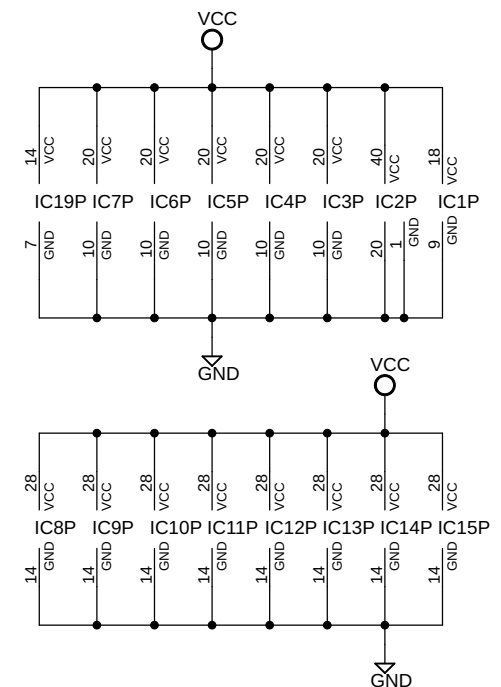
RAM Address Decoder



ROM Address Decoder



Gate and IC Supply



8086 with Clock Generator
Address Latch and Data Buffer

Zach Riane I. Machacon
Designer

TITLE: Machacon_LE4-2

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