1. Description

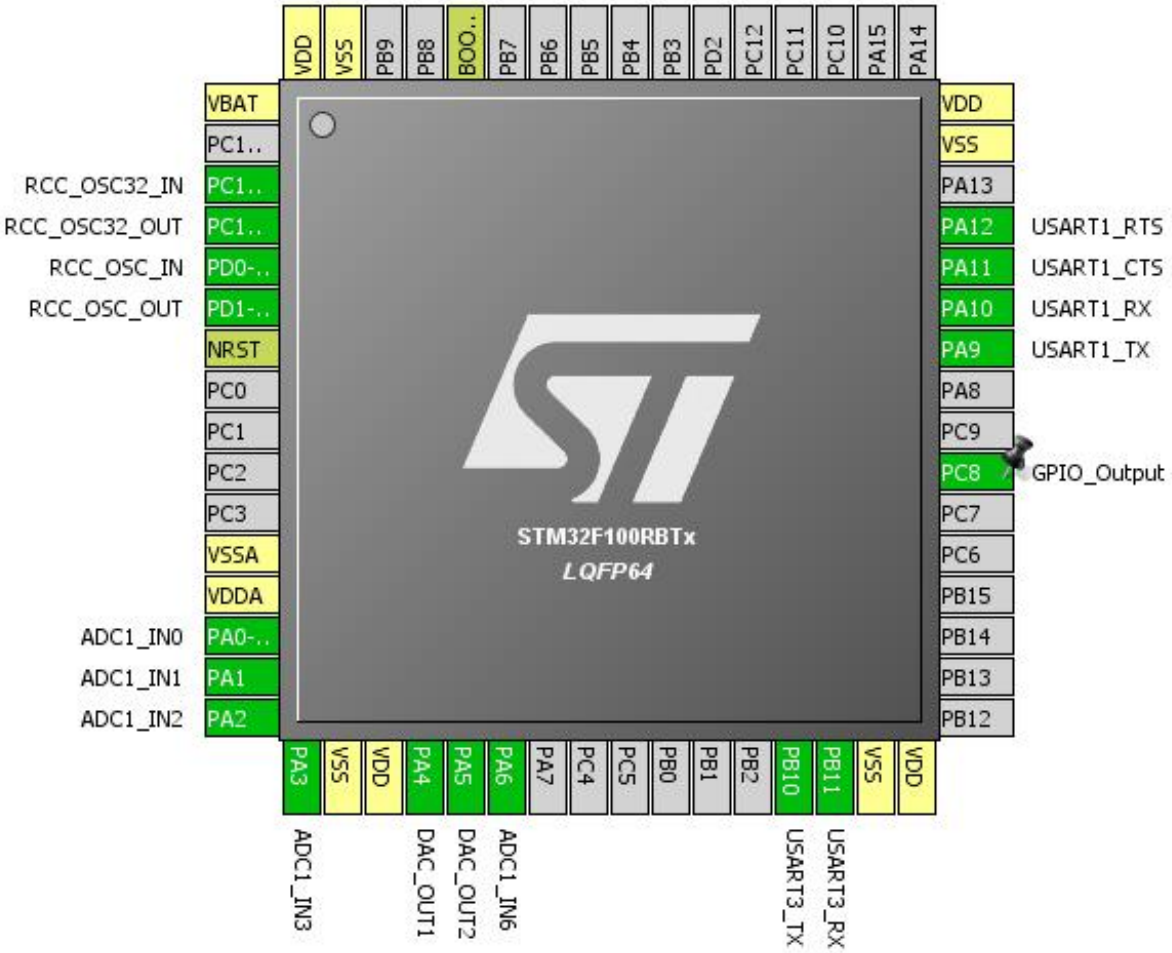
1.1. Project

Project Name	test6
Generated with:	STM32CubeMX 4.6.0
Date	03/14/2015

1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F100 Value Line
MCU name	STM32F100RBTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
ADC1	IN0	ADC1_IN0	PA0-WKUP
	IN1	ADC1_IN1	PA1
	IN2	ADC1_IN2	PA2
	IN3	ADC1_IN3	PA3
	IN6	ADC1_IN6	PA6
DAC	OUT1 Configuration	DAC_OUT1	PA4
	OUT2 Configuration	DAC_OUT2	PA5
RCC	High Speed Clock (HSE): Crystal/Ceramic Resonator	RCC_OSC_IN	PD0-OSC_IN
		RCC_OSC_OUT	PD1-OSC_OUT
	Low Speed Clock (LSE) : Crystal/Ceramic Resonator	RCC_OSC32_IN	PC14-OSC32_IN
		RCC_OSC32_OUT	PC15-OSC32_OUT
USART1	Mode: Asynchronous	USART1_RX	PA10
		USART1_TX	PA9
	Flow Control (RS232): CTS/RTS	USART1_CTS	PA11
		USART1_RTS	PA12
USART3	Mode: Asynchronous	USART3_RX	PB11
		USART3_TX	PB10

4. Pins Configuration

Pin	Pos	Function(s)	Label
PC14-OSC32_IN	3	RCC_OSC32_IN	
PC15-OSC32_OUT	4	RCC_OSC32_OUT	
PD0-OSC_IN	5	RCC_OSC_IN	
PD1-OSC_OUT	6	RCC_OSC_OUT	
PA0-WKUP	14	ADC1_IN0	
PA1	15	ADC1_IN1	
PA2	16	ADC1_IN2	
PA3	17	ADC1_IN3	
PA4	20	DAC_OUT1	
PA5	21	DAC_OUT2	
PA6	22	ADC1_IN6	
PB10	29	USART3_TX	
PB11	30	USART3_RX	
PC8 *	39	GPIO_Output	
PA9	42	USART1_TX	
PA10	43	USART1_RX	
PA11	44	USART1_CTS	
PA12	45	USART1_RTS	

* The pin is affected with an I/O function

5. Power Plugin report

5.1. Microcontroller Selection

Serie	STM32F1
Line	STM32F100 Value Line
MCU	STM32F100RBTx
Datasheet	16455_Rev7

5.2. Parameter Selection

Temperature	25
Vdd	3.3

6. Software Project

6.1. Project Settings

Name	Value
Project Name	test6
Project Folder	D:\Data\FORSLUNC\My Documents\STM32\test6
Toolchain / IDE	TrueSTUDIO 4.3.1
Firmware Package Name and Version	STM32Cube FW_F1 V1.0.0

6.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6.3. Toolchains Settings

Name	Value
Compiler Optimizations	Balanced Size/Speed