

Part Names

- ❖ Diagram compiler generates random/unique names
- ❖ Names are “implied” by a diagram. [3]
- ❖ Text often requires programmer to supply names

Wire Names

- ❖ Each Wire instance is unique.
- ❖ In text, this usually requires a unique name for every Wire instance.
- ❖ In diagrams, Wire names are implicit. [3]