

1. Description

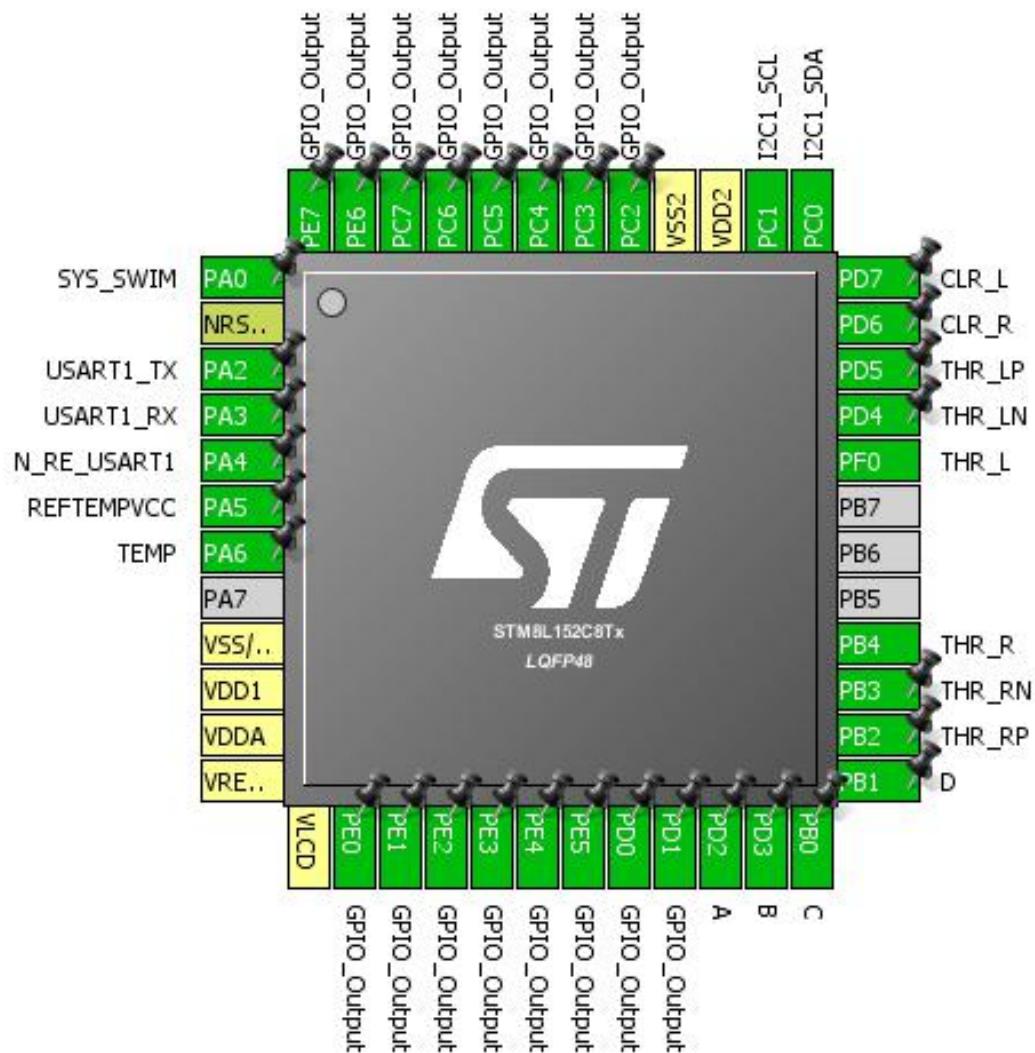
1.1. Project

Project Name	BMO_HUB_31
Board Name	No information
Generated with:	STM8CubeMX 1.5.0
Date	10/03/2024

1.2. MCU

MCU Series	STM8L
MCU Line	STM8L151/152
MCU name	STM8L152C8Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



3. Pins Configuration

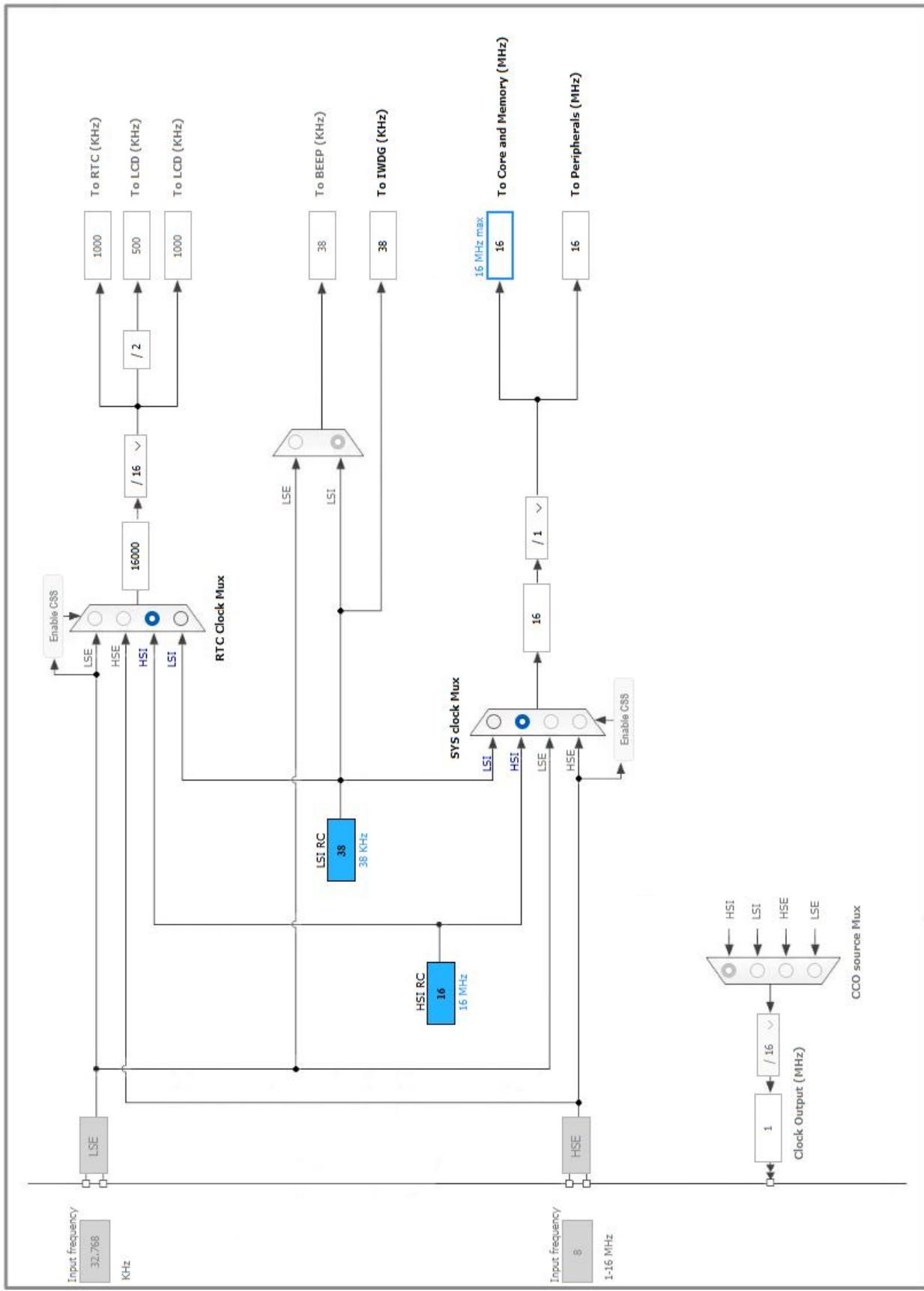
Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	PA0	I/O	SYS_SWIM	
2	NRST/PA1	Reset		
3	PA2	I/O	USART1_TX	
4	PA3	I/O	USART1_RX	
5	PA4 *	I/O	GPIO_Output	N_RE_USART1
6	PA5	I/O	ADC1_IN1	REFTEMPVCC
7	PA6	I/O	ADC1_IN0	TEMP
9	VSS/VSSA/VREF-	Power		
10	VDD1	Power		
11	VDDA	Power		
12	VREF+/VREF+_DAC	Power		
13	VLCD	Power		
14	PE0 *	I/O	GPIO_Output	
15	PE1 *	I/O	GPIO_Output	
16	PE2 *	I/O	GPIO_Output	
17	PE3 *	I/O	GPIO_Output	
18	PE4 *	I/O	GPIO_Output	
19	PE5 *	I/O	GPIO_Output	
20	PD0 *	I/O	GPIO_Output	
21	PD1 *	I/O	GPIO_Output	
22	PD2 *	I/O	GPIO_Input	A
23	PD3 *	I/O	GPIO_Input	B
24	PB0 *	I/O	GPIO_Input	C
25	PB1 *	I/O	GPIO_Input	D
26	PB2	I/O	ADC1_IN16	THR_RP
27	PB3	I/O	ADC1_IN15	THR_RN
28	PB4	I/O	DAC_OUT2	THR_R
32	PF0	I/O	DAC_OUT1	THR_L
33	PD4	I/O	ADC1_IN10	THR_LN
34	PD5	I/O	ADC1_IN9	THR_LP
35	PD6 *	I/O	GPIO_Output	CLR_R
36	PD7 *	I/O	GPIO_Output	CLR_L
37	PC0	I/O	I2C1_SDA	
38	PC1	I/O	I2C1_SCL	
39	VDD2	Power		
40	VSS2	Power		

BMO_HUB_31 Project
Configuration Report

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
41	PC2 *	I/O	GPIO_Output	
42	PC3 *	I/O	GPIO_Output	
43	PC4 *	I/O	GPIO_Output	
44	PC5 *	I/O	GPIO_Output	
45	PC6 *	I/O	GPIO_Output	
46	PC7 *	I/O	GPIO_Output	
47	PE6 *	I/O	GPIO_Output	
48	PE7 *	I/O	GPIO_Output	

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Power Consumption Calculator report

5.1. Microcontroller Selection

Series	STM8L
Line	STM8L151/152
MCU	STM8L152C8Tx
Datasheet	17943_Rev9

5.2. Parameter Selection

Temperature	25
Vdd	3.0