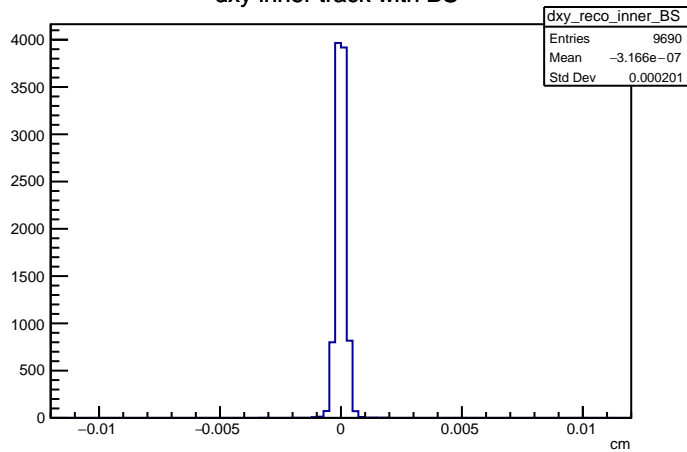
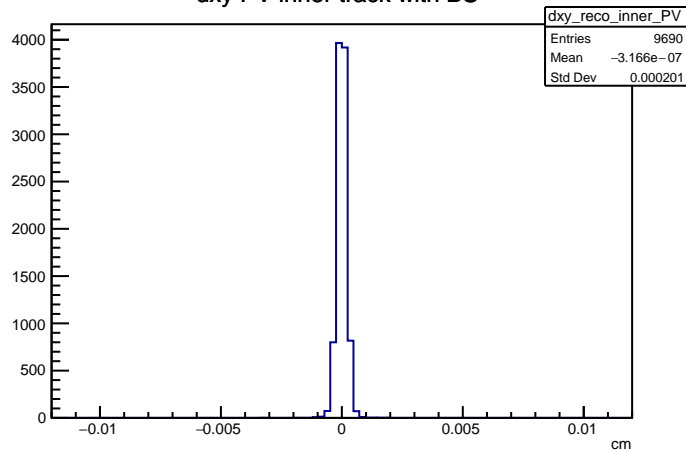


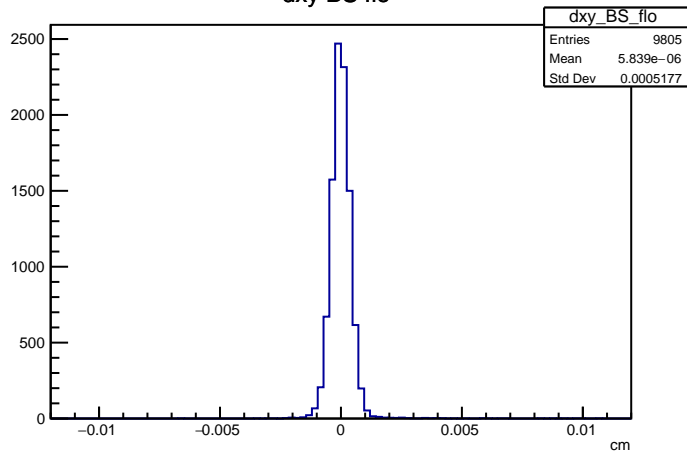
dxy inner track with BS



dxy PV inner track with BS



dxy BS flo



dxy PV flo

