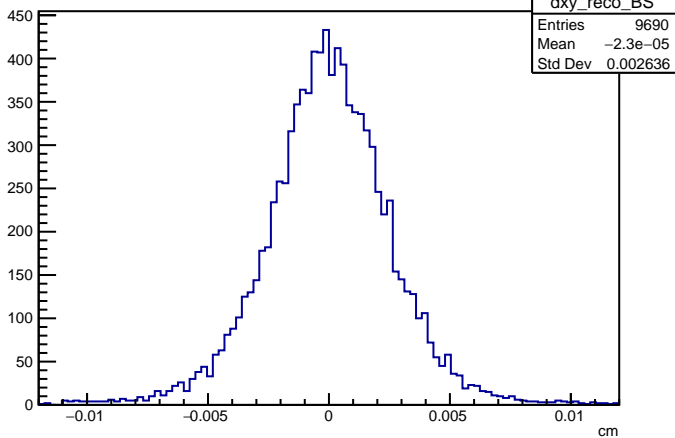
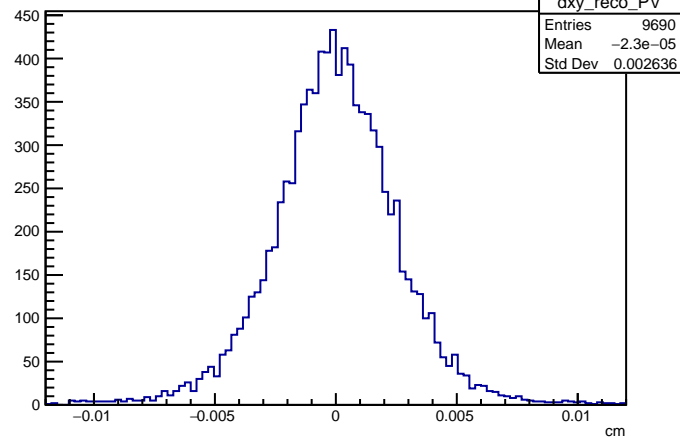


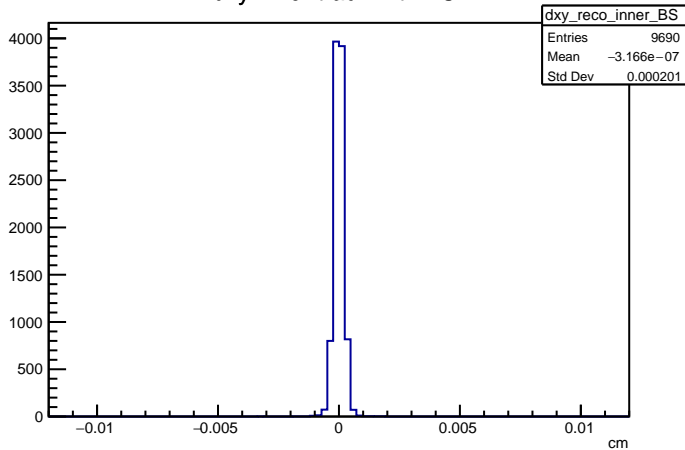
dxy reco BS



dxy reco PV



dxy inner track with BS



dxy PV inner track with BS

