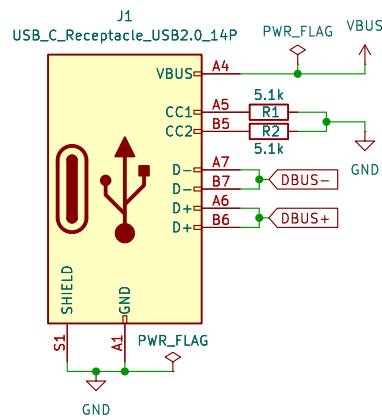
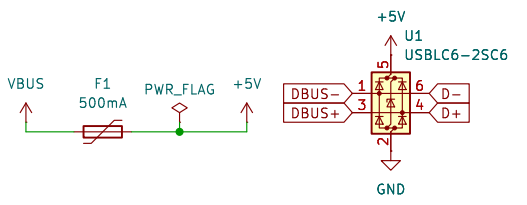


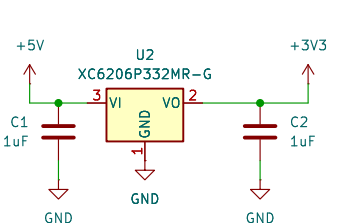
USB



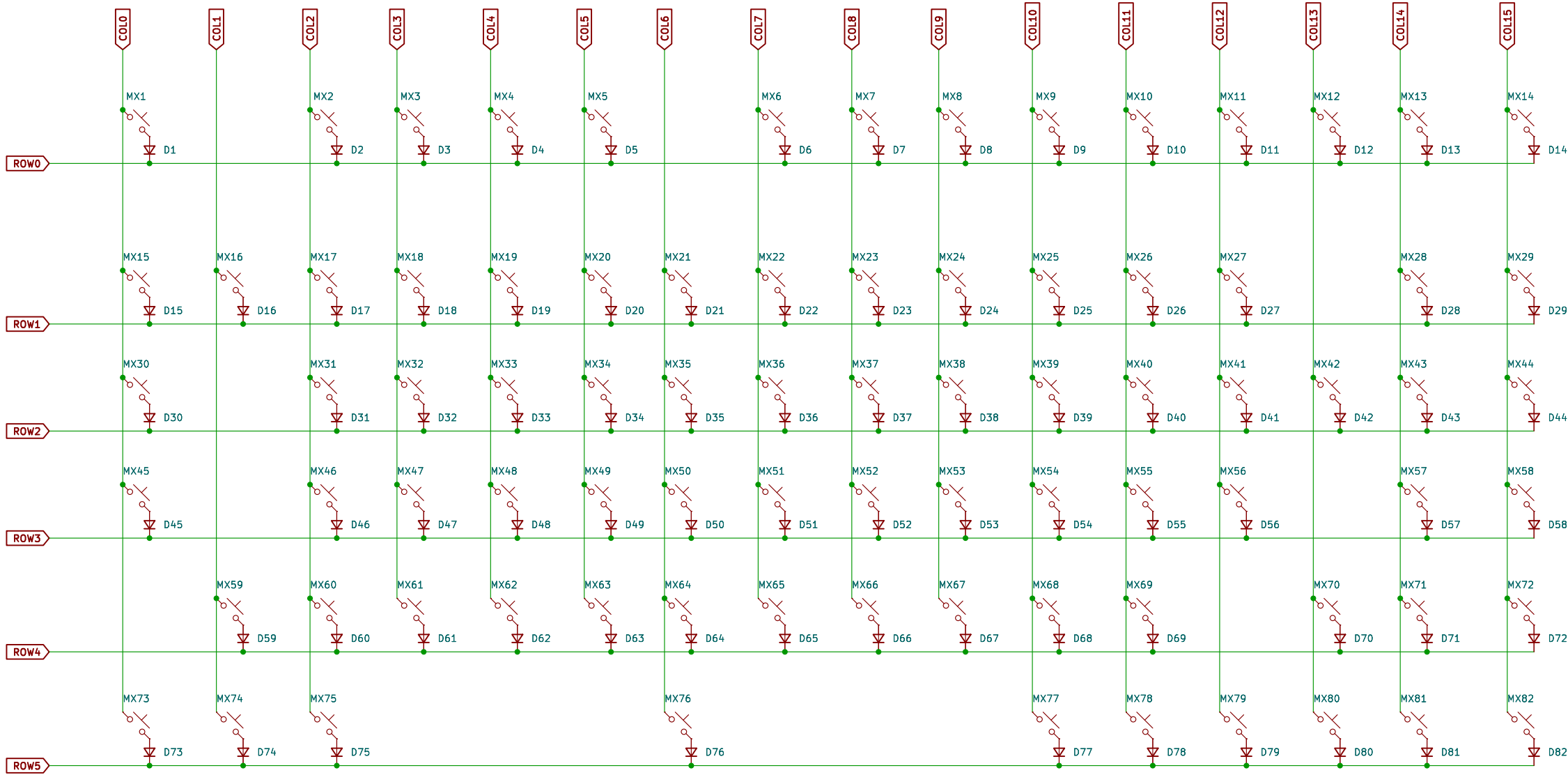
Fuse and ESD Protection



Power Regulation

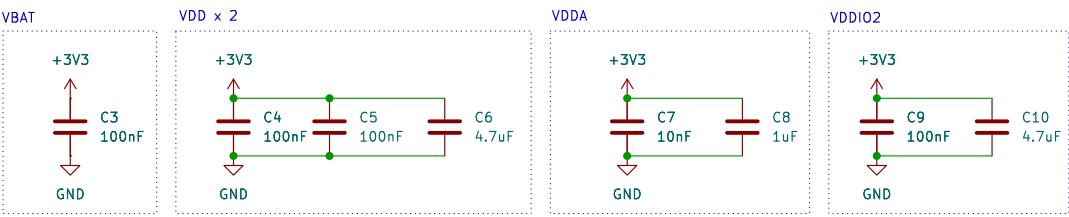


Matrix



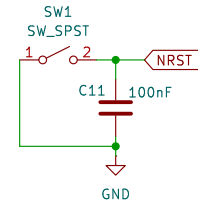
MCU

Decoupling Capacitors



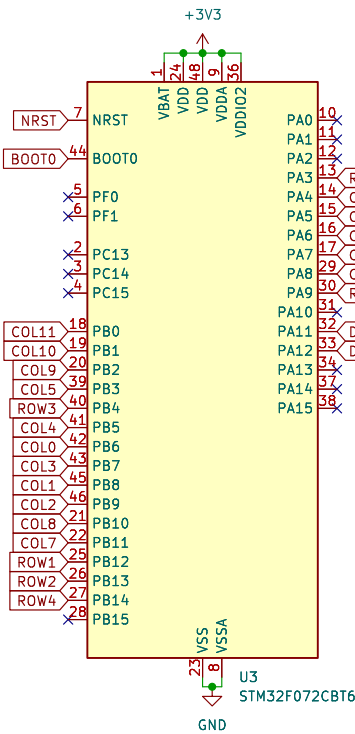
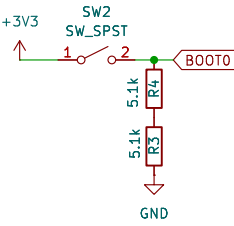
These capacitors must be placed as close as possible to, or below, the appropriate pins.

Reset switch



NRST has an embedded pull-up resistor.

Boot switch



By default, PA13 and PA14 are configured as SWDIO and SWCLK alternate functions after reset.

Fiducials



Mounting Holes



Stabilizers

