

Name	Status	ID	Epic Item	Estimate (hrs)	Remaining (hrs)	Completed (hrs)	Progress
Setup	DONE	0	Setup	3	0	3.5	100%
Research	DONE	1	Research	6	0	9	100%
Create Vision Document	DONE	2	Vision Document	1	0	1.25	100%
Write Vision Document	DONE	3	Vision Document	10	0	9	100%
Review Vision Document	DONE	4	Vision Document	2	0	0.5	100%
Create Project Plan	DONE	5	Project Plan Document	1	0	0.5	100%
Write Project Plan	DONE	6	Project Plan Document	10	0	8.5	100%
Review Project Plan	DONE	7	Project Plan Document	2	0	0.5	100%
Create SQA Plan	DONE	8	SQA Plan Document	1	0	0.5	100%
Write SQA Plan	DONE	9	SQA Plan Document	10	0	3	100%
Review SQA Plan	DONE	10	SQA Plan Document	2	0	0.5	100%
Develop Prototype	DONE	11	Prototype	20	0	26.5	100%
Work on Presentation	DONE	12	Presentation	10	0	10	100%
Update Timelog	DONE	13	Setup Phase 2	1.5	0	1	100%
Modify Vision Document 2.0	DONE	14	Vision Document 2.0	4	0	0.5	100%
Review Vision Document 2.0	DONE	15	Vision Document 2.0	1	0	0.2	100%
Modify Project Plan 2.0	DONE	16	Project Plan 2.0	4	0	0.5	100%
Review Project Plan 2.0	DONE	17	Project Plan 2.0	1	0	0.2	100%
Create Formal Specification	DONE	18	Formal Specification	1	0	0.5	100%

Write Formal Specification	DONE	19	Formal Specification	8	0	4.5	100%
Review Formal Specification	DONE	20	Formal Specification	1	0	1	100%
Create Architecture Design	DONE	21	Architecture Design	2	0	1	100%
Write Architecture Design	DONE	22	Architecture Design	10	0	11.5	100%
Review Architecture Design	DONE	23	Architecture Design	1	0	1.5	100%
Create Test Plan	DONE	24	Test Plan	2	0	1	100%
Write Test Plan	DONE	25	Test Plan	6	0	4	100%
Review Test Plan	DONE	26	Test Plan	1	0	0.5	100%
Writeup Formal Technical Inspection Document	DONE	27	Formal Technical Inspection	4	0	3	100%
Review Formal Technical Document	DONE	28	Formal Technical Inspection	0.5	0	0.5	100%
Develop Architecture Executable	DONE	29	Architecture Executable	15	0	14	100%
Work on Phase 2 Presentation	DONE	30	Phase 2 Presentation	3	0	4	100%
Research TCP/IP	DONE	31	Research	10	0	8	100%
Research GUI	DONE	32	Research	4	0	4	100%
Research Neural Network Design	DONE	33	Research	0	0	0	0%
Update Timelog	DONE	34	Setup Phase 3	0.5	0	0.5	100%
Fix Action Items	DONE	35	Setup Phase 3	3	0	5	100%
Create User Manual	DONE	36	User Manual	1	0	0.5	100%
Write User Manual	DONE	37	User Manual	2	0	2.5	100%
Review User Manual	DONE	38	User Manual	0.5	0	1.5	100%

Create Component Design	DONE	39	Component Design	2	0	0.5	100%
Write Component Design	DONE	40	Component Design	5	0	3.5	100%
Review Component Design	DONE	41	Component Design	1	0	3	100%
Create Assessment Evaluation (Test Report)	DONE	42	Assessment Evaluation	1	0	1	100%
Write Assessment Evaluation (Test Report)	DONE	43	Assessment Evaluation	6	0	4.5	100%
Review Assessment Evaluation (Test Report)	DONE	44	Assessment Evaluation	0.5	0	0.5	100%
Create Project Evaluation	DONE	45	Project Evaluation	0.5	0	0.5	100%
Write Project Evaluation	DONE	46	Project Evaluation	6	0	4	100%
Review Project Evaluation	DONE	47	Project Evaluation	1	0	1	100%
Writeup Formal Technical Inspection Document	DONE	48	Technical Inspection	2	0	3	100%
Create and Write References	DONE	49	Technical Inspection	1	0	1	100%
Review Inspection Letters	DONE	50	Technical Inspection	1.5	0	1	100%
Fix Issues from Insepctions as needed	DONE	51	Develop Final Program	4	0	0	0%
Develop Final Program	DONE	52	Develop Final Program	20	0	22	100%
Create Final Presentation	DONE	53	Phase 3 Presentatio n	3	0	3.5	100%