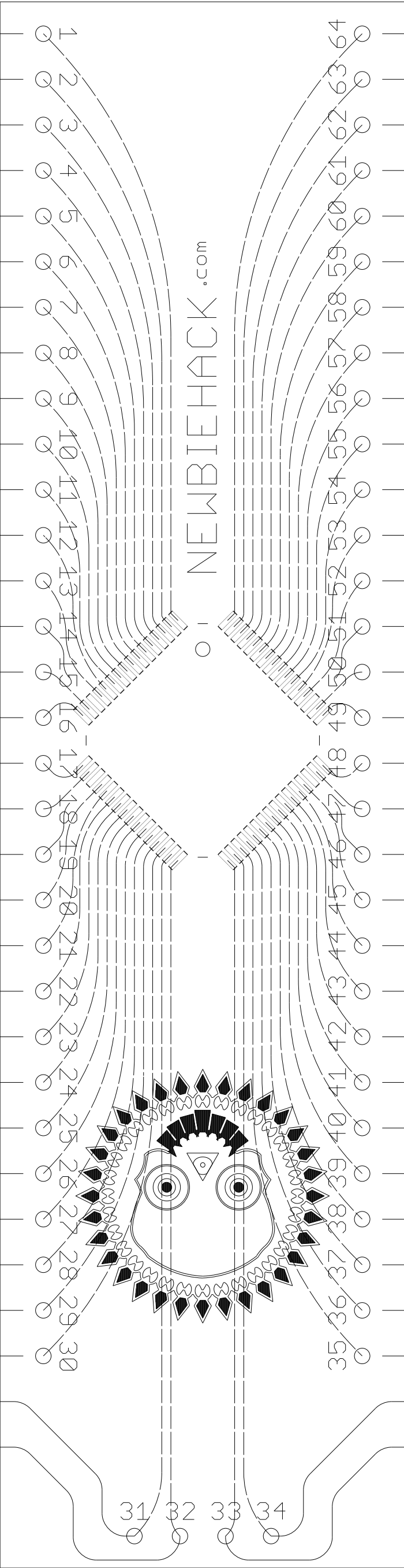


Additional	Alternate	Standard
VDD DIGITAL POWER SUPPLY		
RTC_TAMP1, RTC_TS RTC_OUT, WKUP2	X	PC13
OSC32_IN	X	PC14
OSC32_OUT	X	PC15
OSC_IN	X	PF0
OSC_OUT	X	PF1
NRST RESET INPUT OR INTERNAL RESET OUTPUT		
ACD_IN10	EVENTOUT	PC0
ADC_IN11	EVENTOUT	PC1
ADC_IN12	EVENTOUT	PC2
ADC_IN13	EVENTOUT	PC3
VSSA ANALOG GROUND		
VDDA ANALOG POWER SUPPLY		
ADC_IN0, RTC_TAMP2 WKUP1	USART2_CTS	PA0
ADC_IN1	USART2_RTS EVENTOUT	PA1
ADC_IN2	USART2_TX TIM15_CH1	PA2
ADC_IN3	USART2_RX TIM15_CH2	PA3
X	EVENTOUT	PF4
X	EVENTOUT	PF5
ADC_IN4	SPI1_NSS, USART2_CK TIM14_CH1	PA4
ADC_IN5	SPI1_SCK	PA5
ADC_IN6	SPI1_MISO, TIM3_CH1, TIM1_BKIN TIM16_CH1, EVENTOUT	PA6
ADC_IN7	SPI1_MOSI, TIM3_CH2, TIM14_CH1 TIM1_CH1N, TIM17_CH1, EVENTOUT	PA7
ADC_IN14	EVENTOUT	PC4
ADC_IN15	X	PC5
ADC_IN8	TIM3_CH3, TIM1_CH2N EVENTOUT	PB0
ADC_IN9	TIM3_CH4, TIM14_CH1 TIM1_CH3N	PB1
X	X	PB2
X	SPI2_SCK, I2C1_SCL I2C2_SCL	PB10
X	I2C1_SDA, I2C2_SDA EVENTOUT	PB11
VSS GROUND		
VDD DIGITAL POWER SUPPLY		



Standard	Alternate	Additional
VDD DIGITAL POWER SUPPLY		
VSS GROUND		
PB9	I2C1_SDA, IR_OUT TIM17_CH1, EVENTOUT	X
PB8	I2C1_SCL TIM16_CH1	X
BOOT0 BOOT MEMORY SELECTION		
PB7	I2C1_SDA, USART1_RX TIM17_CH1N	X
PB6	I2C1_SCL, USART1_TX TIM16_CH1N	X
PB5	SPI1_MOSI, I2C1_SMBA TIM16_BKIN, TIM3_CH2	X
PB4	SPI1_MISO, TIM3_CH1 EVENTOUT	X
PB3	SPI1_SCK EVENTOUT	X
PD2	TIM3_ETR	X
PC12	X	X
PC11	X	X
PC10	X	X
PA15	SPI1_NSS, USART2_RX EVENTOUT	X
PA14	USART2_TX SWCLK	X
PF7	I2C2_SDA	X
PF6	I2C2_SCL	X
PA13	IR_OUT SWDIO	X
PA12	USART1_RTS, TIM1_ETR EVENTOUT	X
PA11	USART1_CTS, TIM1_CH4 EVENTOUT	X
PA10	USART1_RX, TIM1_CH3 TIM17_BKIN	X
PA9	USART1_TX, TIM1_CH2 TIM15_BKIN	X
PA8	USART1_CK, TIM1_CH1 EVENTOUT, MCO	X
PC9	TIM3_CH4	X
PC8	TIM3_CH3	X
PC7	TIM3_CH2	X
PC6	TIM3_CH1	X
PB15	SPI2_MOSI, TIM1_CH3N TIM15_CH1N, TIM15_CH2	RTC_REFIN
PB14	SPI2_MISO TIM1_CH2N, TIM15_CH1	X
PB13	SPI2_SCK, I2C2_SDA TIM1_CH1N	X
PB12	SPI2_NSS, TIM1_BKIN EVENTOUT	X