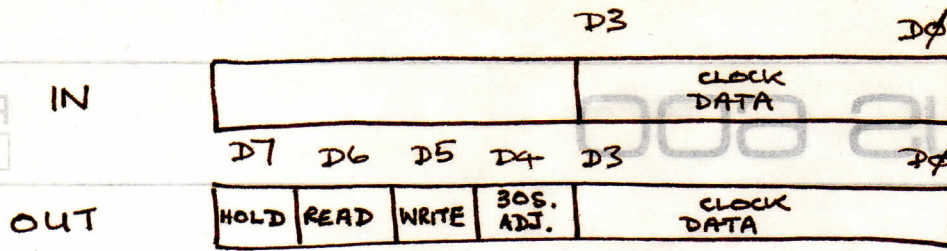


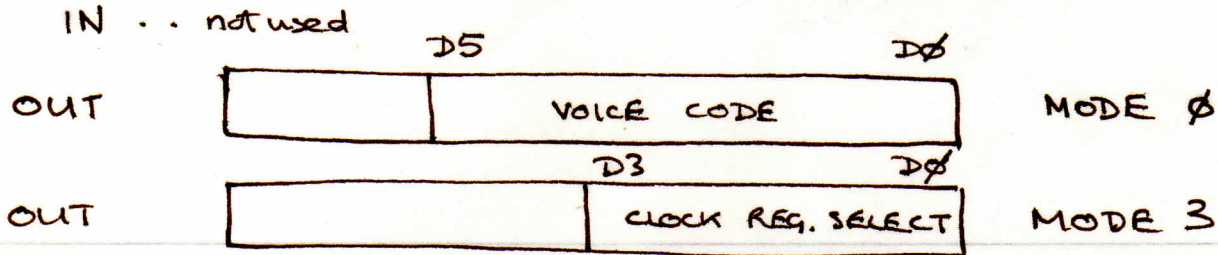
# P10 PORT A

data address 4H  
control addr. 6H

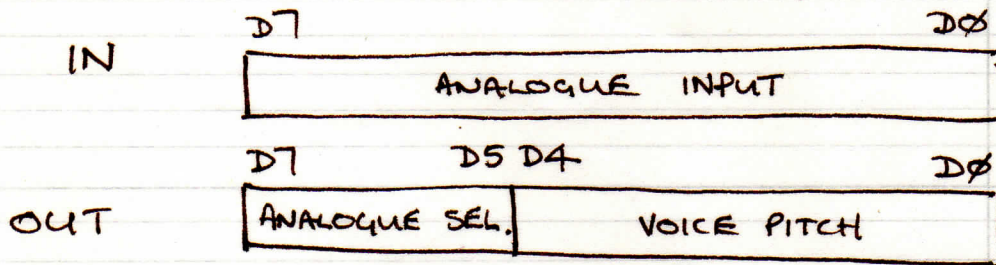


# P10 PORT B

data address 5H  
control address 7H



# I/O PORT address 3H

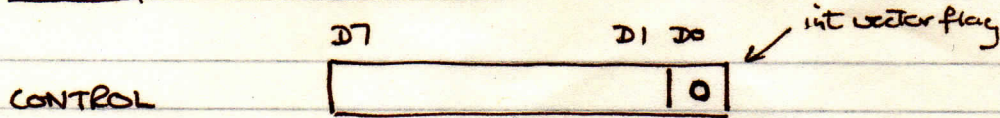


LINE	F	N	COMMENTS
00			
01			
02			
03			
04			
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13			
14			
15			
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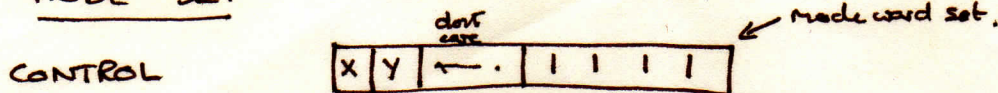


Port 4 DATA A IN/OUT  
 5 DATA B IN/OUT  
 6 CONTROL A  
 7 CONTROL B

### SETTING INTERRUPT VECTOR

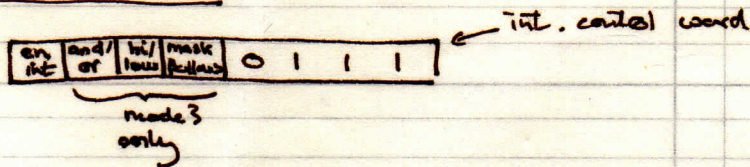


### MODE SET



X	Y	operation
0	0	0 output
0	1	1 input
1	0	2 bidirectional (only port A)
1	1	3 control — next word sent must define iis and outs. bit set 1 input 0 output

### INTERRUPT CONTROL WORD



enable int. = 1 sets port generating interrupts.

and/or — defines logic involved in port monitoring 1 = AND 0 = OR  
 high/low — defines active polarity 1 = monitor for high 0 = monitor for low  
 mask = 1 next control word must define mask  
 mask bit = 0 will define that line to be monitored

### INTERRUPT CONTROL WITHOUT AFFECTING ANYTHING ELSE

