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EET 256 Capstone
Progress Report
Week 8

IC Tester

Current functionality -

Two counters run through all combinations for an 8 input 7400 series logic chip once a start button is pressed. Microcontroller verifies whether the output is correct for a logic chip or not. Output LEDs are multiplexed to save pin space.

Problems -

Still some issues with the program recognizing OR chips, but debugging is going well. Some inconsistency in the push button operation to begin the logic loop.

This Weeks Progress -

Changed output LEDs to a second board so output will be more clear for demonstration. Created files to 3D print to mark the outputs. Lots of code debugging.

Next Weeks Steps -

Check that all chips are correctly recognized. Add pushbuttons to simulate shorts on the chips for presentation. Investigate Op Amp testing.

Percentage complete -

Hardware - 90%

Software - 80%

