

James Solonika
EET 256 Capstone
Progress Report
Week 9

IC Tester

Current functionality -

Two counters run through all combinations for an 8 input 7400 series logic chip once a start button is pressed. Microcontroller verifies whether the output is correct for a logic chip or not. Output LEDs are demultiplexed to save pin space.

Problems -

Realized that the 7400 series NOR chip has a different pinout than the AND, OR, NAND and XOR. Will have to add some selector chips to select different pins as input or output.

This Weeks Progress -

All logic chips work except for NOR.

Next Weeks Steps -

Add in chips to allow NOR testing. Investigate adding OP amp testing.

Percentage complete -

Hardware - 90%

Software - 90%

