

*Note: ABCLK is connected to pin W1, which can be routed to a pll refclk.

*Note: MCLK0 is connected to pin R6, which can be routed to a pll refclk.

Sheet: /FMC Connector/
 File: fmc_connector.sch

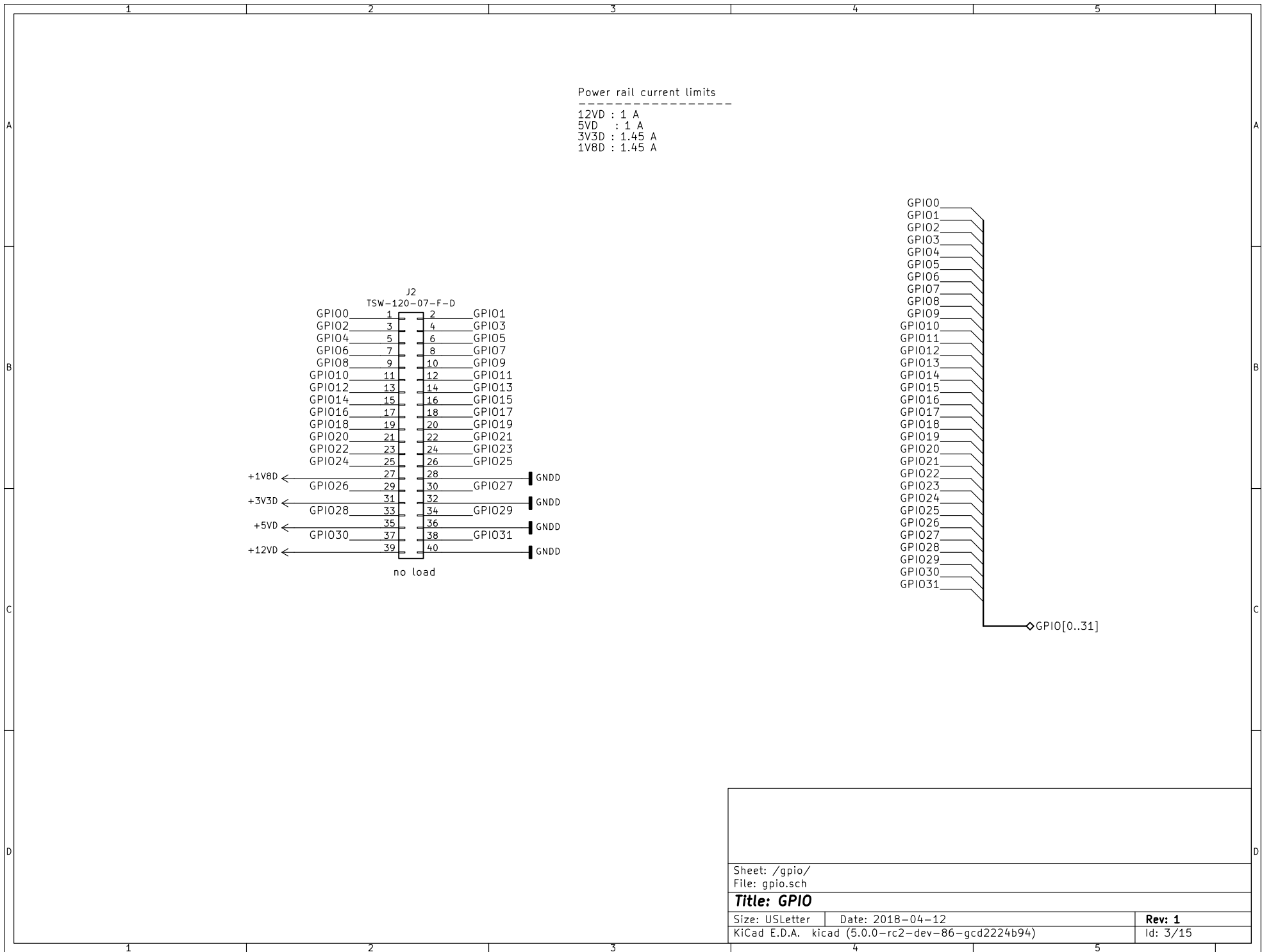
Title: FMC Connector

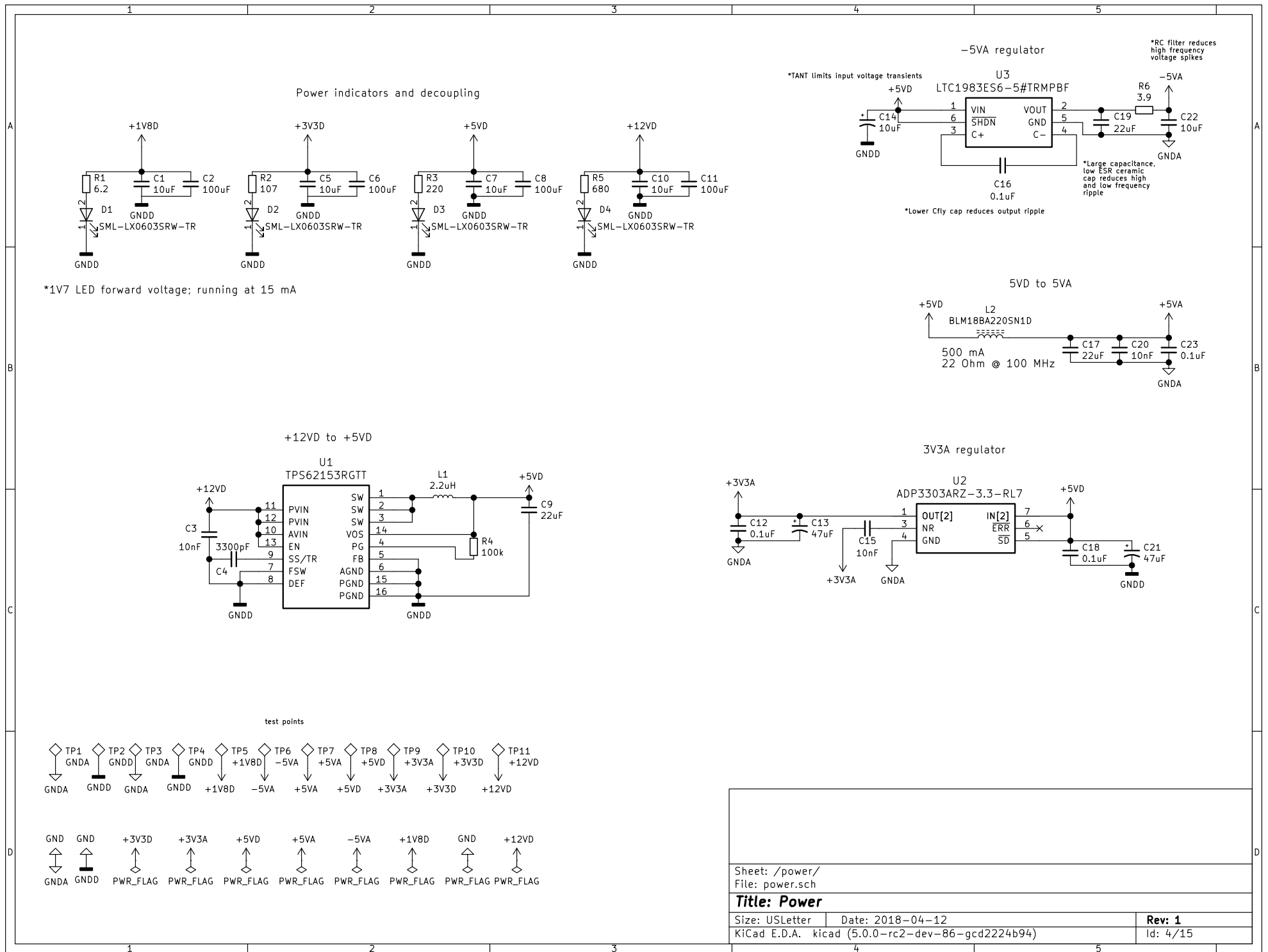
Size: USLetter Date: 2018-04-12

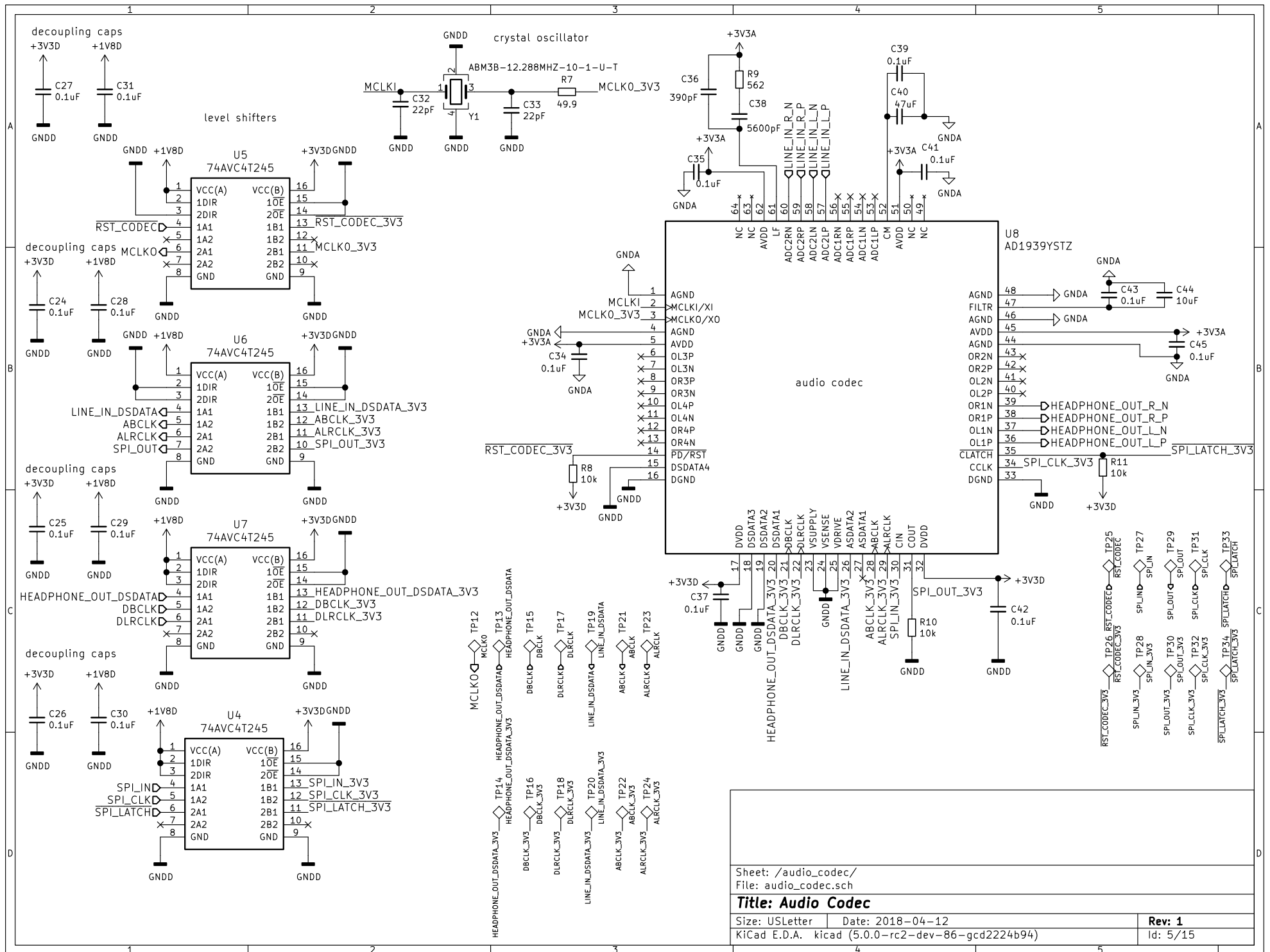
KiCad E.D.A. kicad (5.0.0-rc2-dev-86-gcd2224b94)

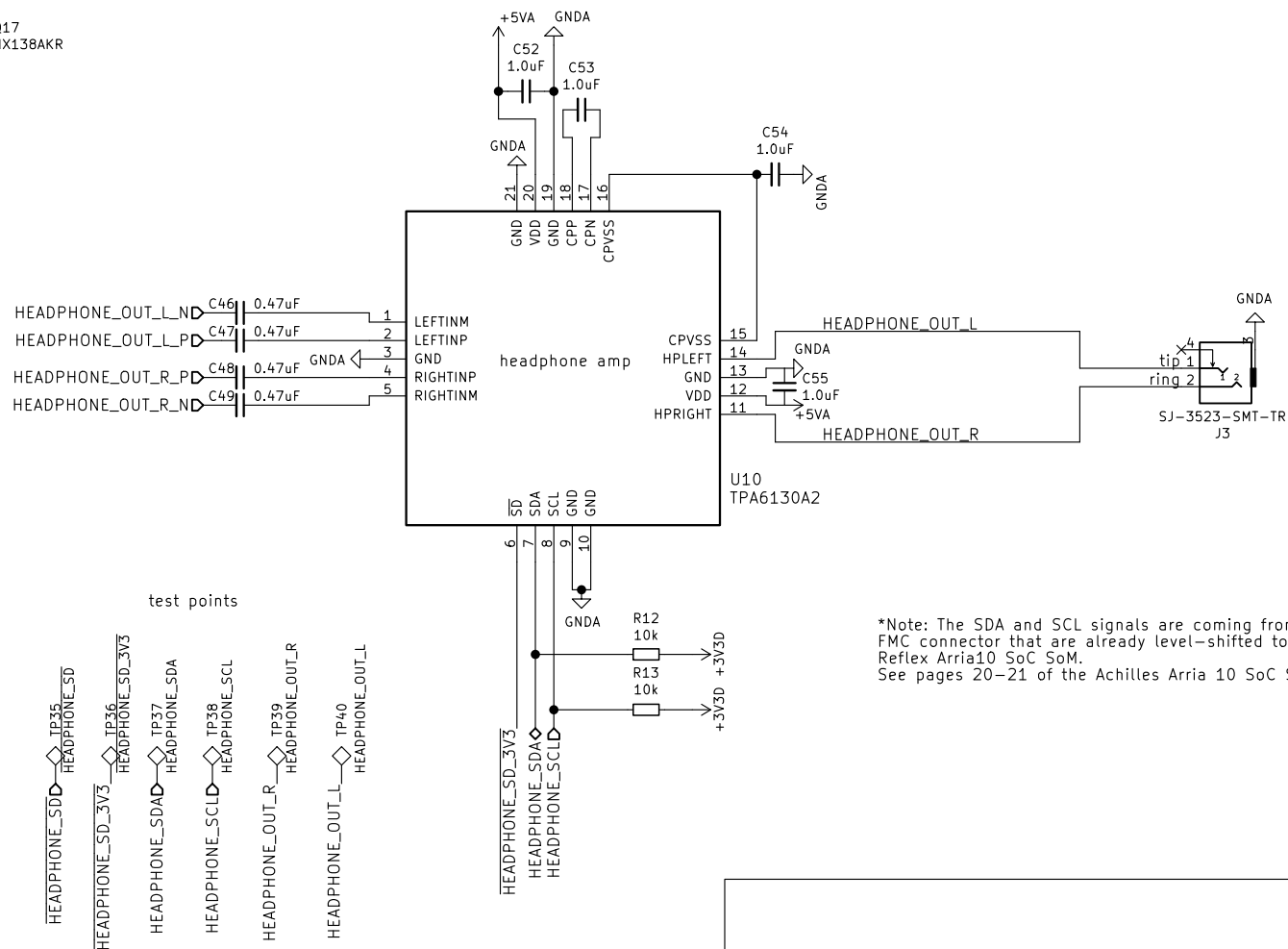
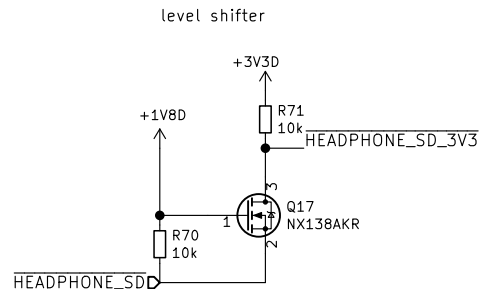
Rev: 1

Id: 2/15









Sheet: /headphone_out/
File: headphone_out.sch

Title: Headphone Out

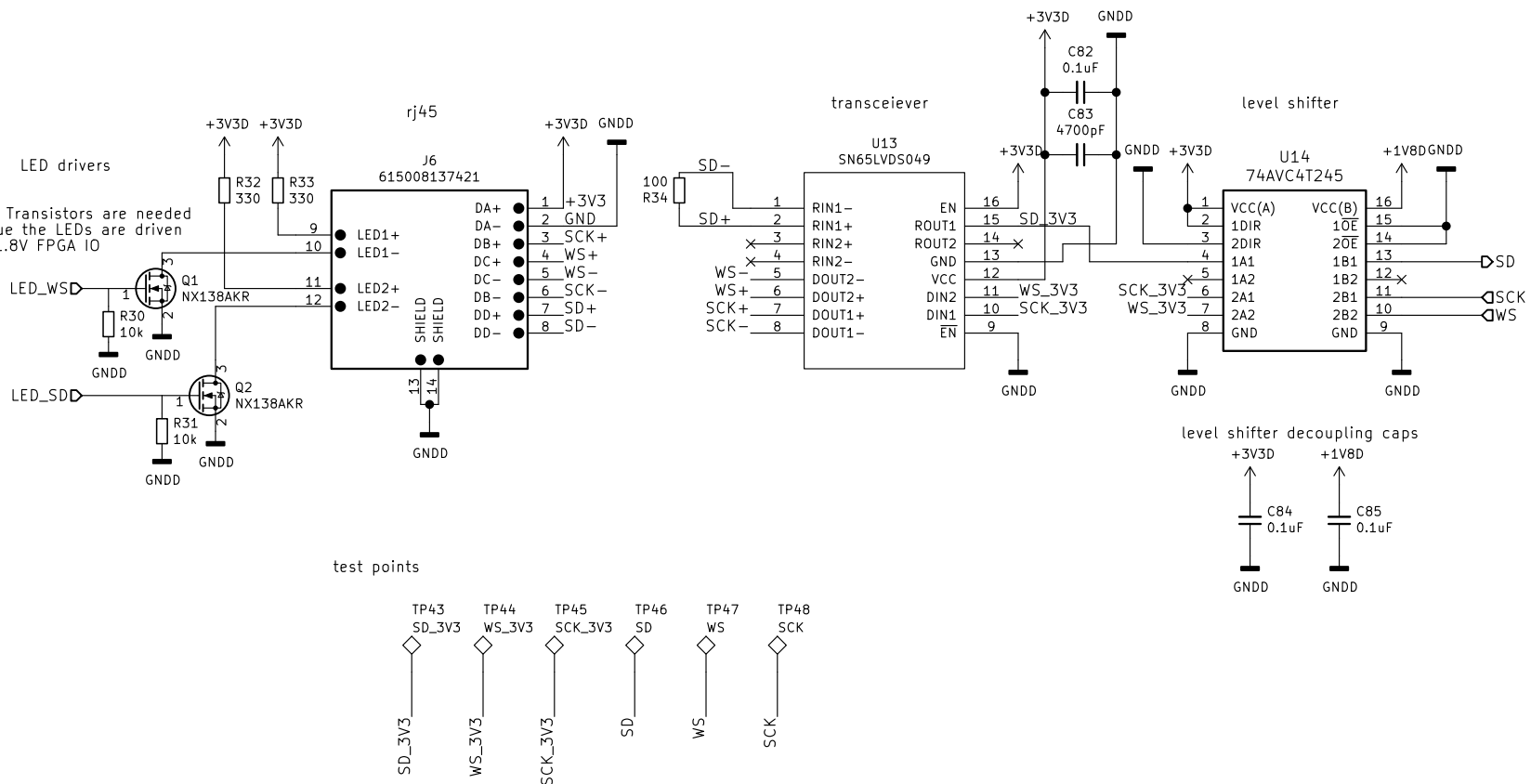
Size: USLetter Date: 2018-04-12

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Rev: 1

Id: 6/15

*Note: Transistors are needed because the LEDs are driven from 1.8V FPGA IO



Sheet: /Microphone Array Interface Ch0/
File: microphone_array_interface.sch

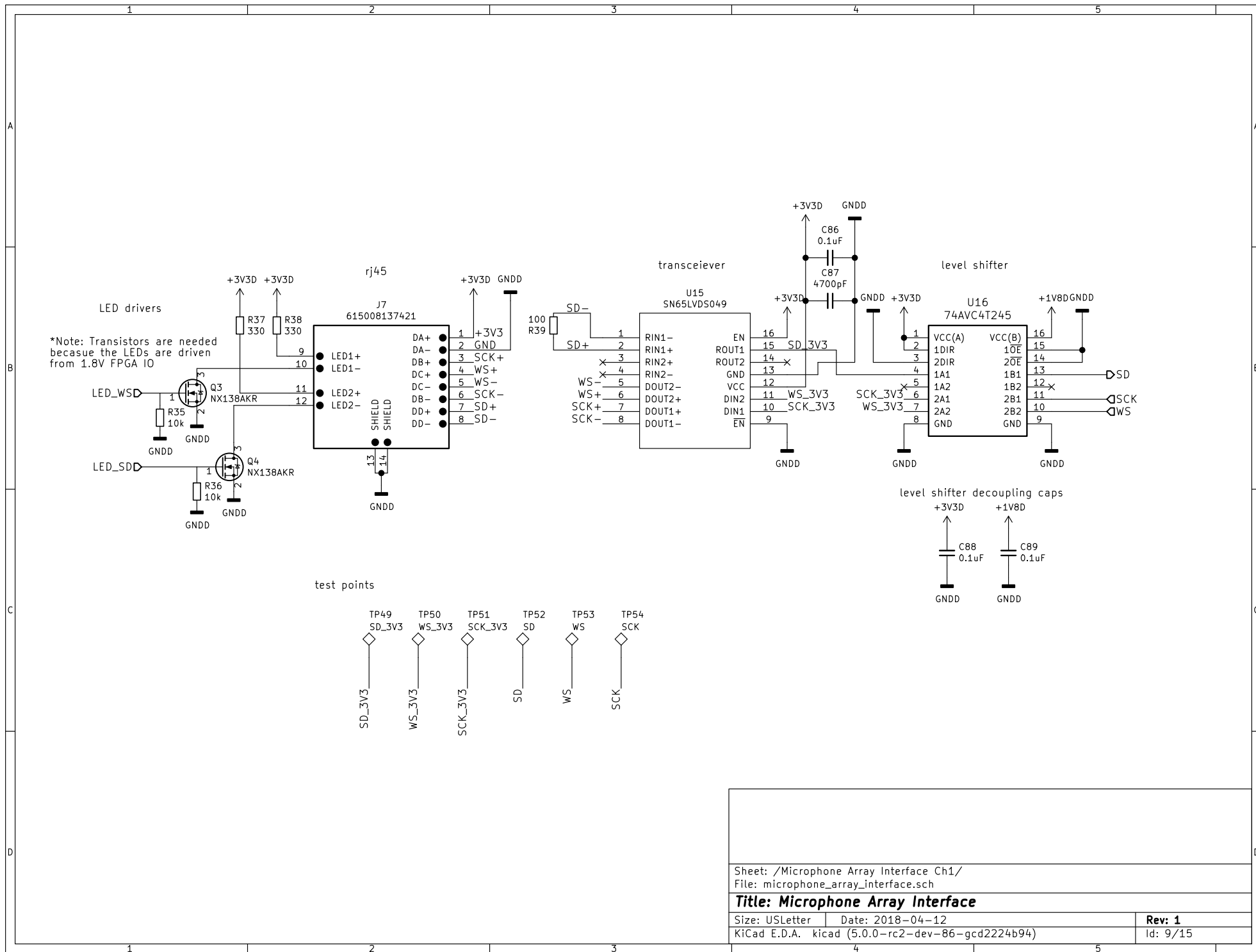
Title: Microphone Array Interface

Size: USLetter Date: 2018-04-12

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Rev: 1

Id: 8/15



Sheet: /Microphone Array Interface Ch1/
File: microphone_array_interface.sch

Title: Microphone Array Interface

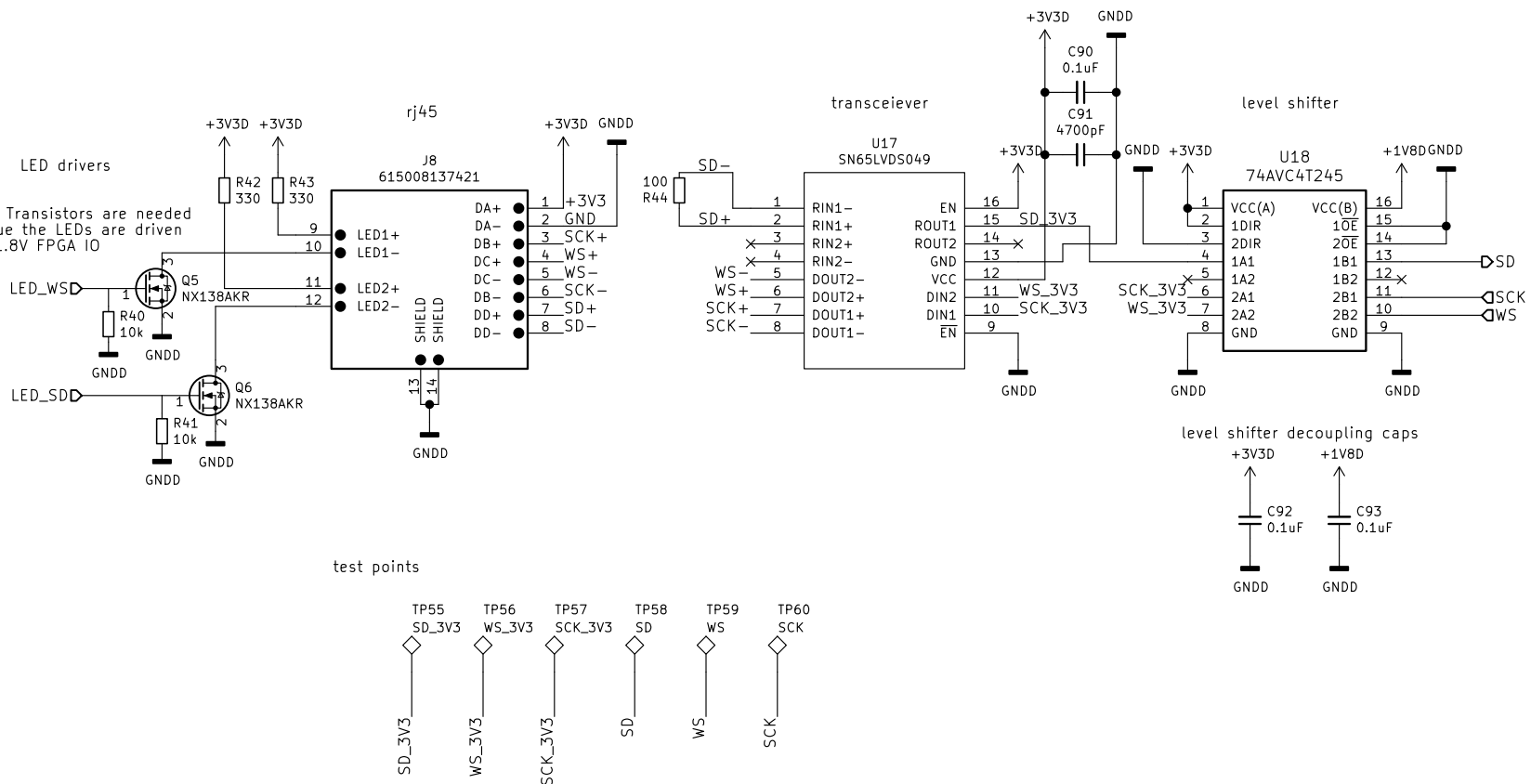
Size: USLetter Date: 2018-04-12

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Rev: 1

Id: 9/15

*Note: Transistors are needed because the LEDs are driven from 1.8V FPGA IO



Sheet: /Microphone Array Interface Ch2/
File: microphone_array_interface.sch

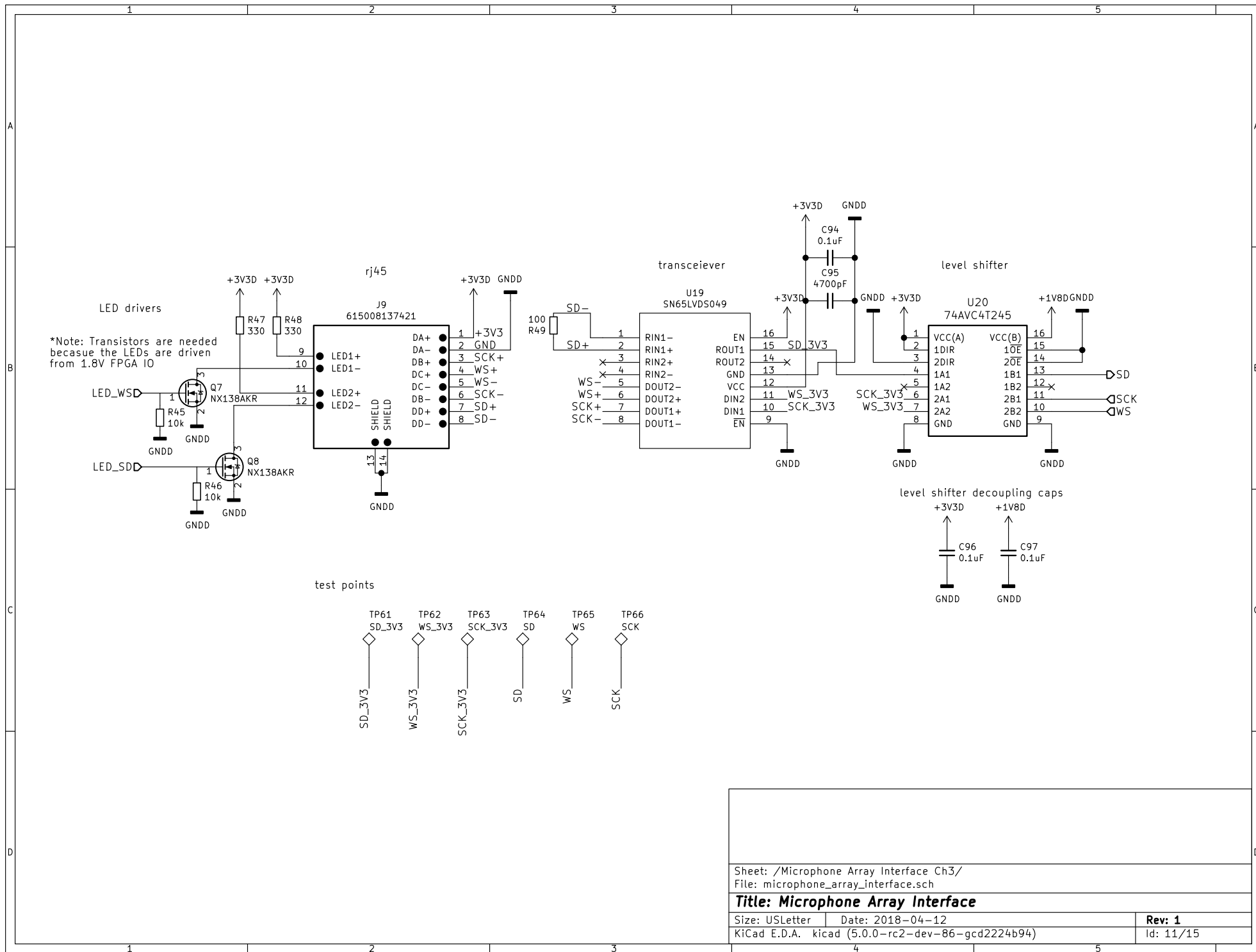
Title: Microphone Array Interface

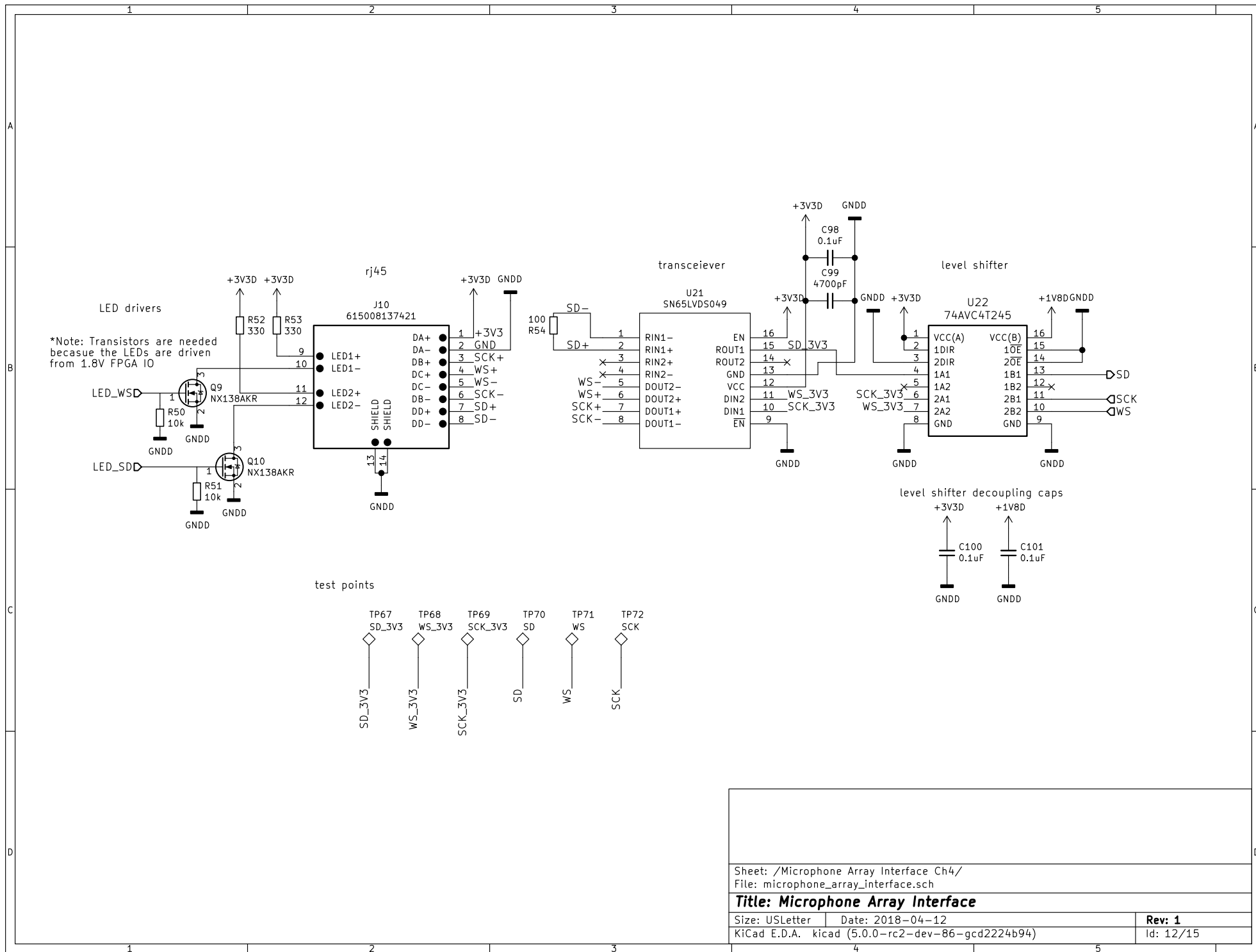
Size: USLetter Date: 2018-04-12

KiCad E.D.A. kicad (5.0.0-rc2-dev-86-gcd2224b94)

Rev: 1

Id: 10/15





Sheet: /Microphone Array Interface Ch4/
File: microphone_array_interface.sch

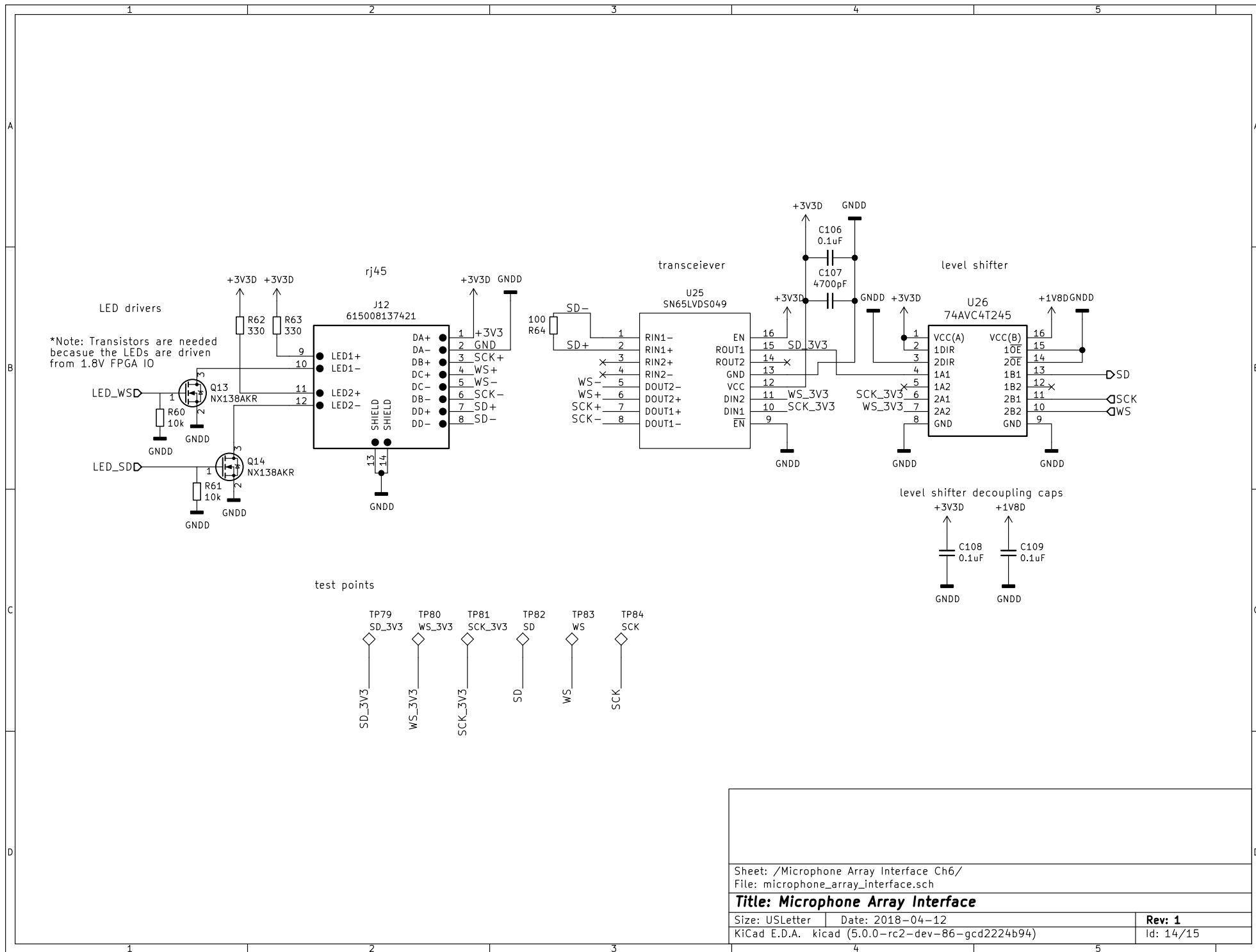
Title: Microphone Array Interface

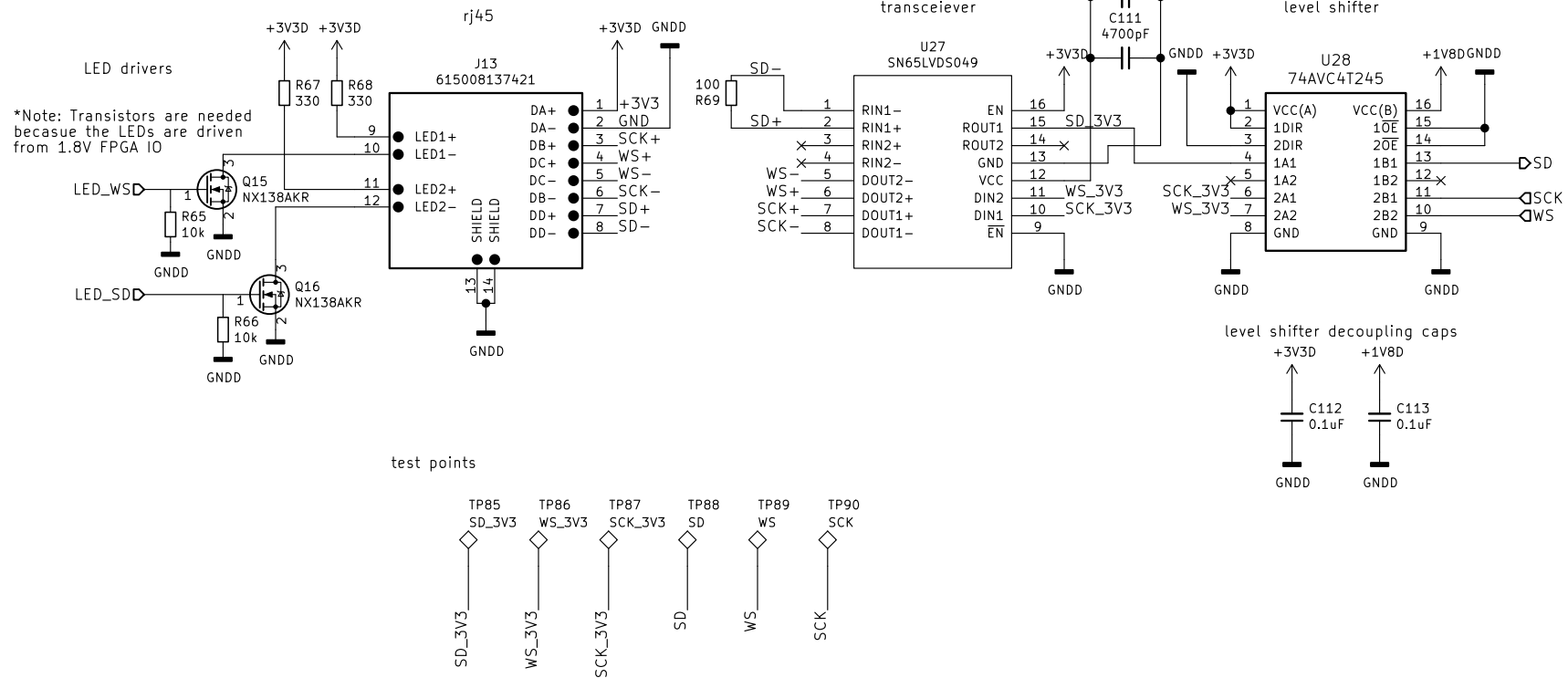
Size: USLetter Date: 2018-04-12

KiCad E.D.A. kicad (5.0.0-rc2-dev-86-gcd2224b94)

Rev: 1

Id: 12/15





Sheet: /Microphone Array Interface Ch7/
File: microphone_array_interface.sch

Title: Microphone Array Interface

Size: USLetter Date: 2018-04-12

KiCad E.D.A. kicad (5.0.0-rc2-dev-86-gcd2224b94)

Rev: 1

Id: 15/15