

## Exercise 21: Split Your Team (`cmd_proc` / `cmd_proc_vld` / `piezo`)

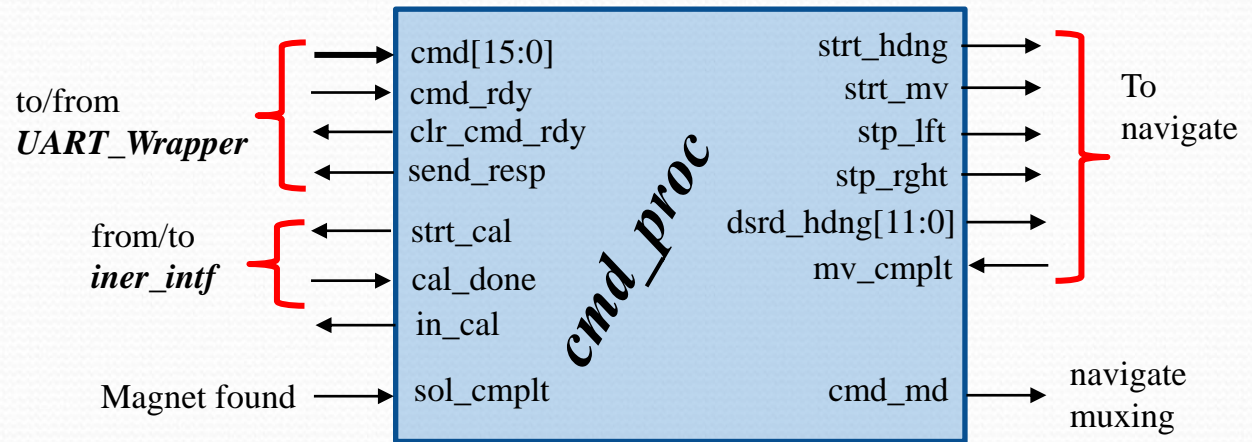
- Recommended Split
  - 2 people on **`cmd_proc`** implementation & test
  - 2 people on **`piezo_drv`** implementation & test
- For this exercise you will split your team and either work on **`cmd_proc`** & testing or on the unit to drive “Charge!” fanfare on the piezo buzzer. They are of similar difficulty.
- Decide how you are splitting roles and jump to the appropriate section of this document. **`cmd_proc`** is covered first.

## Exercise 21: (cmd\_proc)

The *cmd\_proc* unit's main job is to process incoming commands from Bluetooth (*UART\_Wrapper*). The command could be calibrate gyro, a heading/movement command, or a solve maze command

When any new **cmd[15:0]** is **rdy** the *cmd\_proc* unit will

dispatch from its IDLE state to a state to “execute” the command and assert **clr\_cmd\_rdy** in the process. If the command is calibrate then **strt\_cal** will be asserted for 1 clock and the unit will wait for **cal\_done** to be asserted. During this time **in\_cal** should be asserted. When **cal\_done** is asserted the unit should **send\_resp** and return to IDLE. If a heading command is received the heading direction should be registered into **dsrd\_hdng** and **strt\_hdng** should be asserted for 1 clock. When the mazeRunner is at the new heading **mv\_cmplt** will be asserted and the unit will **send\_resp** and return to IDLE. If a movement command is received **strt\_mv** should be asserted for 1 clock, and {**stp\_lft**,**stp\_right**} should be registered from **cmd[1:0]**. When *navigate* is done executing the move **mv\_cmplt** will be asserted. **stp\_lft/stp\_right** indicate the movement should terminate at a left or right opening in the maze if discovered (by IR sensors). Finally a solve command will simply have the unit deassert **cmd\_md** (should have been high otherwise) and wait for **sol\_cmplt** to be asserted. The “intelligence” of solving the maze is in a unit called *maze\_solve*.



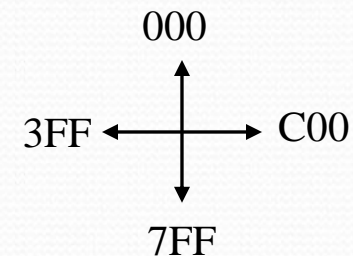


## Exercise 21: (cmd\_proc)

The main job of *cmd\_proc* is to interpret incoming commands. The command opcode is contained in **cmd[15:13]** there are currently only 4 commands “The MazeRunner” can interpret.

<b>cmd[15:13]</b>	<b>Description:</b>
3'b000	Calibrate command (kick off <b>strt_cal</b> and wait for <b>cal_done</b> ). <b>send_resp</b>
3'b001	Heading command. Bits [11:0] represent the desired heading
3'b010	Move command. Bit[1] if set implies movement should stop at a left opening. Bit[0] if set implies movement should stop at a right opening.
3'b011	Start solve command: Deassert <b>cmd_md</b> and wait for <b>sol_cmplt</b> . <b>cmd[0]</b> determines affinity. 1→ left affinity, 0→ right affinity.
3'b1xx	Reserved for future expansion.

<b>cmd[11:0] → Heading</b>	<b>Direction:</b>
12'h000	North
12'h3FF	West
12'h7FF	South
12'hC00	East



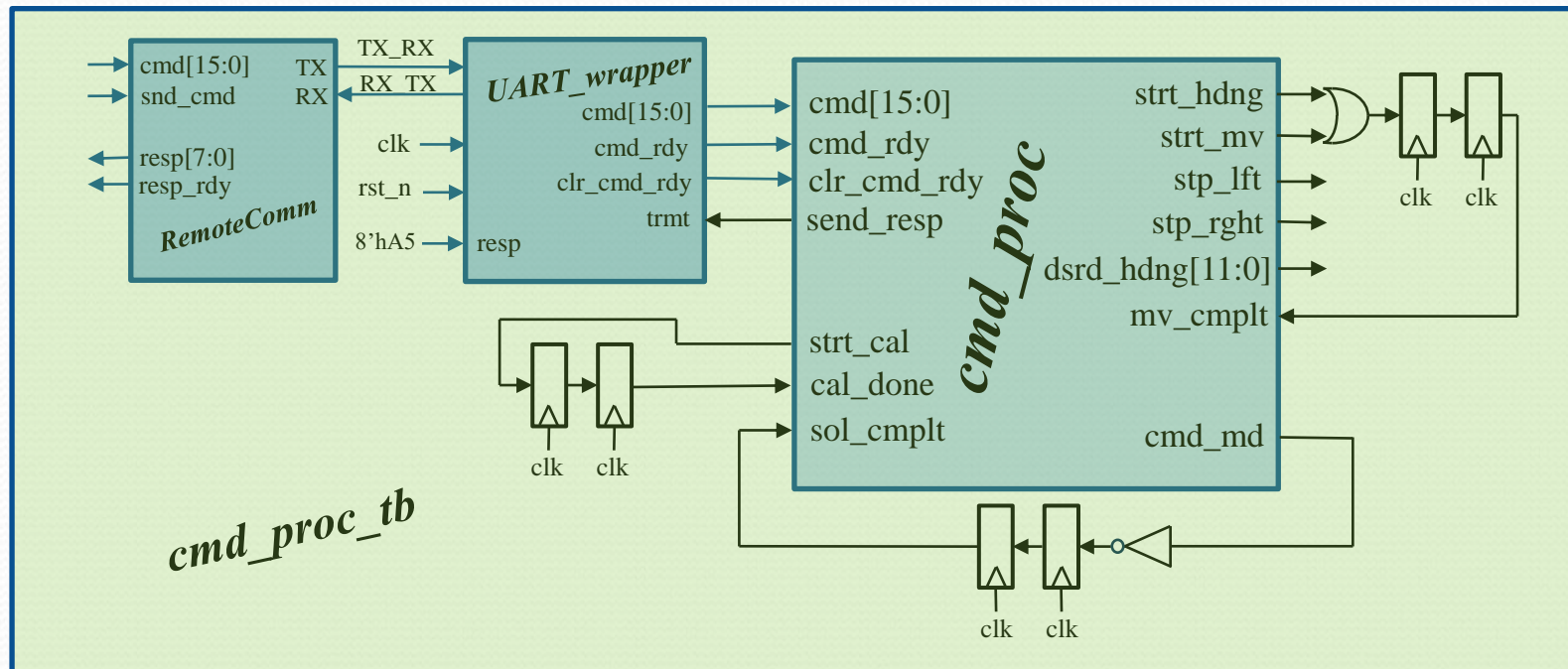
## Exercise 21: (cmd\_proc) (SM Control)

Extract the needed states from this description:

- The machine sits in *IDLE* until there is a **cmd\_rdy**. Then it should dispatch to the appropriate state based on the opcode (**cmd[15:13]**). It should immediately **clr\_cmd\_rdy**,
- If the command is to calibrate is simply asserts **strt\_cal** and waits for **cal\_done**. It should also **send\_resp** (*0xA5 positive ack*) when **cal\_done**.
- If a heading command is given it must capture the desired heading (*into dsrd\_hdng register*) and assert **strt\_hdng** for a single clk. Then it waits for **mv\_cmplt** to be asserted, **sending** a **response** once it is.
- If a movement command is given it must assert **strt\_mv** and register if the movement should stop at a left or right opening in the maze (**stp\_lft/stp\_rght**). When the *navigate* unit is done executing the movement it will assert **mv\_cmplt** and **cmd\_proc** should send a response.
- If a solve maze command is received it must simply deassert **cmd\_md** (*which had been high otherwise*) and wait for the **sol\_cmplt** signal to assert. **sol\_cmplt** asserts when the hall sensor detects a magnet. **cmd\_md** forms a mux select which determines if control signals to the *navigate* unit come from **cmd\_proc**, or the *maze\_solve* unit.



## Exercise 21: (cmd\_proc) (Testing it)



Since *cmd\_proc* is so central to the design it has an extensive interface. For blocks like this it can be good to instantiate in a testbench with some context. In this recommended context you can apply a 16-bit command to *cmd[15:0]* into *RemoteComm* and then assert *snd\_cmd* for one clock. This will send the command and you can check for its proper operation.

**NOTE:** the use of flops to provide **cmplt/done** signals a few clks after their respective **str\_t** signals.

## Exercise 21: (cmd\_proc) (Testing it)

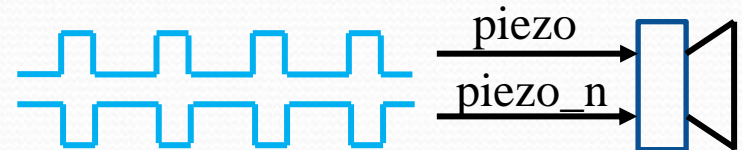
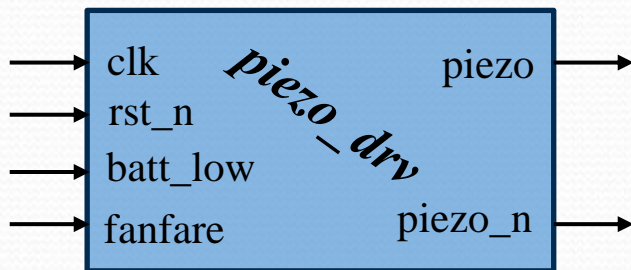
There is a lot to test with *cmd\_proc*, and some of it might be better tested at the actual “fullchip” level when a physics model of the mazeRunner and maze are available. However, the following are tests you should consider performing at this level:

Sequence:	Description:
1	Initialize all inputs to known values and assert/deassert reset
2	Send Calibrate command (0x0000). Wait for <b>cal_done</b> or timeout if it does not occur Wait for <b>resp_rdy</b> or timeout if it does not occur
3	Send heading command and look for <b>strt_hdng</b> at time of <b>cmd_rdy</b> . One clock later check that <b>dsrd_hdng</b> is heading direction you sent. Check that it sends a 0xA5 response when <b>cal_cmplt</b> occurs.
4	Send move command and look for <b>strt_mv</b> at time of <b>cmd_rdy</b> . One clock later check that either <b>stp_lft</b> or <b>stp_rght</b> ( <i>depending</i> ) is asserted.
5	Send solve command and look for <b>cmd_md</b> to be low one clock after <b>cmd_rdy</b> .



## Exercise 21: piezo\_drv.sv (Fanfare & Battery Low)

- There are two sound sequences the *mazeRunner* will make:
  - “Charge!” fanfare when the magnet is found.
  - The quarter notes G6, C7, E7 continuously when the battery is low.
- Battery low has priority over magnet found.
  - If both are asserted battery low wins
- Once “Charge!” is started it is allowed to complete (*no need to preempt “charge!” if battery becomes low*).



A piezo bender is a “speaker” that can be driven with the GPIO’s of our FPGA. We simply drive with a square wave of the frequency we want to generate a tone for.

The duty cycle is not so important (anything from 20% to 80% will do). We will drive differentially for increased amplitude.

## Exercise 21: (Charge! Fanfare)

- When the mazeRunner encounters the magnet it will play “Charge!” fanfare.



- NOTE:** The battery low sound (*G6, C7, E7 played for  $2^{23}$  clocks each*) is comprised of 3-notes that were already part of “Charge!”
- You will need two counters in the design. A note frequency counter, and a note duration counter.

Note:	Freq:	Duration:
G6 (G in octave 6)	1568	$2^{23}$ clocks
C7 (C in octave 7)	2093	$2^{23}$ clocks
E7	2637	$2^{23}$ clocks
G7	3136	$2^{23} + 2^{22}$ clocks
E7	2637	$2^{22}$ clocks
G7	3136	$2^{24}$ clocks

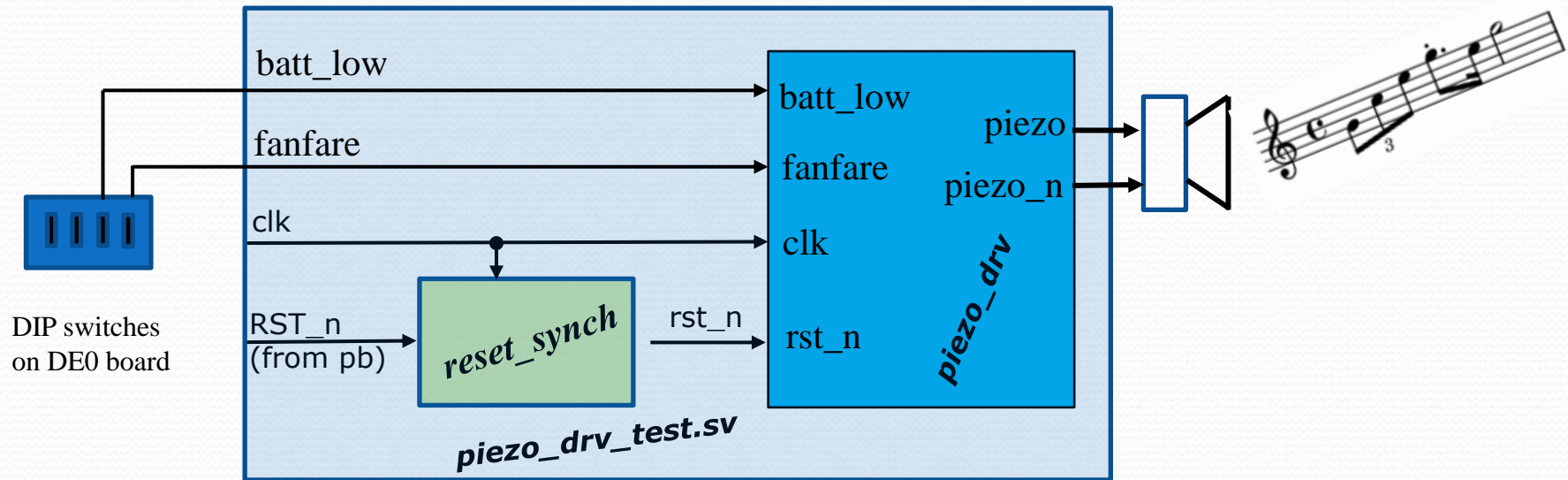
Signal:	Dir:	Description:
clk,rst_n	In	50MHz clk & asynch reset
batt_low	In	Asserted if battery voltage is low ( <i>has priority over fanfare</i> )
fanfare	In	Asserted if magnet has been found
piezo,piezo_n	out	Drive the piezo buzzer with desired note frequency for desired duration.



## Exercise 21: `piezo_drv.sv` (Charge! Fanfare & batt\_low)

- The duration of the notes ( $2^{23}$  clock cycles) is quite long for simulation purposes (*especially when we get to fullchip simulations where `piezo_drv.sv` will be one of many blocks being simulated*).
- We will need a method to speed up simulations. `piezo_drv.sv` should employ a parameter (called **FAST\_SIM**). **FAST\_SIM** should be defaulted true. When **FAST\_SIM** is passed a 0 then durations should be as specified. When **FAST\_SIM** is true then the duration of notes should be 1/16 their normal length. **HINT**: use a **generate if** statement to create an amount by which you increment your duration counter. Increment by 1 or by 16 depending on **FAST\_SIM**.
- Create a simple testbench (`piezo_drv_tb.sv`) that simply instantiates the DUT, applies clock and reset and asserts **fanfare** for 1 clk. Note **piezo** and **piezo\_n** should not be toggling before **fanfare** is asserted or after the “tune” has completed. Then the testbench should assert **batt\_low** and the G6, C7, E7 continuously.
- Once your testbench is passing (visual inspection of **piezo/piezo\_n**) move on to the next portion (testing with DE0).

# Exercise 21: (Charge! Fanfare...testing on DE0)



- Create **piezo\_drv\_test.v** to map your **piezo\_drv.v** to the DE0 and test in “real life”. Don’t forget to pass a **FAST\_SIM** parameter of 0 to charge since we are testing it on real HW now.

Signal:	Dir:	Description:
clk	in	50MHz clock
RST_n	in	Unsynchronized input from push button
Batt_low	In	Indicates battery is low
fanfare	in	Magnet has been found, “Charge!” fanfare should be played
piezo/piezo_n	out	Differential drive of piezo bender



## Exercise 21: (Charge! Fanfare...testing on DE0)

- There are Quartus project file and settings file available for download: (**piezo\_drv\_test.qpf**, **piezo\_drv\_test.qsf**).
- Open the .qpf and **ensure you add** all necessary files to the project.
- Ensure the project builds in Quartus with no errors
- Once it does call Eric, Harish, or Khailani over for a demo with the DE0.
- All groups must demo a working **piezo\_drv.sv** block