Lab 2

Due Sep 29 by 5pm   **Points** 100   **Submitting** a file upload
File Types zip, png, jpg, and jpeg   **Available** Sep 16 at 12am - Sep 29 at 5pm

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This assignment was locked Sep 29 at 5pm.

Link ⤴ **(https://docs.google.com/document/d/19j7bmxT-Rn2eWbmVPJX0xj4AVHcudgp_FWkltQ0IvGY/edit?usp=sharing)** to Lab 2 Manual

Files to Turn In

File Name	Description
03_LED-GREEN.png	A screen capture of your completed 03+:ED-Green.SchDoc page. All components on the page should be clearly visible.
04_LED-YELLOW.png	A screen capture of your completed **04_LED-YELLOW**.SchDoc page. All components on the page should be clearly visible.
05_LED-RED.png	A screen capture of your completed **05_LED-RED**.SchDoc page. All components on the page should be clearly visible.
06_MCU.png	A screen capture of your completed **06_MCU**.SchDoc page. All components on the page should be clearly visible.
08_Power.png	A screen capture of your completed **08_Power**.SchDoc page. All components on the page should be clearly visible.
validate.png	A screen capture of your messages window after you have compiled the design and fixed any errors.
Lab2.zip	A zip file containing all of your Altium files.

Lab 2 Rubric

Criteria	Ratings	Pts
All Nets Have Names - Conflicting net name - Missing net name - Wrong power net name		20 pts
Off sheet Connectors ALL off sheet connectors connected to appropriate pins.		10 pts
Unused Pins Have No ERC Connectors - Missing connector		10 pts
Schematics are Annotated - Missing/incorrect annotation		10 pts
03_Green, 04_Yellow, 05_Red The schematic sheets are drawn correctly.		20 pts
06_MCU Schematic The schematic sheet is drawn correctly.		20 pts
Component Values All resistors, capacitors, and inductors have visible values.		5 pts
Readability Schematics are neat and organized		5 pts
Image Files Submitted No points are awarded, but if files are not submitted, -15pts		0 pts
Zip File Submitted No points awarded, but if file is not submitted (-25pts)		0 pts
		Total Points: 100