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Lab3

Due Oct 15 by 11:59pmPoints 100Submitting a file uploadFile Types png, jpg, jpeg, and zipAvailable Sep 30 at 12am - Oct 15 at 11:59pm

This assignment was locked Oct 15 at 11:59pm.

<u>Link</u> ⊕ (https://docs.google.com/document/d/1ISRI4iWneX9p4tLqn-GVA2ID-vETNmBskya3IsR9fn0/edit? usp=sharing) to Lab 3 Manual.

ECE315.OutJob (https://canvas.wisc.edu/courses/361753/files/34630756?wrap=1) \downarrow (https://canvas.wisc.edu/courses/361753/files/34630756/download?download_frd=1)

Files to Turn In

File Name	Description
DesignRuleCheck.png	A screen capture of your Design Rule Check.
Routing.png	A screen capture of your Top Layer of the PCB.
Lab3.zip	A zip file containing all of your Altium files.

Lab 3 Rubric

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Criteria	Ratings	Pts
Bypass Capacitors Close To Power Pins		40.1
- Caps within 6mm of associated power pin		10 pt
Components Placed on 50mil Grid		40.1
- not on 1mm grid		10 pt
Parts are Rotated to Reduce Rat's Nest		45 1
Related parts should be located near one another and rotated to help simplify routing.		15 pts
Reset Button, User Button, and Buzzer placed on the bottom side of the board		10 pt:
Reference Designators		40.1
- ref designators DO NOT overlap other parts or designators.		10 pts
Power Polygons are Present		10 pts
Name Found on PCB		10 pts
All Nets Are Routed		5 pts
Design Rule Check Has No Errors		10 pts
Orthogonal Routing of Tracks		5 pts
Quality and Effort		
Subjective evaluation of the layout/routing		5 pts