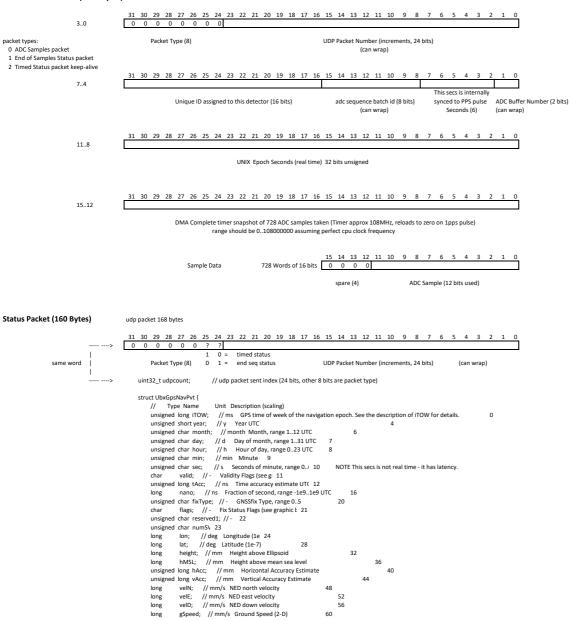
ADC SAMPLE PACKET (1472 Bytes)



```
long heading; // deg Heading of motion 2-D (1 64
                                         unsigned long sAcc; // mm/s Speed Accuracy Estimate 68
                                         unsigned long headingAcc; // deg Heading Accu 72
                                         unsigned short pDOP; // - Position DOP (0.01)
                                         short reserved2; // - Reserved
                                                                                                          78
                                         unsigned long reserved3; //- Reserved
                                                                                                          80
                                         } NavPvt:
                                      31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3
                   clktrim
                                      average of STM32 clock frequency referenced to 1pps
                                                                                                   15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
                                     uint16_t uid;
                                                            // 16 bits used
                                                                                                     Unique ID assigned to this detector (16)
                                                                                                   15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
                                                         // 16 bits used
                                     uint16_t adcpktssent;
                                                                                              ADC number of buffers sent in last capture sequence before status packet (16)
                                                                                                     // Number of ADC pks sent in this trigger event
                                                         adctrigoff : uint16_t;
                                                                                                                    adc trigger offset (above the noise) 12 bits
                                                                                                  15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
                                                         adcbase : uint16_t;
                                                                                                                     adc average base level (ie dc level)
                                                            uint32_t sysuptime;
                                                                                  // number of seconds system up from boot uptime
                                                            uint32_t netuptime;
                                                                                   // number of seconds network up
                                                            uint32 t gpsuptime;
                                                                                  // number of seconds gps locked
                                                                                  // STM firmware major ver
                                                            uint8_t majorversion;
                                                            uint8_t minorversion;
                                                                                  // STM firmware minor ver
                                                                                                   15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
                                                         adcnois: uint16_t;
                                                                                                                     adc average window noise level
                                  31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
uint32 t auxstatus1; AUXSTATUS1
                                                 16 bits reserved
                                                                                                  detection jabbering (8 bits)
                                                                                                                                adc sequence last batch id (8 bits)
                                                                                                    0 = no jabber
                                                                                                                                   (can wrap)
                                                            uint32_t adcudpover;
                                                                                      // adc -> udp send overruns
                                                            uint32_t trigcount;
                                                                                      // adc trigger count
                                                            uint32_t udpsent;
                                                                                      // udp sample packets sent
                                                                                      // unused; was peak trig level
                                                            uint16_t reserved3
                                                            uint16_t jabcnt;
                                                                                      // jabber counter
                                  31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 0
uint32_t temppress TEMPPRESS
                   Data in MPL3115A2 datasheet Temperature (12 bits)
                                                                                                  Pressure (20 bits)
                                                            uint32_t epochsecs;
                                                                                      // unix epoch seconds
                                                            uint32_t reserved1;
                                                                                   // spare
                                                            uint32_t reserved2;
                                                                                  // spare
                                     Any new fields to be added here .....
       end sentinal marker (keep at the end)
                                                            uint32_t telltale1;
                                                                                  // end of status packet marker
                                                                                                                        0xFEEDCODE
```