REV	Description	DATE	BY
v1	First release PCB version is v1	3/6/2015	QXN
v1c	1.Changed the SRAM to the MT41K256M16TW-107:P. 2.Changed the eMMC to the MTFC4GACAJCN-1M WT. 3.Changed the PCB version to v1c	3/22/2016	QXN
v1.1	1. Page 3: Added series resistor (R32,R33,R37-R44)=33R to the MMC1 lines, C6=DNI(10pF) to the MMC1_CLK to DGND; Changed C24 from 2.2uF to 100nF; Changed C30 from 100nF to DNI; Add D6 and D7 to Reset lines; Removed U17.  2. Page 4: Changed USB1_VBUS series resistor from FB8=0.1R 0805 to R36=0R 0402.  3. Page 8: Connected the Pin A6 J5 of U13(eMMC IC) to DGND.  4. Page 9: Added AM3358 GPIO1_8 and a Positive-AND gate(U1) to reset ethernet LAN8710A; Added series resistor (R149,R158,R166,R171,R172)=33R to the LAN8710A TX lines;  5. Page 10: Changed MicroSD Connector from ALPS_SCHA5B0200 to YULIANG_YL004-030-001; Added series resistor (R2,R13,R15,R16,R21,R31)=33R, C5=DNI(10pF) to the MMC0 lines.  6. The PCB Revision for this board is Rev v1.1.	5/24/2021	FJN

PAGE NO.	SCHEMATIC PAGE
1	COVER PAGE
2	POWER MANAGEMENT
3	PROCESSOR 1 OF 3, JTAG HEADER
4	PROCESSOR 2 OF 3, UAB PORTS
5	PROCESSOR 3 OF 3
6	LED, CONFIGURATION AND BUTTON
7	DDR3 MEMORY
8	eMMC FLASH
9	10/100 ETHERNET
10	EXP CONN, uSD

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Title	BeagleBone	Green Cover F	age age		
Size	Document Num	ber			Rev
в					v1
Date:	Monday, May	24, 2021	Sheet 1	of 10	
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