VTR7 designs							
	Two dies		Three dies		Four dies		
Design	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	
bgm	651.96	594.05	649.21	467.81	702.93	391.48	
blob_merge	61.49	35.73	61.32	41.61	66.02	48.35	
boundtop	45.5	40.93	45.87	46.64	48.85	51.22	
diffeq1	6.59	3.97	6.86	4.84	7.15	6.01	
diffeq2	5.05	3.35	5.09	1.43	6.15	1.45	
LU32PEEng	2560.43	1145.24	2753.01	1142.46	3374.38	1150.39	
LU8PEEng	431.58	199.69	416.06	200.27	427.37	199.15	
mcml	2209.24	1469.25	2225.12	1483.13	2342.68	1464.41	
mkDelayWorke	85.57	43.62	84.92	43.84	89.87	43.72	
mkPktMerge	9.25	3.49	9.21	3.59	9.59	3.49	
or1200	38.81	17.99	38.19	18.09	38.75	18.11	
raygentop	45.89	41.78	25.97	23.52	26.13	24.05	
sha	23.32	9.72	23.75	10.89	24.09	16.94	
stereovision0	94.79	79.11	97.86	97.78	106.99	104.03	
stereovision1	133.62	117.59	137.11	134.67	140.56	135.39	
stereovision2	594.42	458.72	593.886	431.58	612.89	471.37	

Koios designs							
	Two dies		Three dies		Four dies		
Design	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	
bnn	7427.5	3008.9	6721.21	5140.27	6252.91	4282.01	
bwave_like.fixe	471.49	281.23	446.79	420.51	414.93	458.33	
clstm_like.medi	20745.77	10825.9	20652.1	20242.5	15214.9	15907.21	
clstm_like.smal	5255.6	5093.1	5281.1	5068.21	5076.41	4440.71	
conv_layer_hls	134.05	189.21	123.89	156.99	150.84	183.54	
dla_like.small	3254.52	2726.65	3938.78	3552.21	3703.49	3600.06	
eltwise_layer	157.69	105.96	155.4	148.85	244.89	121.73	
lenet	196.45	79.26	203.83	166.69	408.18	341.89	
lstm	3795.71	3781.44	2972.13	2906.7	3290.82	3061.76	
reduction_layer	187.81	112.39	132.32	110.51	134.27	130.73	
robot_rl	208.96	155.74	345.67	236.28	352.89	326.08	
softmax	446.34	151.43	270.37	167.15	259.36	242.51	
spmv	243.96	210.17	294.22	218.41	285.84	279.26	
tpu_like.small.o	929.81	880.69	922.17	718.11	984.91	861.58	
tpu_like.small.v	995.31	996.69	969.11	858.31	787.98	724.87	