

VTR7 designs	Two Dies				Routed w
	Routed wirelength		Fmax		
	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	
Design	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR
bgm	591664	585419	44.51	48.89	586332
blob_merge	86514	85914	105.24	108.13	85561
boundtop	29501	28669	159.46	162.24	30490
diffeq1	9389	9005	48.71	49.12	9732
diffeq2	6798	6721	60.7	63.42	7404
LU32PEEng	1699741	1671463	9.19	9.63	1744322
LU8PEEng	415688	405419	8.94	9.12	412923
mcml	1124166	1015142	13.71	14.26	1143349
mkDelayWorker32B	110496	107548	144.26	146.2	108944
mkPktMerge	13644	12469	258.24	259.42	13587
or1200	48998	47476	75.19	76.28	50379
raygentop	28619	27412	194.69	207.98	27969
sha	20984	20994	79.62	78.24	22034
stereovision0	90426	90014	244.15	262.1	93553
stereovision1	160512	154126	177.51	186.79	161302
stereovision2	843896	807082	62.19	67.42	798888

Koios designs	Two Dies				Routed w
	Routed wirelength		Fmax		
	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	
Design					
bnn	2296992	2222412	85.12	86.24	2366846
bwave_like.fixed.small	489862	467824	90.69	95.12	553687
clstm_like.medium	4016482	4012624	69.32	81.88	4198486
clstm_like.small	1634662	1656796	108.14	109.19	1662143
conv_layer_hls	102242	94546	108.21	112.26	101428
dla_like.small	1286428	1342682	91.12	94.84	1384321
eltwise_layer	184864	174485	212.56	216.72	184352
lenet	215544	216442	110.13	109.23	214624
lstm	2104664	1814268	96.88	105.22	2042162
reduction_layer	167462	153486	135.79	138.63	159521
robot_rl	210556	203058	99.21	100.1	216435
softmax	170342	170366	152.42	155.36	174246
spmvs	222481	212864	154.31	156.72	233058
tpu_like.small.os	464263	445674	138.92	138.98	442062
tpu like.small.ws	448432	431542	104.95	105.26	440418

Three Dies			Four Dies			
Wirelength	Fmax		Routed wirelength		Fmax	
	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR
568438	39.32	39.59	584236	528162	39.76	39.82
85272	99.87	102.24	88096	88914	102.82	104.62
28518	155.15	157.329	30484	29982	153.12	156.24
9095	47.23	48.16	9512	8824	48.72	48.68
7172	64.26	66.92	7424	7021	61.72	62.26
1723214	9.43	9.96	1734159	1732294	9.26	9.33
412716	9.44	10.19	405426	405331	8.92	9.02
1144356	13.14	14.24	1138352	1134326	13.92	14.62
108542	137.29	139.82	111552	110284	145.24	147.11
12692	245.39	245.23	13462	12821	259.13	260.24
46246	71.98	73.26	50382	50014	74.67	76.92
25641	212.24	214.31	27826	26143	205.62	206.86
22647	79.29	76.24	22172	20478	73.62	74.24
92393	245.19	246.42	97298	90684	246.38	247.78
160671	179.28	179.98	167649	153552	175.24	179.92
7347288	57.82	62.24	823214	720019	62.19	63.39

Three Dies			Four Dies			
Wirelength	Fmax		Routed wirelength		Fmax	
	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR
2223648	85.21	87.33	2264318	1974646	87.21	89.95
586214	79.19	88.24	456844	461474	79.68	85.92
4169832	83.96	86.19	4364426	4048462	82.71	85.32
1636431	111.2	114.62	1652674	1510798	106.51	109.92
87246	110.16	128.42	108162	80247	113.25	121.26
1333624	96.39	101.26	1321077	1244639	94.12	96.37
171095	209.42	216.39	193694	179528	195.28	214.19
214036	111.24	111.96	232264	232514	97.47	111.42
2033215	88.39	94.17	2103329	1865124	83.56	99.13
159014	135.48	139.1	162525	143653	144.43	147.93
205436	99.19	99.24	227246	200562	95.61	99.26
167532	157.62	159.24	175347	153465	151.78	155.26
224616	141.42	139.39	275619	226414	143.29	146.93
337602	142.36	148.55	451263	393298	139.1	147.25
406520	101.19	108.19	453284	422546	87.92	101.23

