VTR7 designs	Routed wirelength		Fmax		Routed w
Design	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR
bgm	496184	484282	38.72	39.45	487103
blob_merge	86704	82605	100.97	101.12	82019
boundtop	36071	29845	149.55	148.22	35872
diffeq1	15070	14749	46.18	46.45	15483
diffeq2	10754	9801	53.29	57.83	10425
or1200	63914	58763	69.69	71.29	63297
raygentop	38873	35934	183.75	191.55	38388
sha	22745	20524	72.26	72.84	22867

Koios designs	Routed wirelength		Fmax		Routed w
Design	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR
bwave_like.fixed.small	472962	471648	82.15	83.42	561392
conv_layer_hls	114021	102344	101.16	101.94	122584
eltwise_layer	211461	223812	196.94	198.87	208395
lenet	213891	201496	116.89	117.41	215813
reduction_layer	176749	175361	119.06	119.64	172207
robot_rl	218917	216442	92.02	92.75	209939
softmax	175277	150801	150.57	151.88	171741
spmv	231964	224672	136.24	136.19	242127

Three	Dies		Four Dies				
rirelength	Fmax		Routed wirelength		Fmax		
FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	
481111	38.61	39.96	485475	488215	38.75	39.79	
80469	100.66	101.42	82000	80963	99.48	100.26	
31693	147.56	148.16	35303	33428	148.96	149.52	
15112	45.67	45.95	15604	14312	45.88	47.21	
10450	54.84	56.19	10479	9127	56.64	56.83	
61237	69.33	70.09	64832	61779	69.98	70.21	
37477	195.02	192.17	38542	36954	195.02	197.65	
21542	73.25	74.21	24698	23886	72.37	72.54	

Three	Dies		Four Dies				
rirelength	Fmax		Routed wirelength		Fmax		
FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	Default-VTR	FPGAPart-VTR	
561219	60.25	61.19	521265	501919	62.41	65.36	
114362	96.84	97.21	120243	119854	108.73	108.95	
205872	196.45	206.78	211903	210854	198.03	199.15	
215801	112.67	113.17	215368	215262	117.72	118.76	
154941	128.25	143.14	173740	164217	123.72	144.52	
220126	92.54	92.62	215686	221028	93.06	94.22	
156219	152.66	155.42	176786	154264	143.24	150.75	
232943	122.65	124.22	262142	253986	126.45	125.19	