

# Position-sensitive device

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# 1 Introduction

## 2 Position-sensitive detector

[1]

Parameter	Symbol	Values		Unit
		Typical	Maximum	
Dark current	$I_d$	0.5	10	nA
Interelectrode resistance	$R_e$	7	15	k $\Omega$
Terminal capacitance	$C_t$	150	300	pF

Table 1: Important parameters of the S5990 extracted from the datasheet [2].

### 3 Preamplifier

The photocurrents created by our detector are in the range of microampere where they are vulnerable to noise. Using a preamplifier, we can increase the amplitude of the signal for an improved signal-to-noise ratio. The typical photocurrent preamplifier is based on the transimpedance (current-to-voltage) amplifier design using a voltage-feedback operational amplifier. Converting the current to a voltage signal has the benefit that the voltage signal can be easily visualized with an oscilloscope. Furthermore, the voltage-feedback operational amplifier design appears to be more common than the current-feedback operational amplifier, as manufacturers offer much more choice and they are more prominent in the literature. That said, current-feedback operational amplifiers are reported to be a viable solution for high-speed and high-bandwidth applications, see Ref. [3, p. 110] for an overview of the benefits of current-feedback amplifiers and Ref. [4, Ch. 9] for a comparison with voltage-feedback amplifiers.

In the following, we will always refer to the voltage-feedback operational amplifier if not noted otherwise. Moreover, we expect the reader to be familiar with the foundations of the operational amplifier. Well-written introductions to this topic can be found in [3, Ch. 1] and [4, Ch. 3].

#### 3.1 Basic design

The basic design of a transimpedance amplifier is illustrated in Figure 1. Ignoring imperfections of the photodiode, we can represent the photodiode as a current source. The non-inverting input of the operational amplifier is connected to ground. The inverting input is coupled with the output of the operational amplifier via a feedback impedance  $Z_f$ .

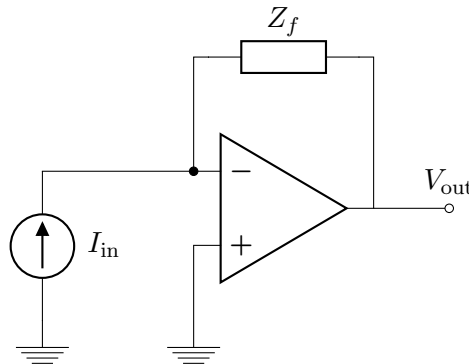


Figure 1: Basic transimpedance amplifier using voltage feedback operational amplifier.

The ideal operation amplifier has zero input current, thus, in the node of the inverting input of the operational amplifier Kirchhoff's law states that the current going through the feedback impedance has to cancel the current of the current source  $I_{in}$ . The current through the feedback impedance  $Z_f$  can be expressed in terms of the feedback impedance  $Z_f$  and the output voltage  $V_{out}$  of the operational amplifier through the use of Ohm's law, yielding,

$$V_{out} = -Z_f I_{in}. \quad (1)$$

Given a maximum current  $I_{in}$  and a desired maximum output voltage  $V_{out}$ , Equation (1) determines the feedback impedance. Limitations arise for real operational amplifiers where the output voltage is limited to be below the supply voltage of the operational amplifier.

Aside from photodiode applications, it is more common to find the inverting (voltage-to-voltage) operational amplifier in the literature. By using the source transformation on the current source with a parallel impedance in the transimpedance amplifier circuit, we can recover the inverting operational amplifier circuit, and thereby easily use results obtained for the inverting amplifier design.

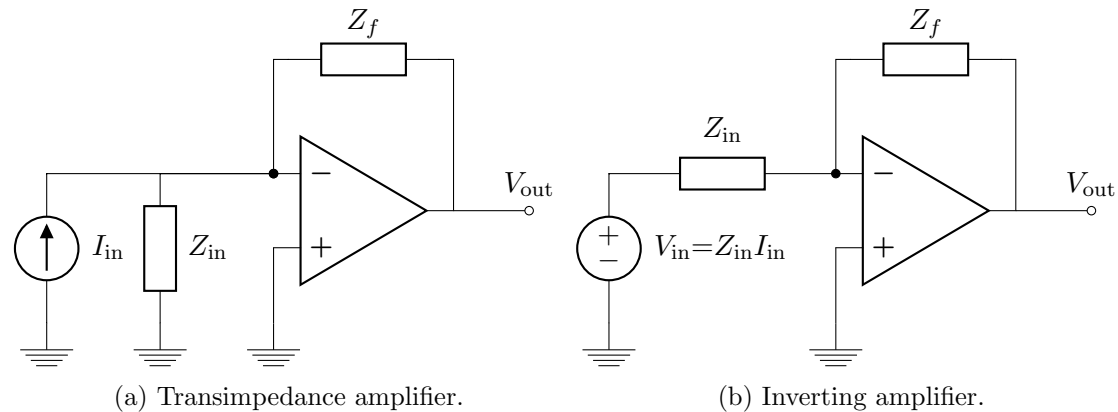


Figure 2: Equivalence between transimpedance and inverting amplifier using source transformation.

Figure 2 shows the source transformation applied to transimpedance and inverting amplifier circuits. Given a current source  $I_{in}$  with parallel impedance  $Z_{in}$  the equivalent voltage source has value  $V_{in} = Z_{in} I_{in}$  with impedance  $Z_{in}$  in series.

### 3.2 Input offset voltage

Real operational amplifiers only reduce the voltage difference between the inverting and non-inverting input to a non-zero input offset voltage. For high-precision operational

amplifiers, the input offset voltage is in the range of microvolts. We can model the input offset voltage as a voltage source at the non-inverting input of an ideal operational amplifier in our transimpedance circuit, as can be seen from Figure 3. In Figure 3 we amended the basic transimpedance circuit of Figure 1 by inserting a voltage source with the input offset voltage between the non-inverting input and ground.

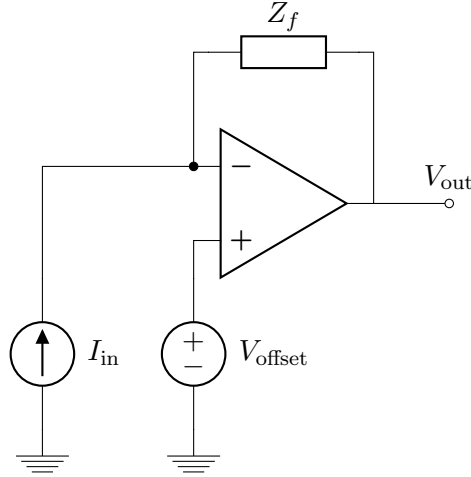


Figure 3: Input offset voltage in the transimpedance amplifier.

In order to estimate the output offset  $V_{out}$  caused by the input offset voltage  $V_{offset}$  we adapt the picture of the inverting amplifier. Figure 4 shows an inverting amplifier circuit with input impedance  $Z_{in}$  and input offset voltage  $V_{offset}$  at the non-inverting input of the operational amplifier. According to the superposition theorem, we can replace the input voltage source with a short circuit to estimate the contribution of the input offset voltage source.

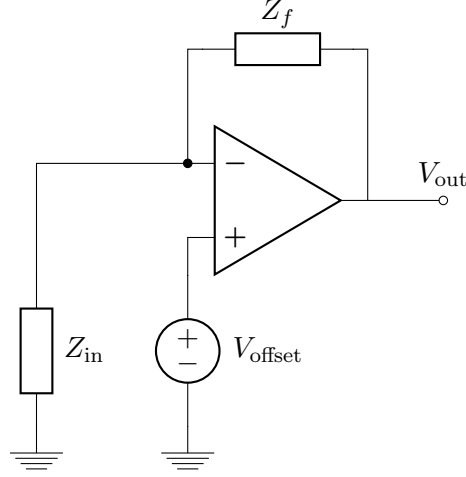


Figure 4: Input offset voltage in the inverting amplifier.

From Figure 4 we identify input and feedback impedance as a voltage divider in which the input and output nodes are exchanged. The respective transfer function reads,

$$V_{\text{out}} = -\frac{R_f + R_{\text{in}}}{R_{\text{in}}} V_{\text{offset}} = -\left(1 + \frac{R_f}{R_{\text{in}}}\right) V_{\text{offset}}. \quad (2)$$

Comparing Equation (2) with Equation (1) discloses a difference in gain. The gain of the input signal in Eq. 1 is commonly referred to as signal gain. The gain of a signal applied directly to the inputs of the operational amplifier is referred to as noise gain. In the case of the position-sensitive detector, the input resistance is of order  $10 \text{ k}\Omega$ . Using a feedback resistance of  $R_f = 100 \text{ k}\Omega$  we find that the input offset voltage experiences a gain of 11.

One approach to compensate for the input offset voltage as described is depicted in Figure 5, see also Ref [3, p. 54]. A potentiometer with maximum resistance  $R_p$  connects the positive and negative supply voltage. The wiper of the potentiometer is connected with a first resistor  $R_1$  to a node. The node is connected with a second resistor  $R_2$  and an optional bypass capacitor to ground. Finally, the equivalent input offset voltage source connects the node with the non-inverting input of the operational amplifier.

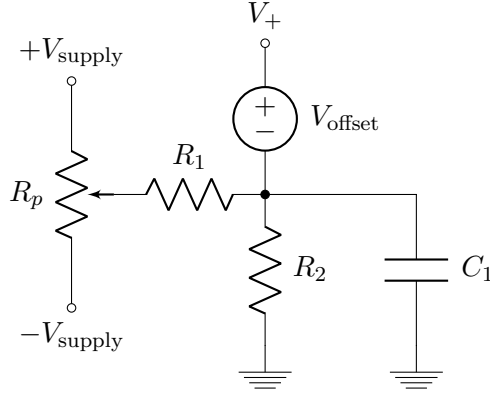


Figure 5: Input offset voltage compensation using adjustable potentiometer.

Let  $0 \leq x \leq 1$  be the position of the potentiometer. For  $x = 1/2$  the resistance of the potentiometer is  $R_p/2$  and there is no offset compensation. For  $x < 1/2$  the input offset compensation is negative to compensate for a positive input offset voltage. For  $x > 1/2$  the input offset compensation is positive to compensate for a negative input offset voltage. The maximum input offset compensation is obtained for  $x = 0$  and  $x = 1$ . Using circuit analysis techniques, we obtain,

$$V_c(x) = \frac{R_2(1 - 2x)}{R_1 + R_2 - R_p(1 - x)x} V_{\text{supply}}, \quad (3)$$

as an analytical expression for the input offset voltage compensation measured between the node and ground. The maximum absolute value of the compensation voltage follows to be,

$$V_c(0) = V_c(1) = \pm \frac{R_2}{R_1 + R_2} V_{\text{supply}}. \quad (4)$$

Given a maximum input offset voltage of  $100 \mu\text{V}$  and a supply voltage of  $15 \text{ V}$ , we find approximate resistor values  $R_1 = 3 \text{ M}\Omega$  and  $R_2 = 2 \Omega$ . The resistor value of the potentiometer  $R_p$  should be chosen sufficiently large such to limit the current. For example, a potentiometer resistance of  $R_p = 15 \text{ k}\Omega$  would limit the current to  $2 \text{ mA}$  with a heat dissipation of  $60 \text{ mW}$ . In practice, one should aim for a slightly higher maximum compensation voltage in order to handle resistor mismatches.

That said, there are some practical arguments against the use of the described input offset voltage compensation. The first argument is that the low resistance of  $R_2$  acts as a dominant source for thermal current noise density of about  $100 \text{ pA}/\sqrt{\text{Hz}}$ . As this current noise contributes to the input of the transimpedance amplifier it gets amplified by the feedback impedance  $Z_f$ , yielding up to  $110 \mu\text{V}/\sqrt{\text{Hz}}$  in voltage noise density, which — depending on the bandwidth — surpasses the actual input offset voltage to compensate. The second argument is that high-precision potentiometers with large dynamic range get very large and difficult to accommodate on a printed circuit board.



### 3.3 Input bias current

In addition to the input offset voltage  $V_{\text{offset}}$ , there is a second effect that causes an offset in the output voltage  $V_{\text{out}}$ . This second effect stems from the small amount of current that is drawn from the inputs of the operational amplifier. We highlighted the input currents in Figure 6 where they are represented by the current flows  $i_+$  and  $i_-$  directly pointing into the inputs of the operational amplifier in the transimpedance circuit.

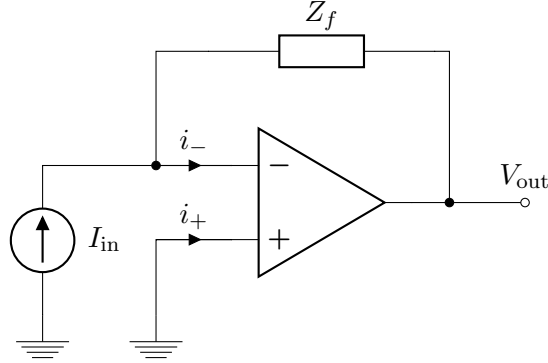


Figure 6: Input bias current offset in the transimpedance amplifier.

Input bias currents for high-precision operational amplifier are in the range of picoamperes. These small currents are usually difficult to measure and may differ between inputs. The datasheet of the operational amplifier, therefore, doesn't report the input bias current per terminal but the mean  $I_{\text{bias}}$  and the difference between the input currents  $I_{\text{offset}}$ . The relationship between the mean input bias current  $I_{\text{bias}}$  and the input offset current  $I_{\text{offset}}$  with respect to the actual input bias currents  $i_+, i_-$  is given below.

$$i_+ = I_{\text{bias}} + \frac{1}{2}I_{\text{offset}} \quad I_{\text{offset}} = i_+ - i_- \quad (5)$$

$$i_- = I_{\text{bias}} - \frac{1}{2}I_{\text{offset}} \quad I_{\text{bias}} = \frac{i_+ + i_-}{2} \quad (6)$$

According to Ref. [3, p. 57] and Ref. [5, p. 25] the effect of the input bias currents  $i_+, i_-$  on the output voltage  $V_{\text{out}}$  can be compensated with an impedance  $Z_c$  at the non-inverting input of the operational amplifier. Figure 7 illustrates the inverting amplifier configuration with such a compensation impedance  $Z_c$ .

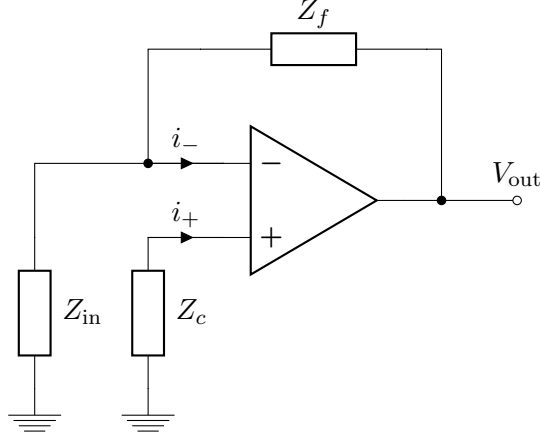


Figure 7: Input current offset compensation.

Following the argumentation from Ref. [6, p. 383], the input bias current at the inverting input of the operational amplifier  $i_-$  develops a voltage of  $Z_f i_-$  over the feedback impedance  $Z_f$ . The input bias current at the non-inverting input  $i_+$  develops the voltage  $Z_c i_+$  over the compensation resistor  $Z_c$ . The voltage  $Z_c i_+$  propagates through the voltage divider comprising the feedback and input impedance analogue to the input offset voltage. According to the superposition theorem, both of these contributions add and yield,

$$V_{\text{out}} = i_+ Z_c \left( 1 + \frac{Z_f}{Z_{\text{in}}} \right) - i_- Z_f, \quad (7)$$

at the output voltage. If  $i_+ \approx i_-$  we can choose,

$$Z_c = \frac{Z_{\text{in}}}{Z_{\text{in}} + Z_f}, \quad (8)$$

to compensate for the output voltage offset caused by the input bias currents. The condition  $i_+ \approx i_-$  is usually satisfied if the input offset current  $I_{\text{offset}}$  reported in the datasheet is less than the mean input bias current  $I_{\text{bias}}$ . High-precision operational amplifier usually have compensated bias currents that do not satisfy this condition. In this case, there is no generic formula for the value of the compensation impedance  $Z_c$  in terms of input and feedback impedance and one needs to perform impedance matching on a per-device basis.

### 3.4 Stability and bandwidth

The outputs of the transimpedance and inverting amplifier designs have a phase shift of  $180^\circ$ . If an additional phase shift of  $180^\circ$  accumulates because of the inherent bandwidth limitations of the operational amplifier and the gain is equal or greater than unity, the

conditions for oscillations are met and the amplifier will become unstable, see Ref. [6, p. 395]. By artificially limiting the bandwidth with an additional frequency pole, we can flatten out the frequency response at high frequencies, and thereby stabilize the amplifier, see Ref. [7, p. 184].

Figure 8 shows a Bode plot of the open-loop (black) and noise gain (red) of an operational amplifier. The noise gain separates into a compensated (solid) and uncompensated (dotted) branch.

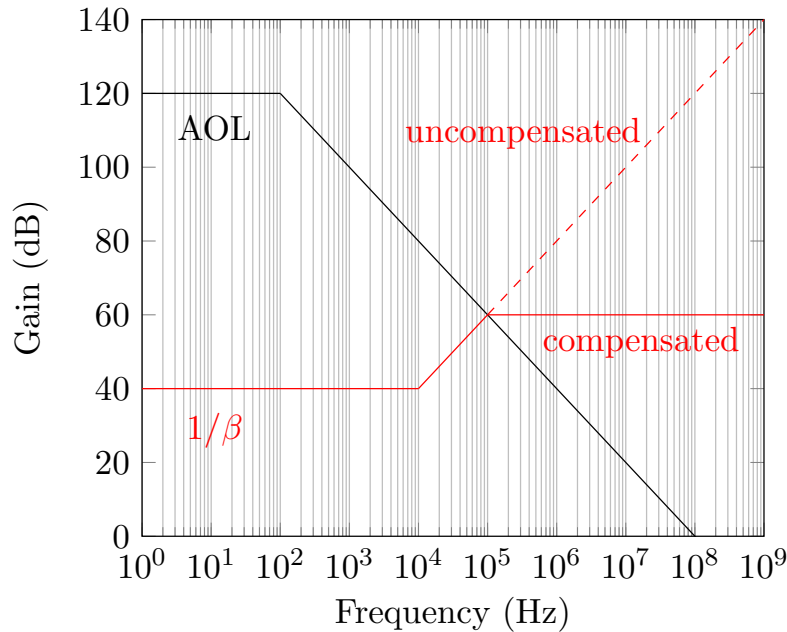


Figure 8: Bode plot of the open-loop and closed-loop gain of the amplifier.

With the uncompensated branch we would observe instability due to oscillations. The compensated branch is obtained through addition of a feedback capacitor. According to Ref. [3, p. 113], Ref. [8, p. 693] and Ref. [5, Ch. 3] the value for the feedback capacitance is given by,

$$C_f \geq \sqrt{\frac{C_{in}}{2\pi R_f f_u}}, \quad (9)$$

wherein  $R_f$  denotes the feedback resistance and  $f_u$  the unity-gain-bandwidth of the operational amplifier. The capacitance should be chosen larger to ensure design margin. The unity-gain-bandwidth of the operational amplifier denotes the frequency where the open-loop gain equals unity and is reported in the datasheet as gain-bandwidth-product (GBP) of the operational amplifier.  $C_{in}$  in Equation (9) represents the sum of the input capacities of the amplifier circuit and the capacitance of the signal source. According to

Ref. [4, p. 185] a more exact formula for the feedback capacitance is given by,

$$C_f \geq \frac{C_{\text{aux}}}{2} \left( 1 + \sqrt{1 + \frac{4C_{\text{in}}}{C_{\text{aux}}}} \right) \quad C_{\text{aux}} = \frac{1}{2\pi R_f f_u}, \quad (10)$$

which is also valid if not  $C_{\text{in}} \gg C_f$ .

Figure 9 illustrates the transimpedance amplifier with capacitive elements. Parallel to the current source  $I_{\text{in}}$  we find the input capacitance  $C_{\text{in}}$  comprising source capacitance and operational amplifier capacitance. Parallel to the feedback resistance  $R_f$  we find the feedback capacitance  $C_f$  with value given in Equation (10) or Equation (9).

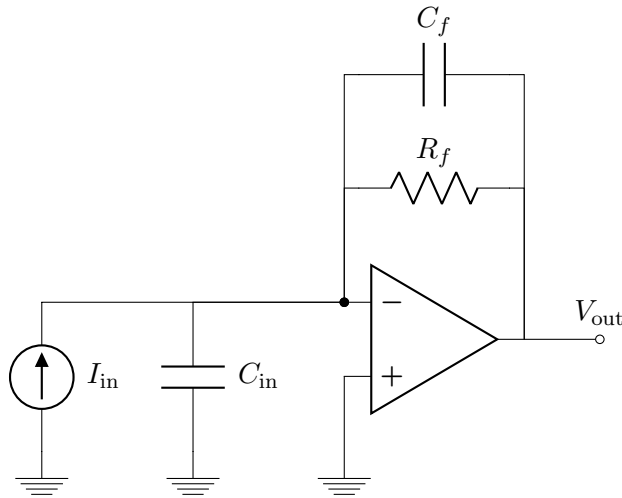


Figure 9: Transimpedance amplifier with capacitive elements.

As noted earlier, the bandwidth is reduced through the additional feedback capacitor. The new bandwidth is now given by the RC pole formed by the feedback impedance,

$$BW = \frac{1}{2\pi R_f C_f}. \quad (11)$$

Given a unity-gain-bandwidth of the operational amplifier of  $f_u = 10$  MHz and a feedback resistance of  $R_f = 100$  k $\Omega$  Equation (9) yields a minimum feedback capacitance of  $C_f = 4$  pF. Together with  $R_f$  this feedback capacitance limits the bandwidth to about 400 kHz.

## Glossary

**GBP** gain-bandwidth-product. 11

**S5990** Hamamatsu two-dimensional PSD. 3

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