# Proiect AC (Mips 32)

Student: Stanculesc Bogdan

Procesorul este functional si a fost testat pe placuta.

Cele 4 instructiuni implementate

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Instrucțiune** | **Opcode**  *Instr[31-26]* | **RegDst** | **ExtOp** | **ALUSrc** | **Branch** | **Br\_?**  (opțional) | **Jump** | **JmpR** (opțional) | **MemWrite** | **MemtoReg** | **Reg Write** | **ALUOp[1:0]** | **function**  *Instr[5-0]* | **ALUCtrl[2:0]** |
| srl | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 000 | 000111 | 110 |
| sra | 000000 | 1 | X | 0 | 0 |  | 0 |  | 0 | 0 | 1 | 000 | 001000 | 111 |
| andi | 001100 | 0 | 0 | 1 | 0 |  | 0 |  | 0 | 0 | 1 | 011 | X | 010 |
| ori | 001101 | 0 | 0 | 1 | 0 |  | 0 |  | 0 | 0 | 1 | 100 | X | 011 |