

# Catapult FPGA board

## Stratix V FPGA

OpenCL Kernels

OpenCL Runtime (BSP)

PCIe DMA

40GE MAC

DRAM  
Controller

Shell

40GE

40GE

DRAM

PCIe x8  
Control plane

Host  
Machine

NIC

ToR  
switch

PCIe x16  
Data plane

