



This quiz is worth a total of **100 points**.

You are allowed to use one sheet of scrap paper. Feel free to request scrap paper from your Teaching Assistants. **Please make sure that all of your answers are contained within the answer boxes or the fill-in lines.** Do not write your work in the answer boxes, **keep all of your work on your scrap paper.** You will **NOT** be given credit for just showing work. Having anything except the answer inside the boxes or above the fill-in lines reduces autograder performance and might cause incorrect results. **Make sure to write your name, username, and answers legibly. You will not receive credit for illegible answers.**

True or False

1. Please fill in the appropriate circle given the statement. No explanation is required.

(a) The **BR** and **JMP** instructions use the same addressing mode. ☐ True ☒ False

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(b) The instruction **TRAP x27** loads the PC with the address **x0027**. ☐ True ☒ False

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Tracing a Program

2. Fill in the entirety of **both tables** on the right side. For the first table, fill in the first 5 instructions executed. If **HALT** is reached, fill in "HALT" for the PC in the corresponding line. Do not fill in any rows after **HALT**. For the second table, fill it in as if the program has **completely executed**.

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For example, for the first table, after the first instruction is executed, record the contents of the registers in row 1.

Note: Write the PC in hexadecimal and R0, R1, and R2 in decimal. On rows you use, fill in all boxes regardless of whether or not their values are changed.

Label	Address	Instruction
	x3000	LD R0, START
LOOP	x3001	LDR R1, R0, #0
	x3002	BRZ DONE
	x3003	ADD R0, R0, #1
	x3004	ADD R1, R1, R1
	x3005	ADD R1, R1, R1
	x3006	LDI R2, ANSWER
	x3007	ADD R2, R1, R2
	x3008	STI R2, ANSWER
	x3009	BR LOOP
DONE	x300A	HALT
START	x300B	.fill x6000
ANSWER	x300C	.fill x7000

	x6000	.fill #3
	x6001	.fill #10
	x6002	.fill #0

	x7000	.fill #0

Instr. #	PC	R0	R1	R2	CC
Initial	x3000	0	3	0	z
1	x3001	24576	3	0	p
2	x3002	24576	3	0	p
3	x3003	24576	3	0	p
4	x3004	24577	3	0	p
5	x3005	24577	6	0	p

Once the program has finished executing, what values are at the following memory addresses?

Address	Contents
x6000	3
x6001	10
x6002	0
...	...
x7000	52



Tracing a Program: Input / Output

3. Consider the LC-3 assembly program in the left column. The contents of memory starting at address x5000 are also provided in the middle column. Assuming the program begins executing at x3000 and continues until HALT, please indicate the final console output. Note that the ASCII value for character '0' (i.e. zero) is 48.

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Note: OUT sends the character represented by the ASCII code contained in R0 to the console.

<pre>.orig x3000 LD R1, OFFSET LD R2, ARRAY LD R3, LENGTH LOOP BRz DONE ADD R4, R2, R3 LDR R4, R4, #-1 AND R5, R4, #1 BRz DECR ADD R0, R1, R4 OUT DECR ADD R3, R3, #-1 BR LOOP DONE HALT ARRAY .fill x5000 LENGTH .fill 10 OFFSET .fill 48 ; this is the character code for ASCII '0' .end</pre>	<pre>.orig x5000 .fill 2 .fill 7 .fill 6 .fill 4 .fill 1 .fill 9 .fill 5 .fill 4 .fill 3 .fill 1 .end</pre>	<p>Console output:</p> <p style="text-align: center; color: red;">135917</p> <hr/>
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Pseudocode to Assembly

4. Consider the following incomplete LC-3 program. There is a positive number in register R0 and a negative number in R1. Register R2 has been initialized to zero. Fill in the blank space with valid assembly code such that the completed program, when run, will accumulate the sum of R0 and R1 in R2, decrement R0, and increment R1. Feel free to use both columns for your code.

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<pre>.orig x3000 ; Assume the following: R0 > 0, R1 < 0, ; and R2 = 0. ; Convert the pseudocode below to ; assembly: ; while (R0 > 0 R1 < 0) { ; R2 = R2 + R1 + R0 ; R0 = R0 - 1 ; R1 = R1 + 1 ; }</pre>	<pre>WHILE_CHECK ADD R0, R0, #0 ;; Set CC from R0 BRP WHILE_BODY ;; if R0 > 0 ADD R1, R1, #0 ;; Set CC from R1 BRZP DONE ;; if R1 < 0 WHILE_BODY ADD R2, R2, R1 ;; R2 = R2 + R1 ADD R2, R2, R0 ;; R2 = R2 + R1 + R0 ADD R0, R0, #-1 ;; R0 = R0 - 1 ADD R1, R1, #1 ;; R1 = R1 + 1 BR WHILE_CHECK DONE ;; ... HALT .end</pre>
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