



LogicalPhysical100%330

STAR TOPOLOGY

```
graph TD; S[2510-24 Switch] --- PC1[PC-PT PC1]; S --- PC2[PC-PT PC2]; S --- PC3[PC-PT PC3]; S --- PC4[PC-PT PC4]; S --- PC5[PC-PT PC5];
```

Time: 00:13:41.510PLAY CONTROLS

Scenario 0

NewDelete

Toggle PDU List Window

Fire	Last Status	Source	Destination	Type	Color	Time(μs)	Periodic	Num	Edit	Delete
	In Progress	PC1	PC4	ICMP		352.872	N	0	(edit)	(delete)
	In Progress	PC1	PC4	ICMP		352.872	N	1	(edit)	(delete)

Simulation Panel

Event List

WkTime(sec)Last Device

Reset SimulationConstant DelayCaptured352.872

Play Controls

Event List Filters - Visible Events

ICMP

Edit FiltersShow All Nodes

Event ListRealtimeSimulation





