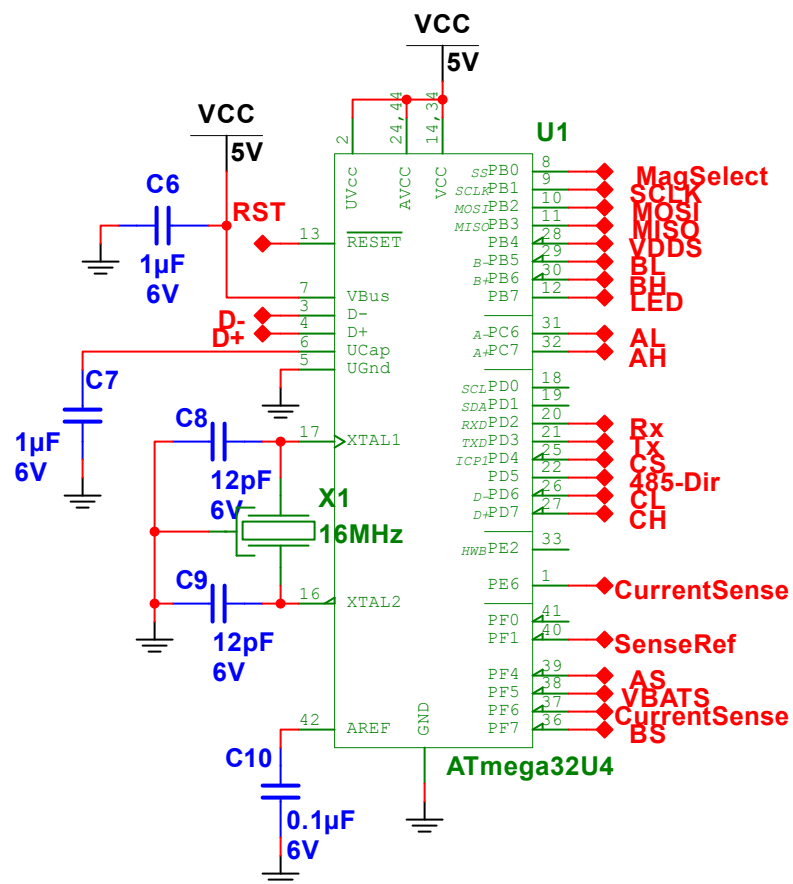
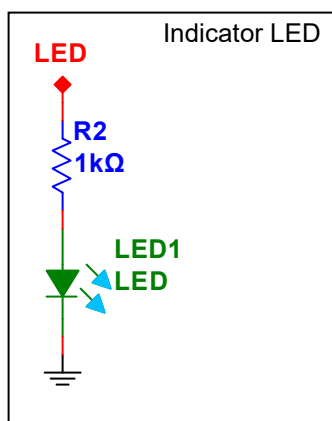
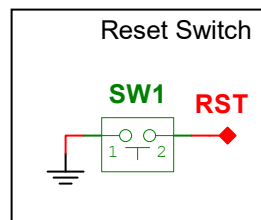
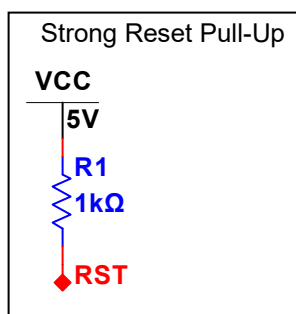
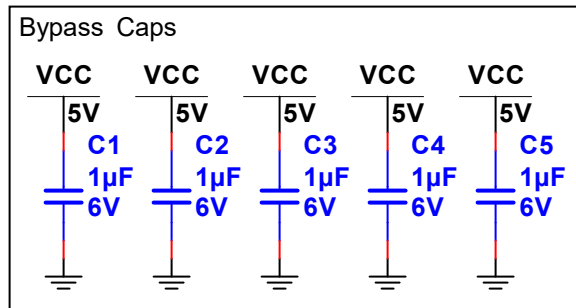
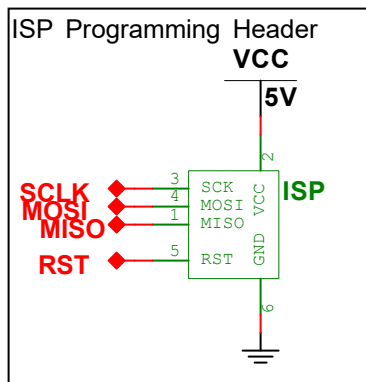
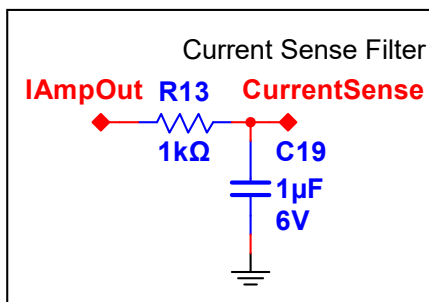
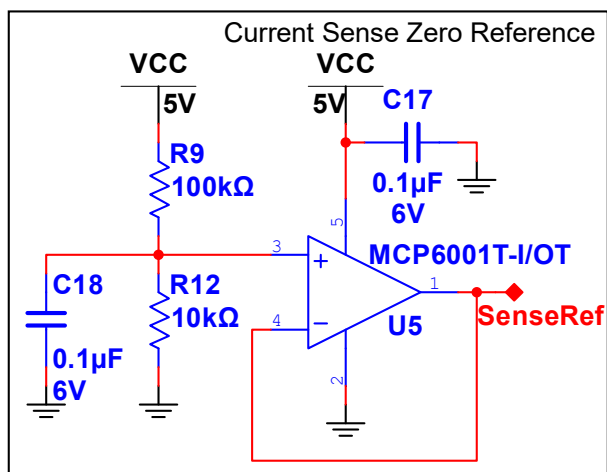
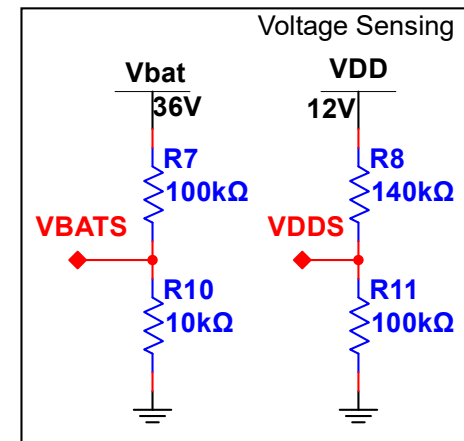
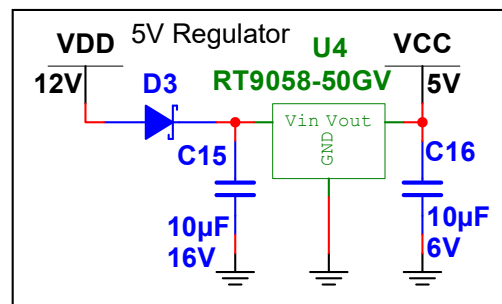
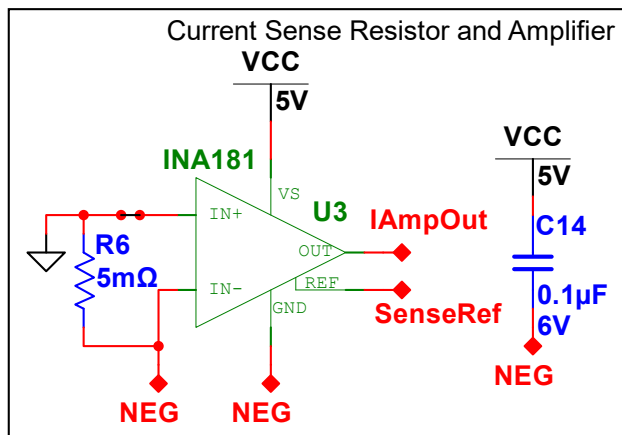
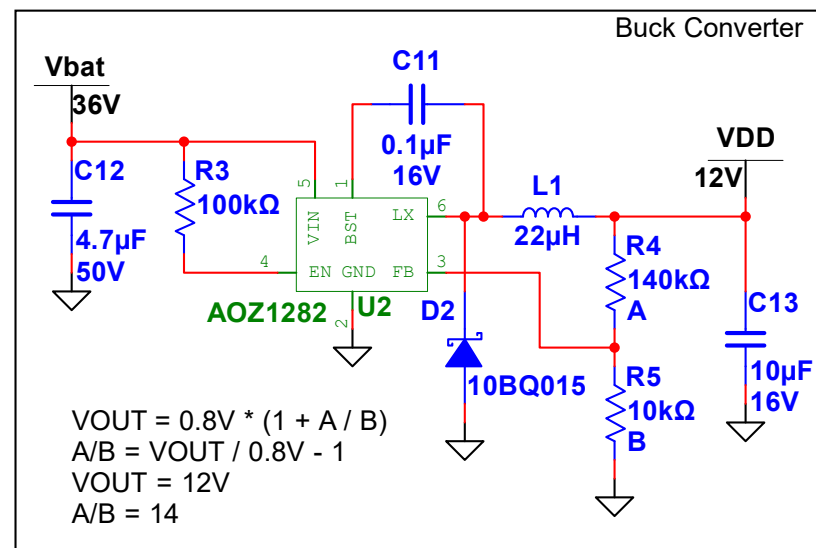
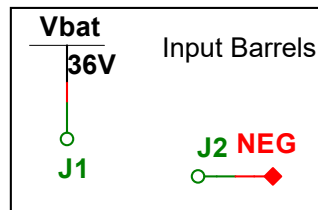
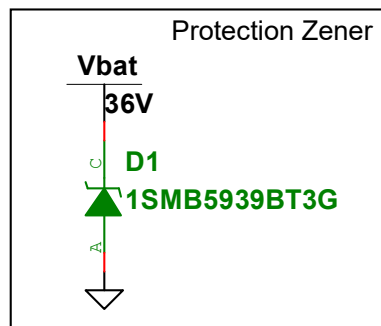


# CPU

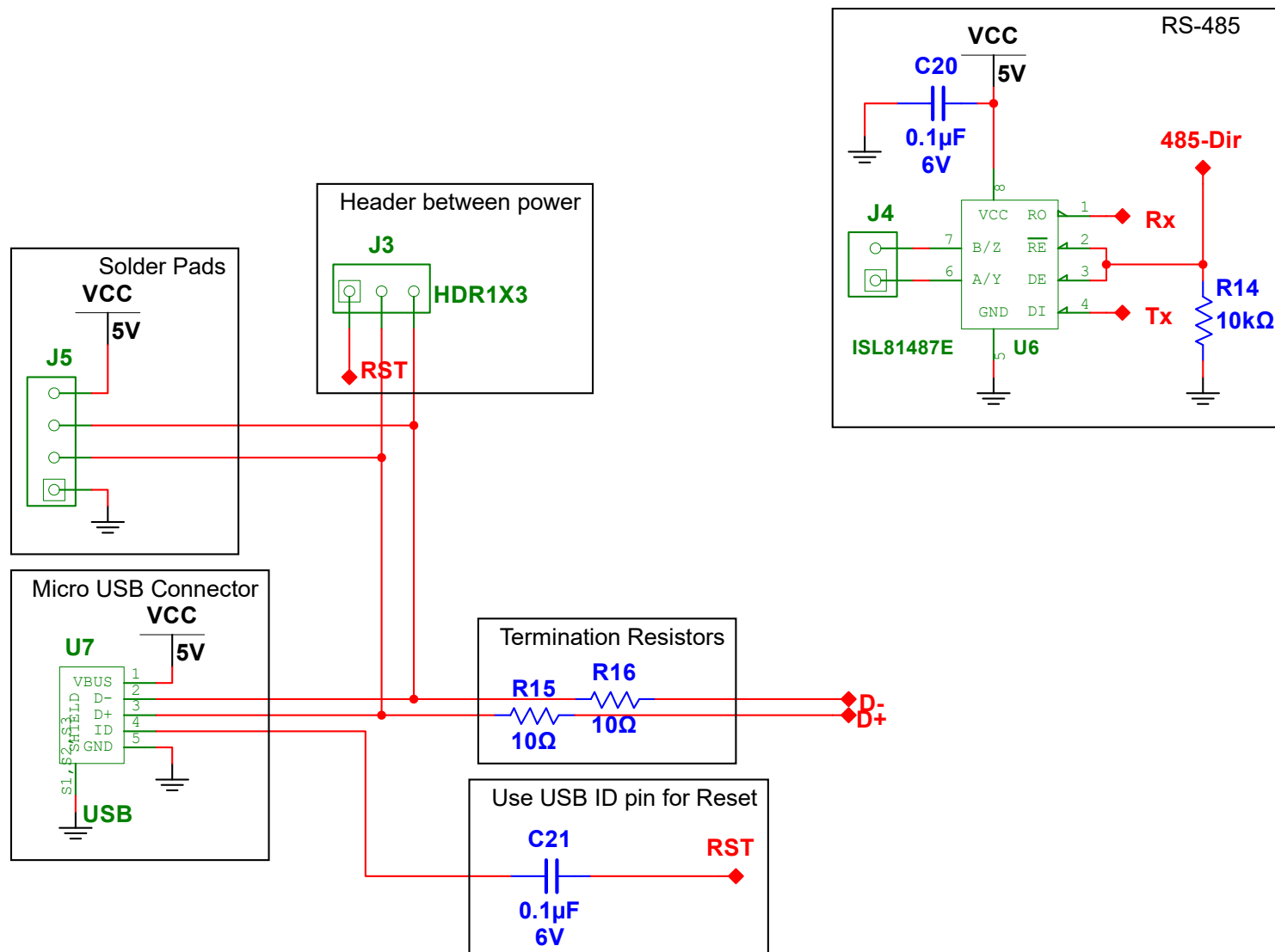


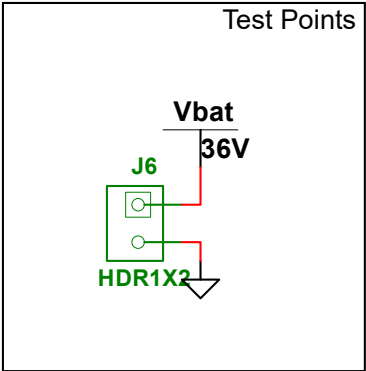
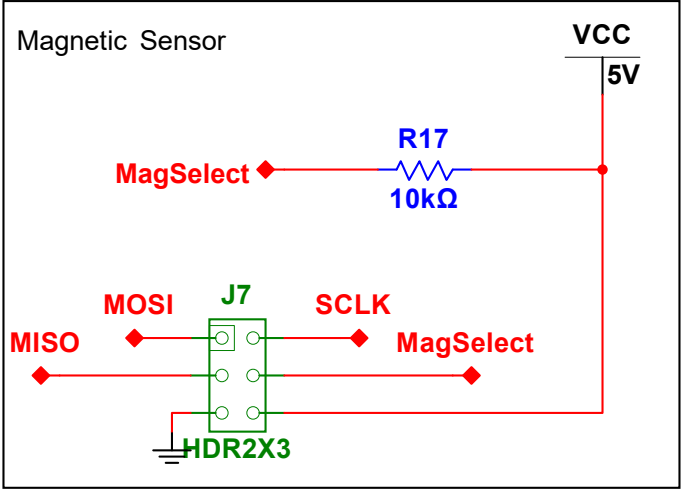
	0	1	2	3	4	5	6	7	8		
A	<div>3 Phases</div>										A
B	<div><div><div><div>DRA</div><div><div>AH</div><div>AL</div><div><div>H</div><div>L</div><div>Sense</div></div><div>Drive</div><div>AS</div></div></div><div><div><div>DRB</div><div><div>BH</div><div>BL</div><div><div>H</div><div>L</div><div>Sense</div></div><div>Drive</div><div>BS</div></div></div><div><div><div>DRC</div><div><div>CH</div><div>CL</div><div><div>H</div><div>L</div><div>Sense</div></div><div>Drive</div><div>CS</div></div></div></div></div></div></div>										B
C											C
D											D
E											E
F											F
G											G
	0	1	2	3	4	5	6	7	8		

# Power

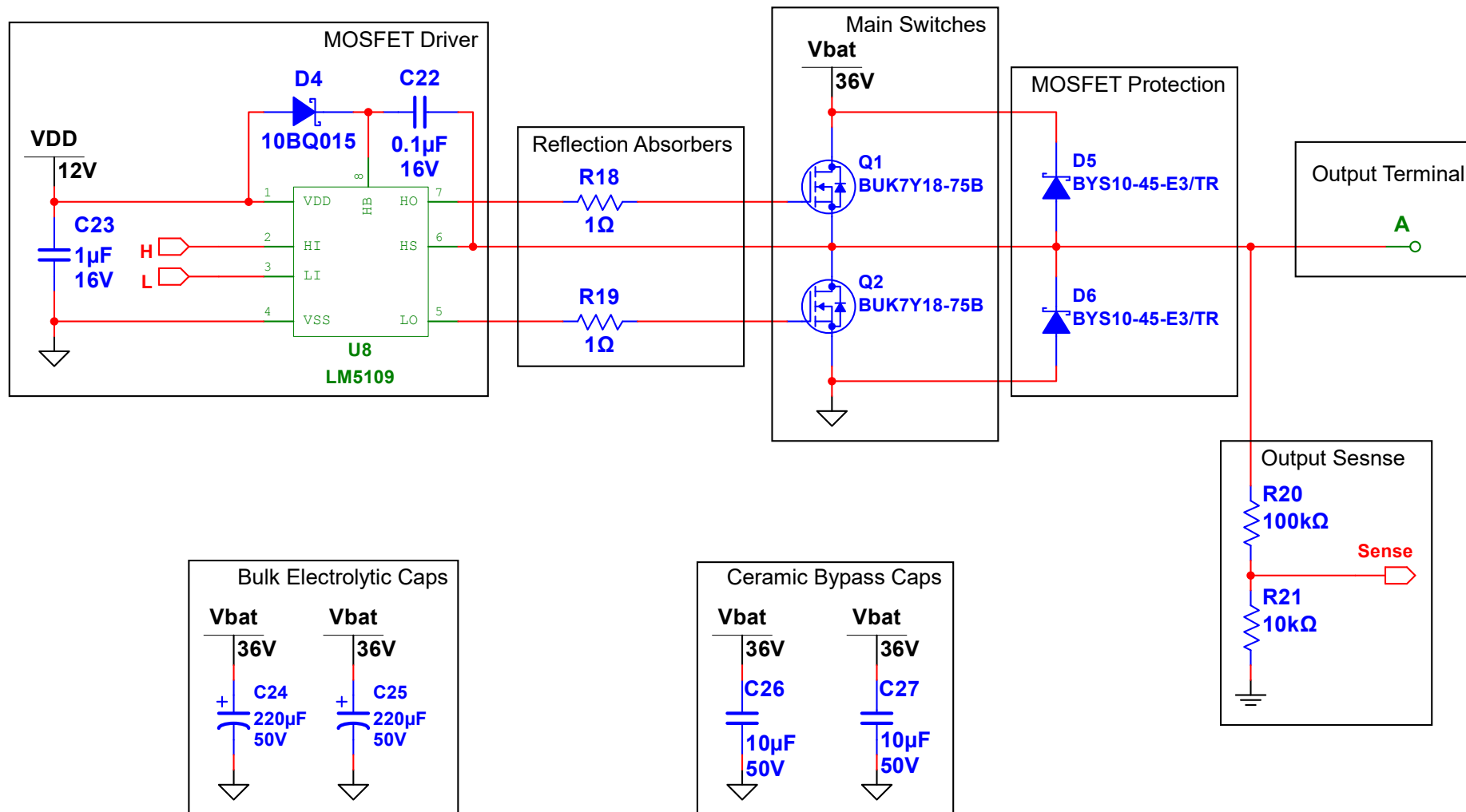


# Upstream

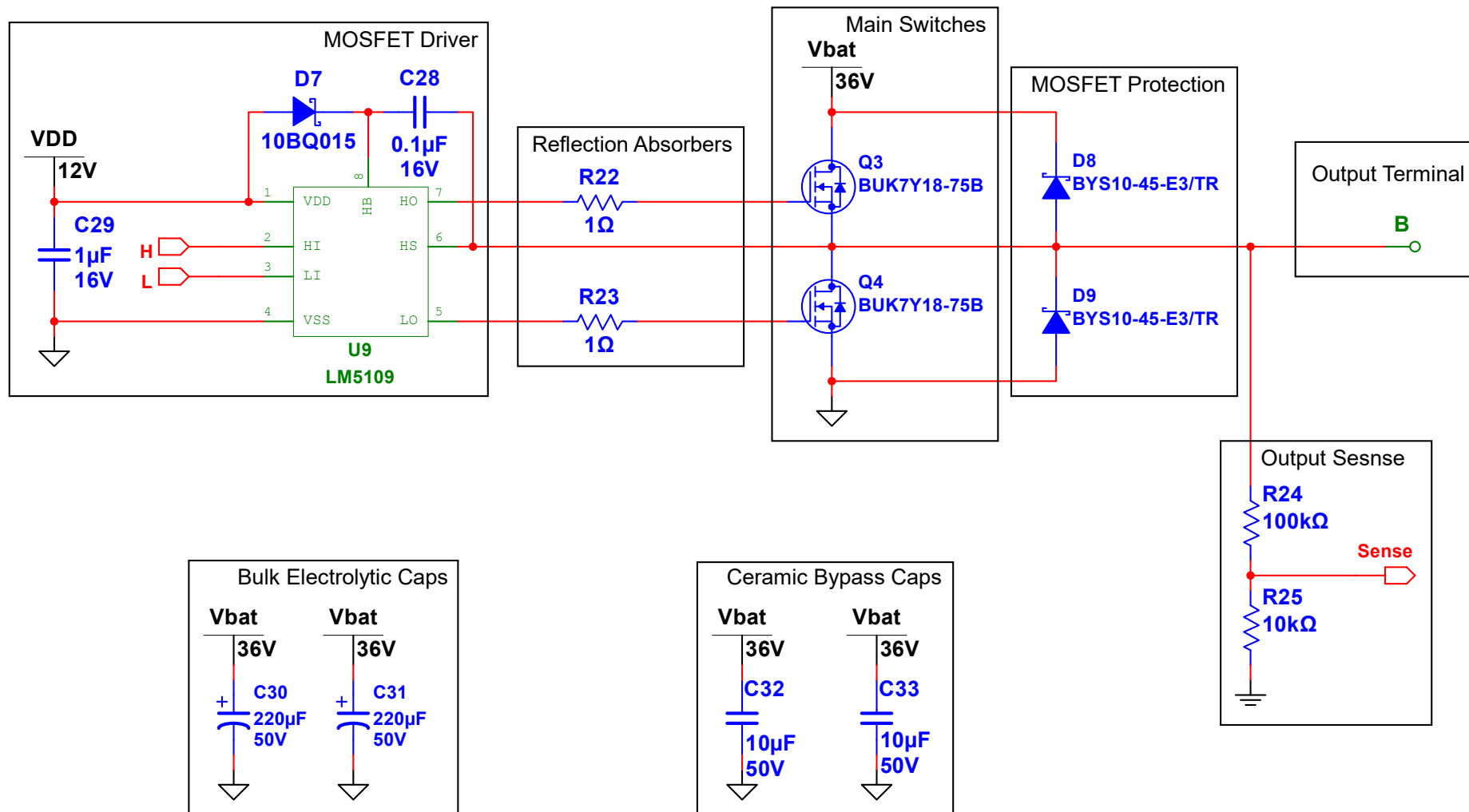




# Half-H



# Half-H



# Half-H

