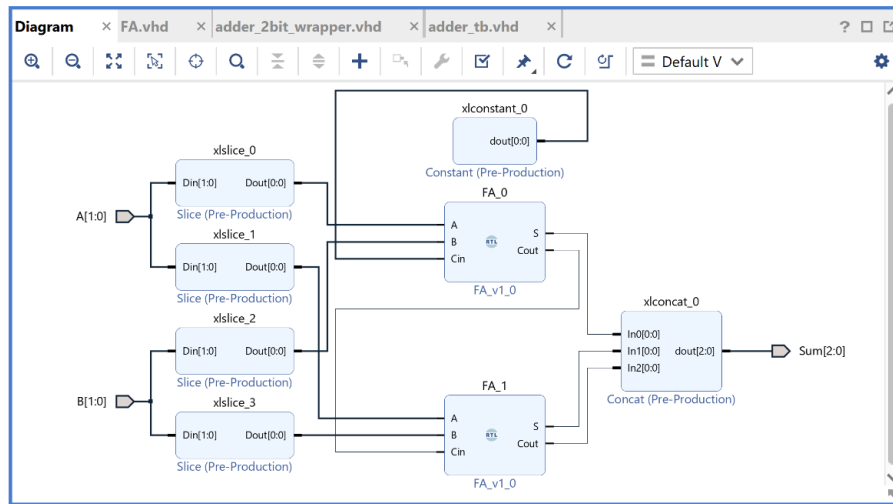
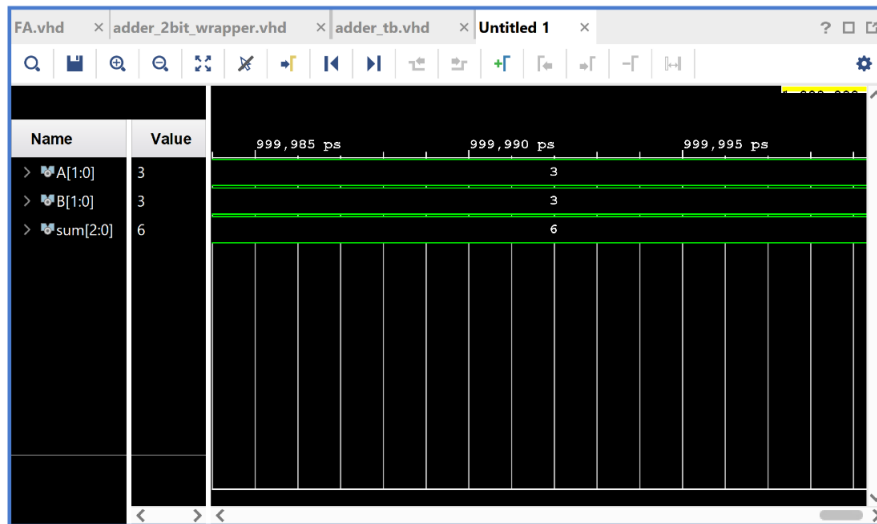


CECS 323 (Intro to Vivado IP Integrator)

Design:



Waveform:



Learning Outcome:

This lab was built upon concepts that we learned in 225 like the full adder but integrated learning what the IP Integrator does in Vivado. We learned that using Vivado's IP integrator aids in the process of an n-bit adder design as we can easily connect ports to each instance of a design onto the block design window. It also allows for seamless board component connectivity, rather than the usual zoom in choose port, careful where you connect to design that we used the most in our 225 class.