High-Level Parallel Programming EDSL A BOOST libraries use case

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J. Falcou

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 - Fault Tolerance
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Presentation Layout

- Context
 - Living Moore's Law Ending
 - The Desktop Super-Computer Era
 - Classic Tools Design
- Parallel Skeletons in a (somehow large) Nutshell
- 3 From Theory to C++ Practice : the QUAFF library
- Experimental Results
- Conclusion



"Where a calculator on the ENIAC is equipped with 19,000 vacuum tubes and weighs 30 tons, computers in the future may have only 1,000 vacuum tubes and perhaps only weigh 1.5 tons."

- Popular Mechanics, March 1949

Moore's Law is ending

CPU performances stalls despite continuous progress in transistor integration

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- Thermal dissipation
- The 'Die Desert' Syndrom
- Memory bandwidth

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Switching gears in the GHZ race

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- 2003 : Multi-cores (2X, 4X and soon 8X cores)

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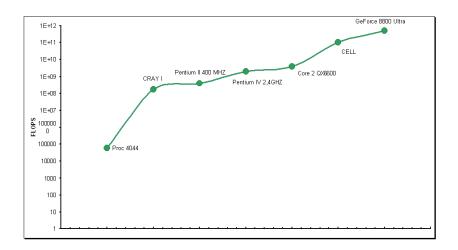
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- 20xx : On-Chip Heterogeneous Architectures (CELL, Larabee)



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The Infamous GFLOPS Slide



Is the Free Lunch over yet?

The hidden cost of new architectures

The more parallel, the more complex it becomes to work with:

- Many programming model
- Many technologic choices
- Inter-operability of heterogeneous machines

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Our Stance

Tools for programming upcoming new parallel architectures are bound to become strategic for both academics and industrials



```
//detection of most easy points to track
local detected pts number=0; //reset detection
top1 = mtime():
if(!first iteration)
  Detection(INT img pred, DETBORDER left, DETBORDER up, DETBORDER right, DETBORDER bottom,
            local Detected point x,local Detected point y, &local detected pts number,local wanted pts number);
top2 = mtime();
timer[3] = top2-top1;
//tracking of selected points
top1 = mtime();
TrackingPoints(local Detected point x,local Detected point y,local Tracked point x,local Tracked point y,local detected pts number,
img pred.data,img curr.data,
LR2 img pred.data,LR2 img curr.data,
LR4 img pred.data, LR4 img curr.data,
TX.TY);
SwapImages():
MPI Barrier(MPI COMM WORLD):
MPI Gather( local Detected point x, local wanted pts number, MPI DOUBLE, Detected point x, local wanted pts number, MPI DOUBLE, MASTER,
MPI Gather (local Detected point y, local wanted pts number, MPI DOUBLE, Detected point y, local wanted pts number, MPI DOUBLE, MASTER,
MPI Gather (local Tracked point x, local wanted pts number, MPI DOUBLE, Tracked point x, local wanted pts number, MPI DOUBLE, MASTER, MF
MPI Gather (local Tracked point y, local wanted pts number, MPI DOUBLE, Tracked point y, local wanted pts number, MPI DOUBLE, MASTER, MF
MPI Reduce(&local detected pts number, detected pts number,1, MPI INT, MPI SUM, MASTER, MPI COMM WORLD);
MPI Allgather( local Detected point x, local wanted pts number, MPI DOUBLE, Detected point x, local wanted pts number, MPI DOUBLE, MPI
MPI Allgather( local Detected point y, local wanted pts number, MPI DOUBLE, Detected point y, local wanted pts number, MPI DOUBLE, MPI
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MPI Allreduce(&local detected pts number, detected pts number, 1, MPI INT, MPI SUM, MPI COMM WORLD);
MPI Allreduce(timer, timer, 10, MPI FLOAT, MPI MAX, MPI COMM WORLD);
// movement matrix extraction (median square value)
                                                                                   4□ > 4□ > 4□ > 4□ > 4□ > 900
```

top1 = mtime();

The Parallel Gordian Node

For a given task at hand, we want

- Easy to read/maintain code
- Efficiency
- Abstraction of underlying architectures

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What to do then?

- New Languages with parallel semantic
- Software libraries for existing languages

Compiler-based Tools

Principles

Existing compilers are modified to add either new languages constructs or unerlying parallelised code generator

Compiler-based Tools

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Existing compilers are modified to add either new languages constructs or unerlying parallelised code generator

Limitations

- One compiler per architectures ?
- Time to market
- Long term support
- Acceptance by actual users

Library-based Tools

Principles

- Gather data structures and related functions
- Favor binary level reuse
- Capitalize on Algorithmic Expertise

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Limitations

- Few to none inter-procedural optimization
- Combinatorial explosion of variants
- Internal dependances on data structures choice

Best of both worlds

• Compilers :

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 - Generated code is efficient
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Why not using Embedded DSL?

Presentation Layout

- Context
- Parallel Skeletons in a (somehow large) Nutshell
 - The Parallel Skeletons paradgim
 - Formal model for Parallel Skeletons
- From Theory to C++ Practice: the QUAFF library
- Experimental Results
- 5 Conclusion

"When we had no computers, we had no programming problem either. When we had a few computers, we had a mild programming problem. Confronted with machines a million times as powerful, we are faced with a gigantic programming problem."

- Edsger Dijkstra

Parallel Skeletons in a few words

Basic Principles

- Co-proposed by Cole and Darlington in 1989
- There is patterns in parallel applications
- Those patterns can be generalized
- Application = Combination of such patterns

Parallel Skeletons in a few words

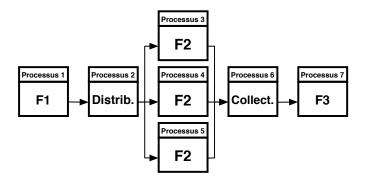
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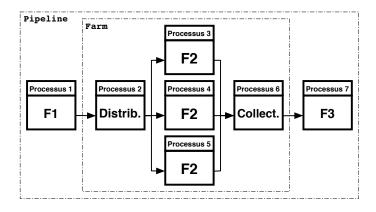
Functionnal point of view

- Skeletons are Higher-Order Functions
- Skeletons support a compositionnal semantic
- Application = composition of state-less functions

Spotting skeletons when you see one



Spotting skeletons when you see one



State of the Art in Parallel Skeleton

Dedicated Languages

Mostly functional oriented

- P3L [DANELUTTO 95]
- Skipper [SEROT 95]

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Software Libraries

Mostly imperative

- C : eSkel [COLE 89]
- C++: Muesli [KUCHEN 03]
- JAVA : Lithium [DANELUTTO 02]

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Applications

- Google MapReduce
- MalBa for non-linear optimisation

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Definitions

A Small SKeleton Grammar

- $\Sigma ::= \operatorname{\mathsf{Seq}} f \mid \operatorname{\mathsf{Pipe}}(\Sigma_1,\ldots,\Sigma_n) \mid \operatorname{\mathsf{Farm}}(n,\Sigma) \mid \operatorname{\mathsf{Scm}}(n,f_{\mathtt{S}},\Sigma,f_m)$
- ullet $f, f_s, f_m ::=$ sequential, application-specific user-defined functions
- n ::= integer ≥ 1

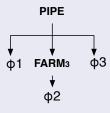
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Example

Pipe(Seq ϕ_1 , Farm(3, Seq ϕ_2), Seq ϕ_3)



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Process network structure

The CSP model

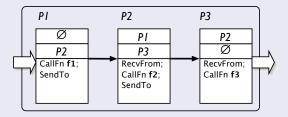
- Set of processes communicating by channels
- Each process keeps tracks of its predecessors and successors
- Each process executes a sequence of instructions

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The CSP model

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Example: process network for Pipeline



Process network algebra

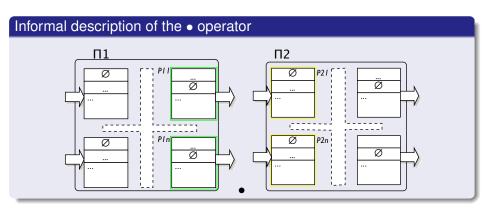
Definition

- Operators incrementally build process networks
- Operators are formally defined using production rules

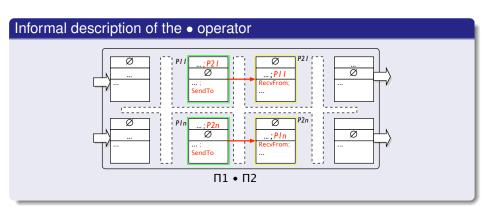
Example: the • operator

$$\frac{\pi_i = \langle P_i, I_i, O_i \rangle \ (i = 1, 2) \qquad |O_1| = |I_2| = m}{\pi_1 \bullet \pi_2 = \langle (P_1 \cup P_2)[(o_1^j, \sigma) \leftarrow \phi_d((o_1^j, \sigma), i_2^j)]_{j=1...m}[(i_2^j, \sigma) \leftarrow \phi_s((i_2^j, \sigma), o_1^j)]_{j=1...m},} I_1, O_2 \rangle$$

Process network algebra



Process network algebra



Skeleton as PN Algebra operators

Definition

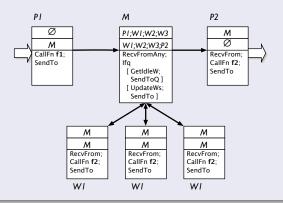
We define a conversion function $\mathcal{C}[[]]$ that transforms a skeleton into the equivalent process nextwork.

```
 \begin{array}{cccc} \mathcal{C}[[\mathsf{Seq}\ f]] & \equiv & \lceil f \rceil \\ \mathcal{C}[[\mathsf{Pipe}\ \Sigma_1 \dots \Sigma_n]] & \equiv & \mathcal{C}[[\Sigma_1]] \bullet \dots \bullet \mathcal{C}[[\Sigma_n]] \\ \mathcal{C}[[\mathsf{Farm}\ n\ \Sigma]] & \equiv & \lceil \mathsf{FarmM} \rceil \bowtie (\mathcal{C}[[\Sigma]]_1 \parallel \dots \parallel \mathcal{C}[[\Sigma]]_n) \\ \mathcal{C}[[\mathsf{Scm}\ m\ f_s\ \Sigma\ f_m]] & \equiv & \lceil f_s \rceil \ \triangleleft \ (\mathcal{C}[[\Sigma]]_1 \parallel \dots \parallel \mathcal{C}[[\Sigma]]_m) \ \rhd \ \lceil f_m \rceil \\ \end{array}
```

Skeletons to process network transformation

Exampel

Pipe(Seq ϕ_1 , Farm(3, Seq ϕ_2), Seq ϕ_3)



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Objectives

Proposing a C++ skeletons library

- Limit runtime overhead
- Use the formal model skeleton as guidelines

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What we want to write

```
run( pipeline( seq(F1), seq(F2) ) )
```

Objectives

Proposing a C++ skeletons library

- Limit runtime overhead
- Use the formal model skeleton as guidelines

What we want to run

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Design Rationale

Analysis

- Once defined, a skeleton application structures is immutable
- No need for dynamic add/remove of processus
- Represent Processes as statically polymorphic objects

Design choice

- Compile-time constructs represent processes
- Meta-programming helps handling those construct
- Proto bridges the gap between user interface and code generation

Challenges

User Interface

- Seamless integration of legacy code
- Support C and C++ style functions/functors
- Extract send/receive schemes from function prototypes
- BE usable by 'Joe Average' developper

Code Generation

- Turn skeleton AST into Proto usable elements
- Implement meta semantic rules
- Optimize out process network
- Generate proper MPI commands

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 - Image processing on the Cell processor
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3D tracking of pedestrians

Objectives

Detect and track pedestrians in a stereoscopic video stream while keeping up with stream frequency (25-30 FPS)

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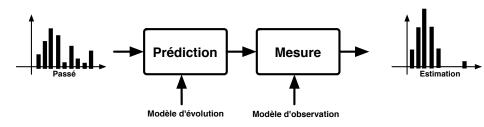
Difficulties

- Lightning, textures or pose may vary
- Partial or global occultations
- Dynamic background

Trackign with particle filter [BLAKE 96]

Principles

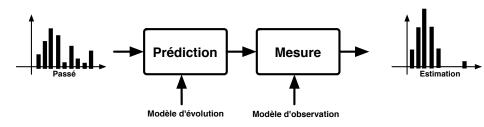
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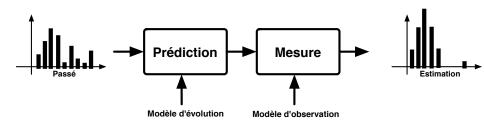
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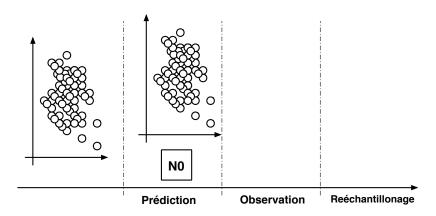


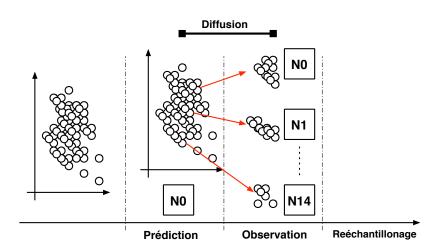
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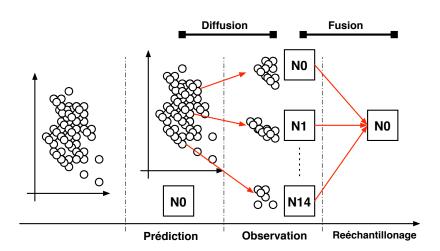
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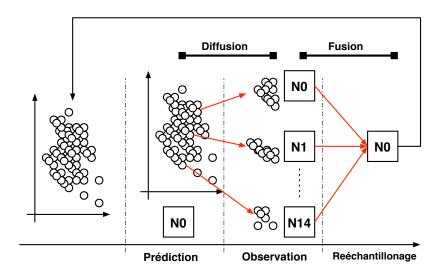






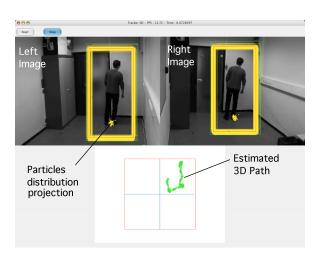








Sample application



Runtime Performances

	100	500	1000	5000
Sequential	0.061 <i>s</i>	0.299 <i>s</i>	0.704 <i>s</i>	11.358
FPS	16.38	3.34	1.42	0.09
Parallel	0.014 <i>s</i>	0.0195 <i>s</i>	0.0334 <i>s</i>	0.105 <i>s</i>
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Results (CIMCV/ECCV 06)

Real-time obtained for more than 1000 particles

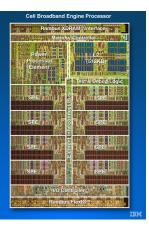
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Results (CIMCV/ECCV 06)

- Real-time obtained for more than 1000 particles
- Maximum speed-up greater than ×100.

Architecture of the CELL processor



Features

- 9 Cores :1 PPE + 8 SPE
- 200 GB/s Bus
- Multi-thread support
- SIMD support (Altivec)
- Up to 400 GFLOPS

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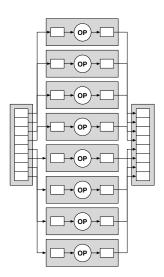
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Developping on the CELL

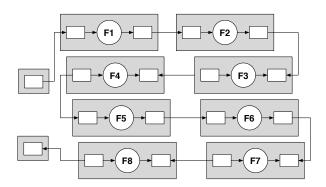
How should I map an application on this thing?



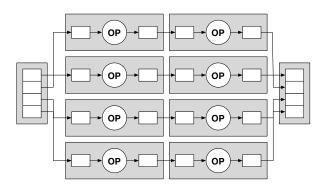
Various mapping ...



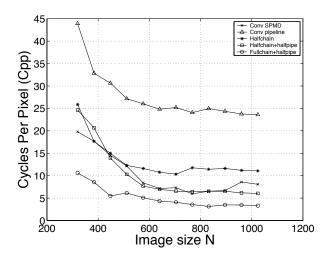
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Various mapping ...



... lead to various results





Modification to the skeletons grammar and semantic

Identified Skeletons

- Pipe: parallel function composition over SPEs
- Pardo : replicate skeleton over SPEs
- Seq: skeleton integration

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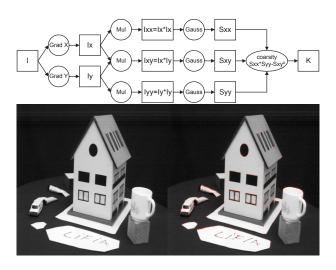
Associated Grammar

```
\mathcal{A} ::= \operatorname{\mathsf{run}} \Sigma
```

$$\Sigma ::= \Gamma \mid Pardo n \Gamma$$

$$\Gamma ::= Seq f \mid Pipe \Gamma_1 \dots \Gamma_n$$

$$n$$
 ::= integer ≥ 1



PPE code

```
#include <quaff/quaff.hpp>
QUAFF_REGISTER_KERNEL(harris);
int main(int ,char**)
{
    tile<float> in(1,512,1,512);
    tile<float> out(1,512,1,512);
    harris(in,out);
    return 0;
}
```

SPE code - classic Pipeline

```
#include <quaff/quaff.hpp>
using namespace quaff;

void sobel ( tile<float> const& in,tile<float>& sx,tile<float>& sy);
void mul ( tile<float> const& sx,tile<float> const& sy
, tile<float>& sxx,tile<float>& sxy,tile<float>& syy);

void gauss ( tile<float> const& sxx,tile<float>& sxy,tile<float>& syy);

void coarsity( tile<float> const& sxx,tile<float>& gxxy,tile<float>& gyy);

void coarsity( tile<float> const& gxx,tile<float> const& gxy,tile<float>& gxy,tile<float> const& gyy
, tile<float> const& out);

BEGIN_SKELL_KERNEL(harris);

void harris( tile<float> const&, tile<float>&)
{
    run( pardo<2>( seq(Sobel) | seq(Mul)) | seq(Gauss) | seq(Coarsity) ) );
}
```

SPE code - Half-chain

SPE code - Full chain

Runtime performance

Mode	Classic	Half chain	Full chain
Hand-written	27.67 cpp	11.26 cpp	8.34 cpp
Quaff	28.07 cpp	11.36 cpp	8.42 cpp
Overhead	1.46 %	0.8%	0.9%

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Code-bloat impact

- Marshalling code ≈ 2Ko
- Functions interface ≈ 1Ko par opérateur
- Manual implementation: 8Kb per SPE
- Quaff implementation: 70Kb per SPE

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BOOST as libraries building blocks

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- Ease the transition between the Proto transform and actual runtime elements
- Work like peas in pod :)



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Boost.MPI

One word: serialization



Similar works in progress

NT2 - EDSL for linear algebra

- Matrix container with algebra and numeric operations
- Support SIMD extensions, OpenMP, pThread and soon GPUs
- Uses Proto, Fusion, Thread and MPL

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BSP++ - Bulk Synchronous Parallelism in C++

- A BSP programs = sequence of parallel super-step
- BSP programs performances can be predicted with an analytic model
- Uses Lambda, Fusion and MPI



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BSP++ - Bulk Synchronous Parallelism in C++

- A BSP programs = sequence of parallel super-step
- BSP programs performances can be predicted with an analytic model
- Uses Lambda, Fusion and MPI

Toward BOOST-based multi-stage programming

- Develop a variant of Kelly's TaskGraph library
- Planned to use meta-programming instead of macros



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Future works

BOOST and parallel computing

- Parallelism became a recurring topic on the Dev. List
- Low-level support : threads, coroutine
- Structured support : futures



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Possible Contributions

- BOOST support for CELL processor
- Boost.Simd proposal



PROTO contributions

Improvements

- Improving compilation time
- Support for van Wijngaarden grammars

Extending PROTO scope

- Adding support for generic template virtual machines
- Higher-level code transformation algorithms



Thanks for your attention.