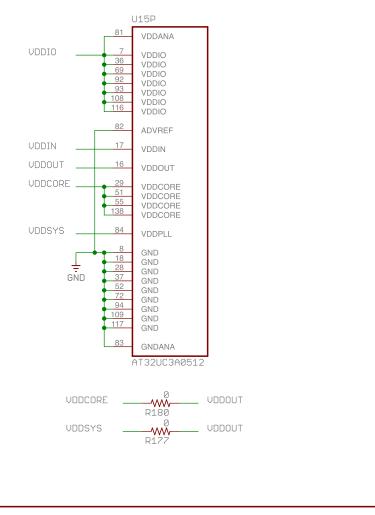
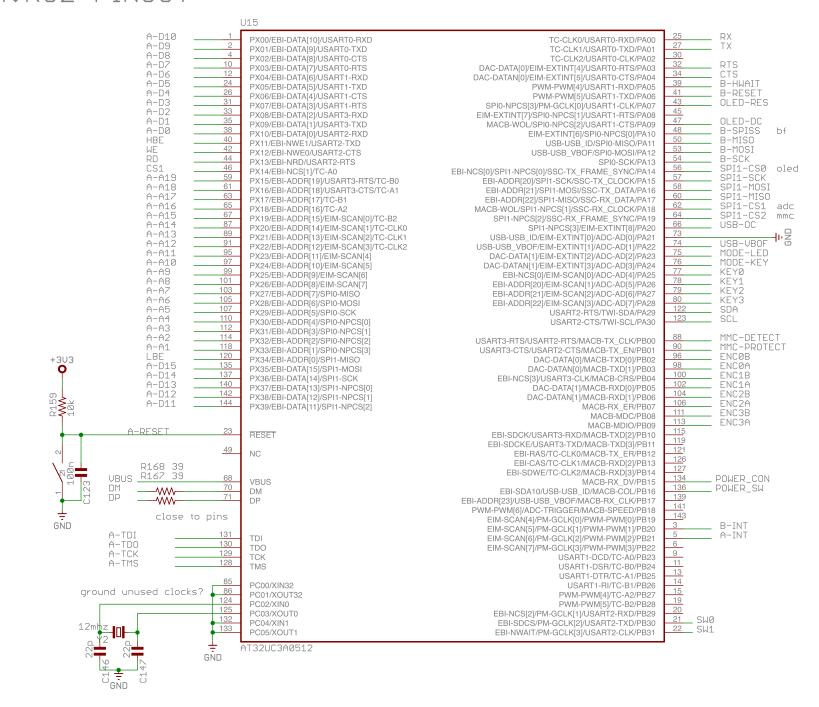
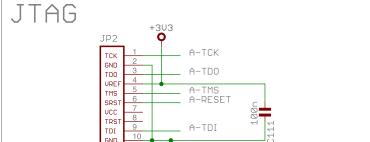
## See doc32090 section 2.1 VDDIO VDDOUT VDDOUT VDDSYS VDDCORE VDDCORE

## AUR32-POWER



## AVR32-PINOUT





AUR-JTAG GND

note: pin7 used to supply power to JTAG ICE (n/a here) doc2475.pdf page 2-2 AUR32

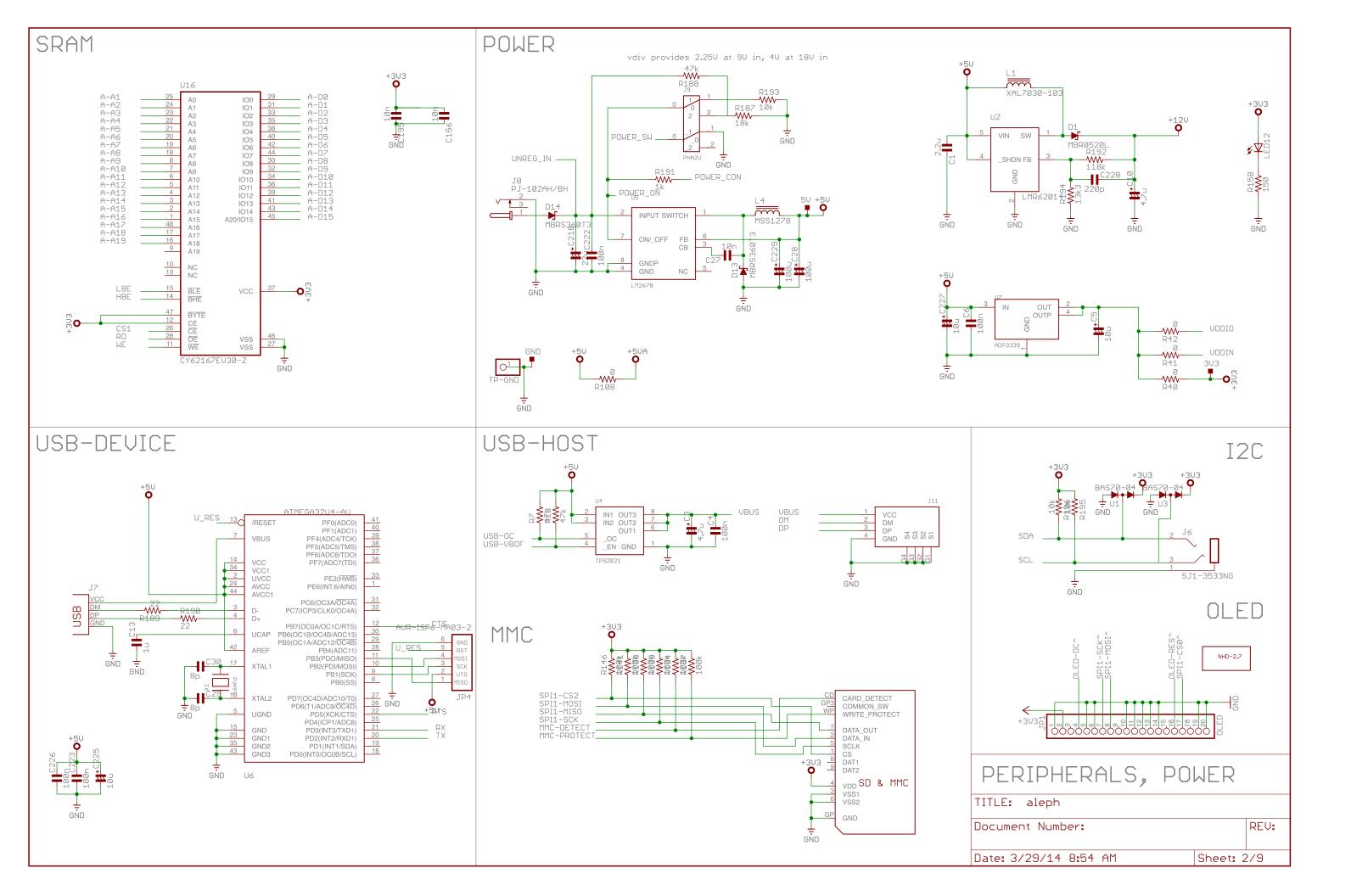
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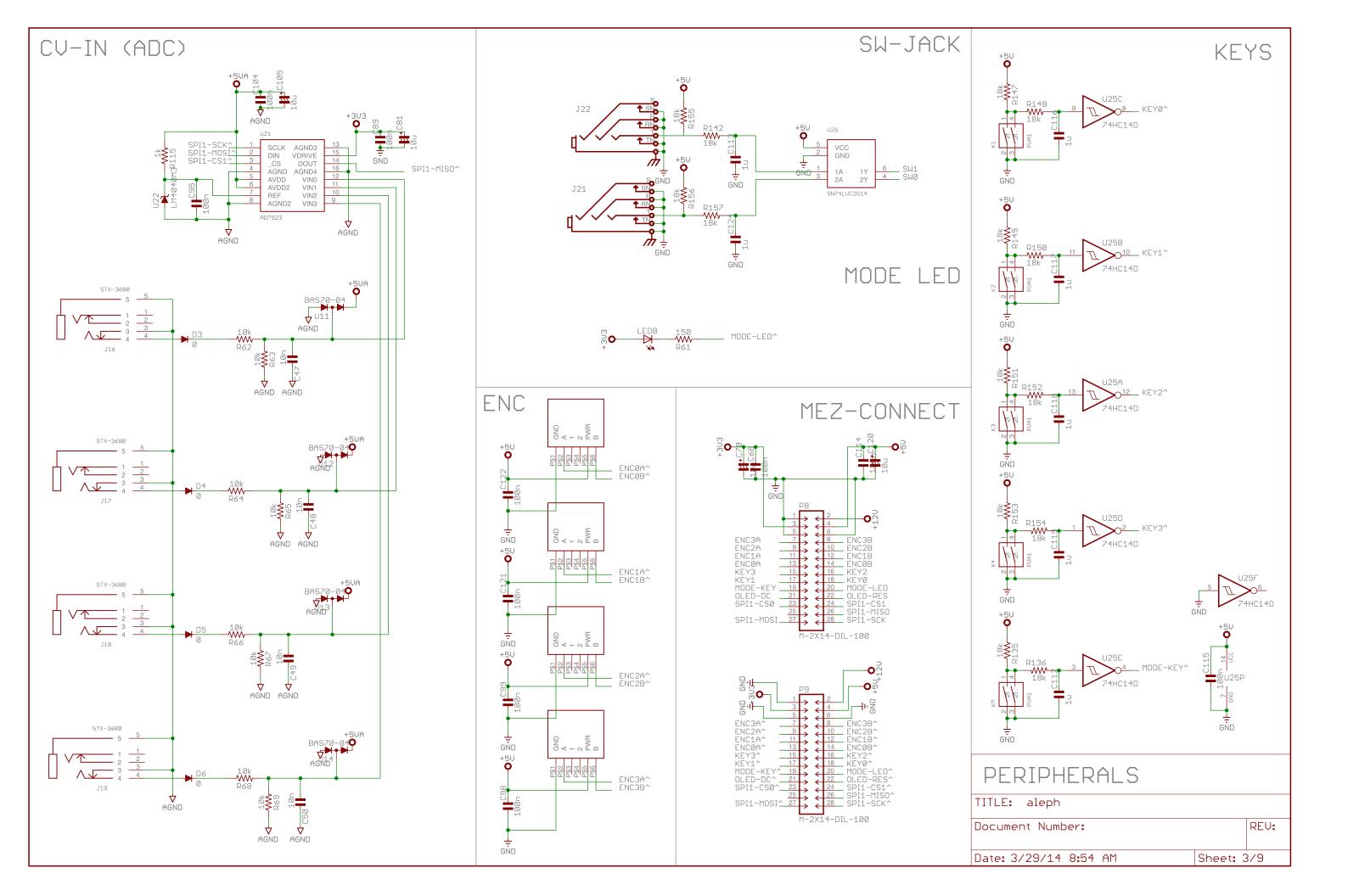
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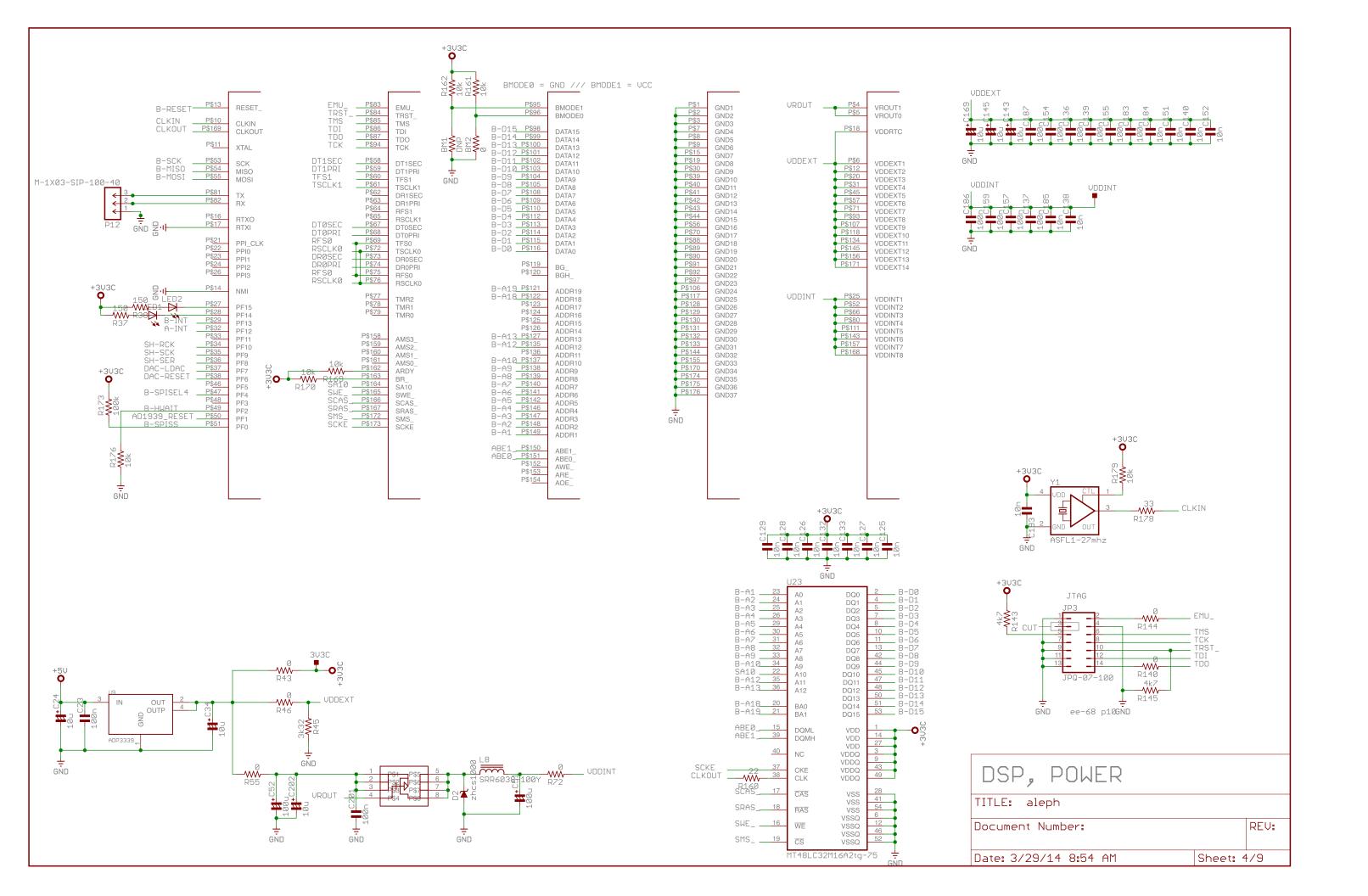
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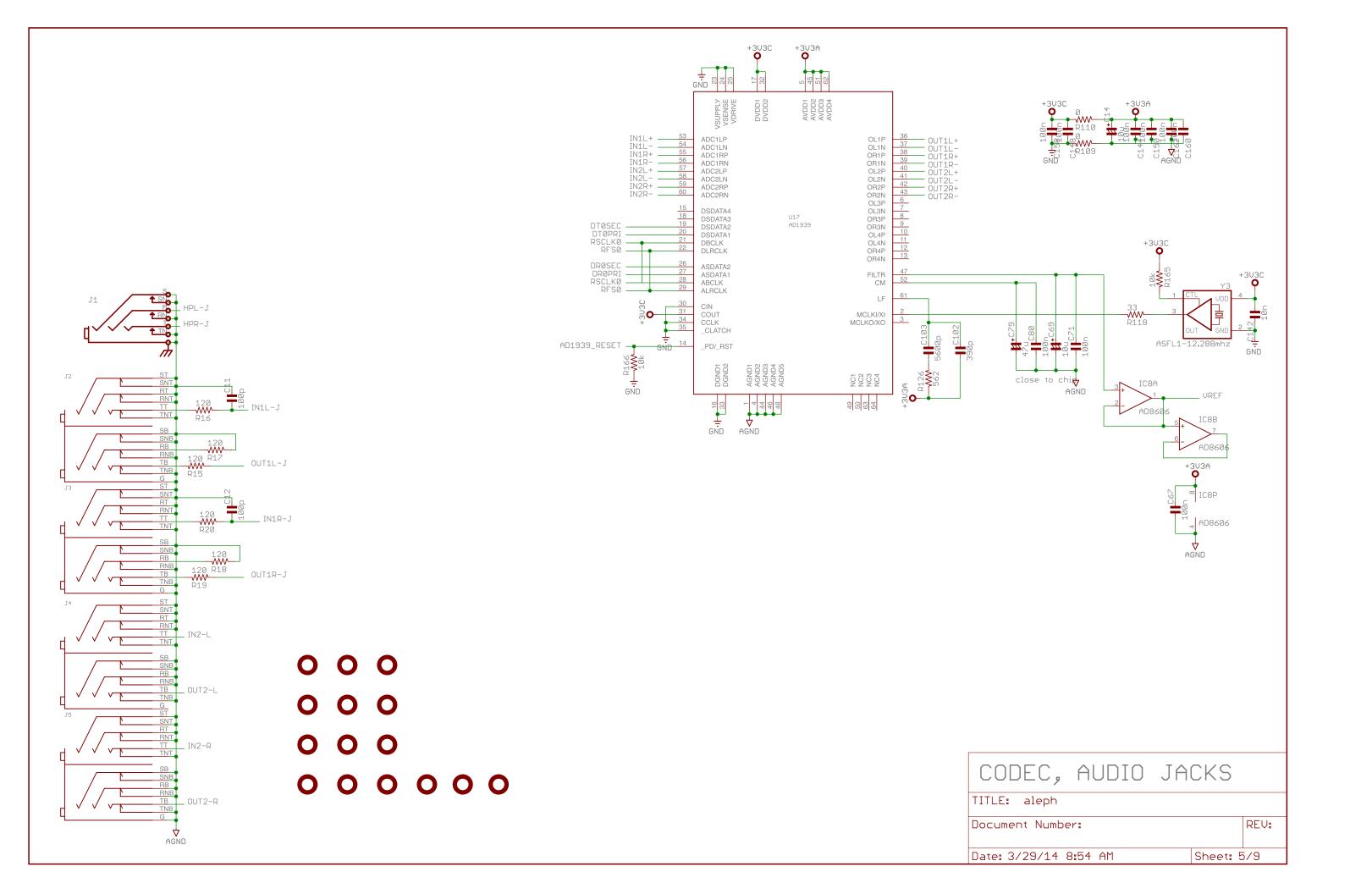
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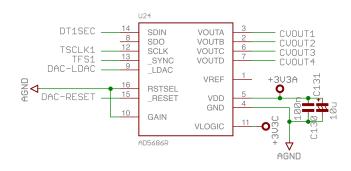
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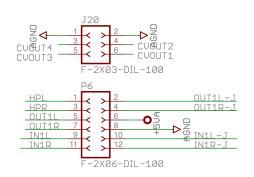


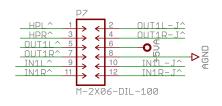


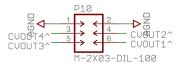


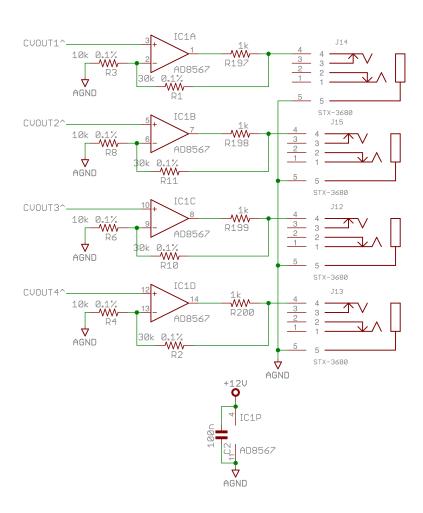




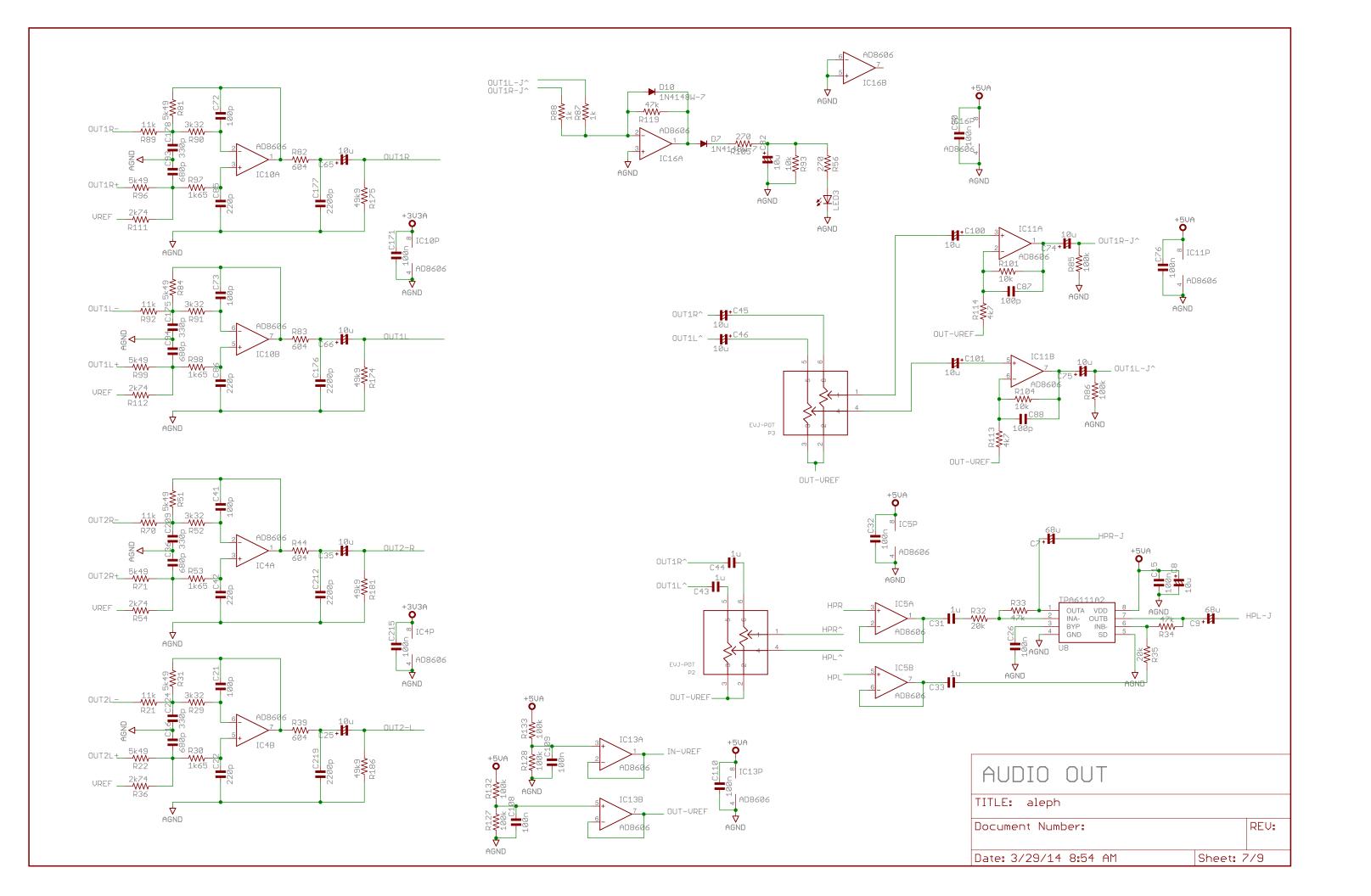


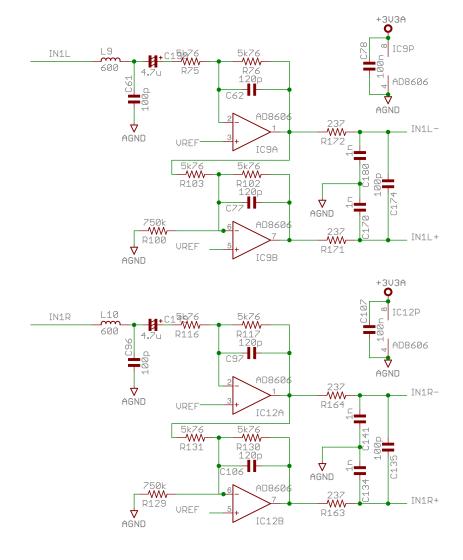


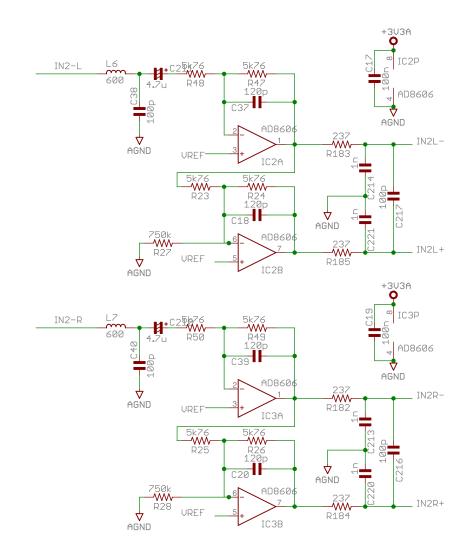




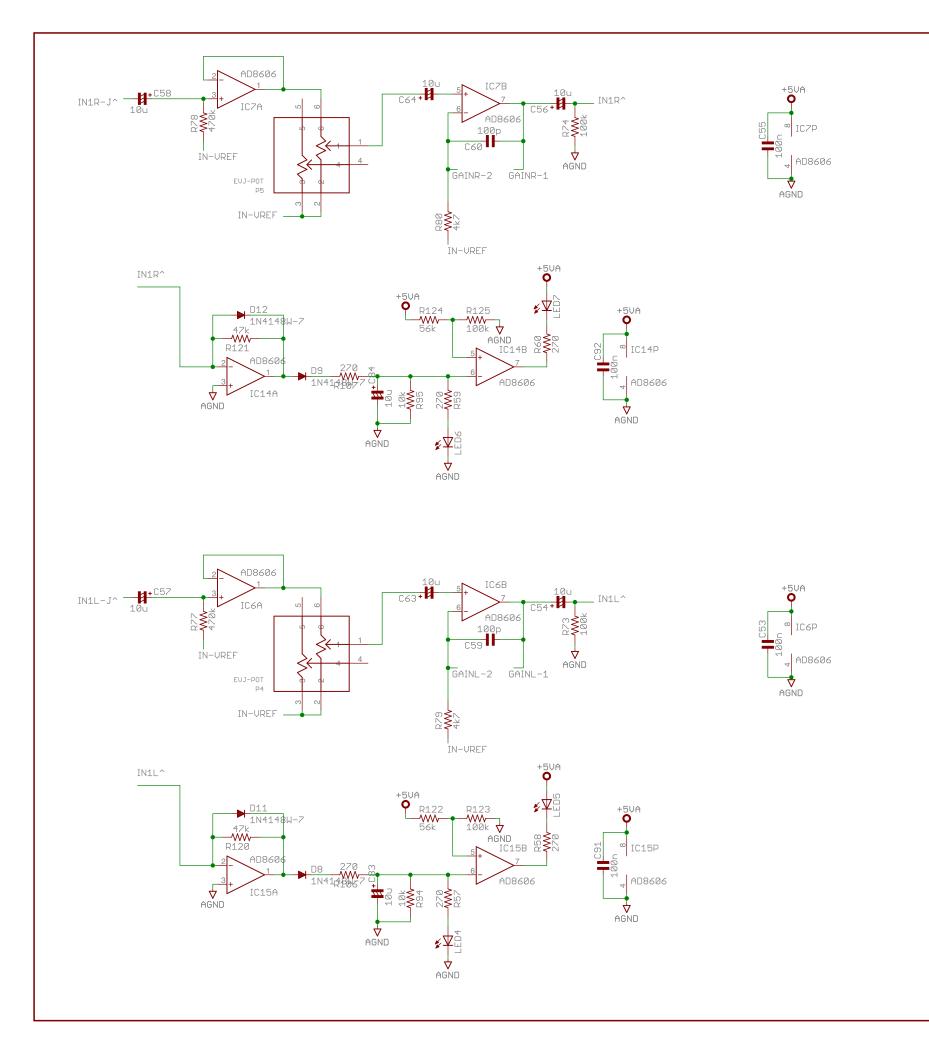
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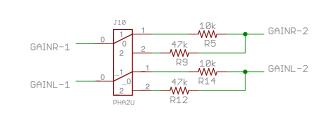






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AUDIO I	N GAIN		
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