

## Intel Jasper Lake Platform

V1.1

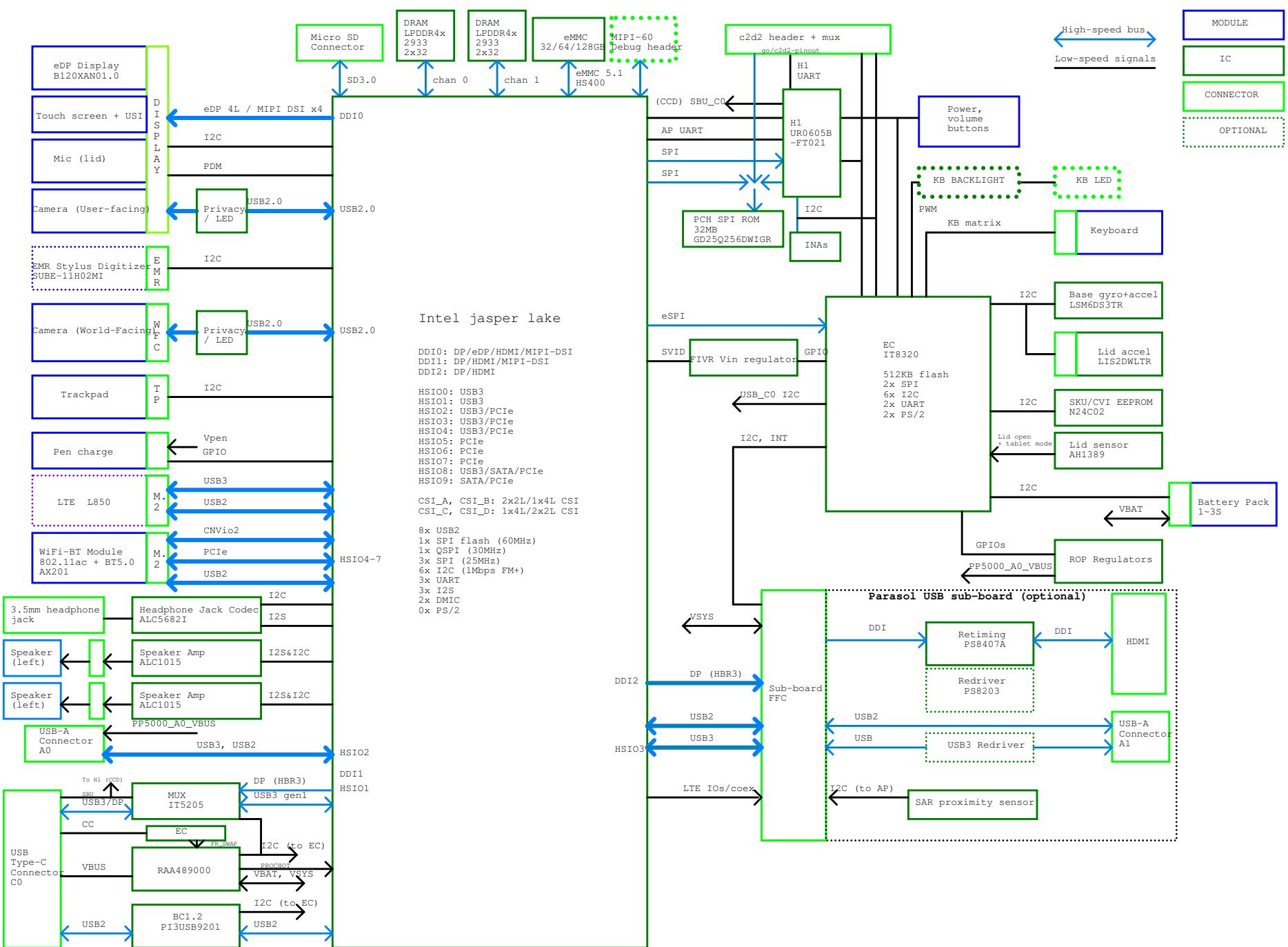
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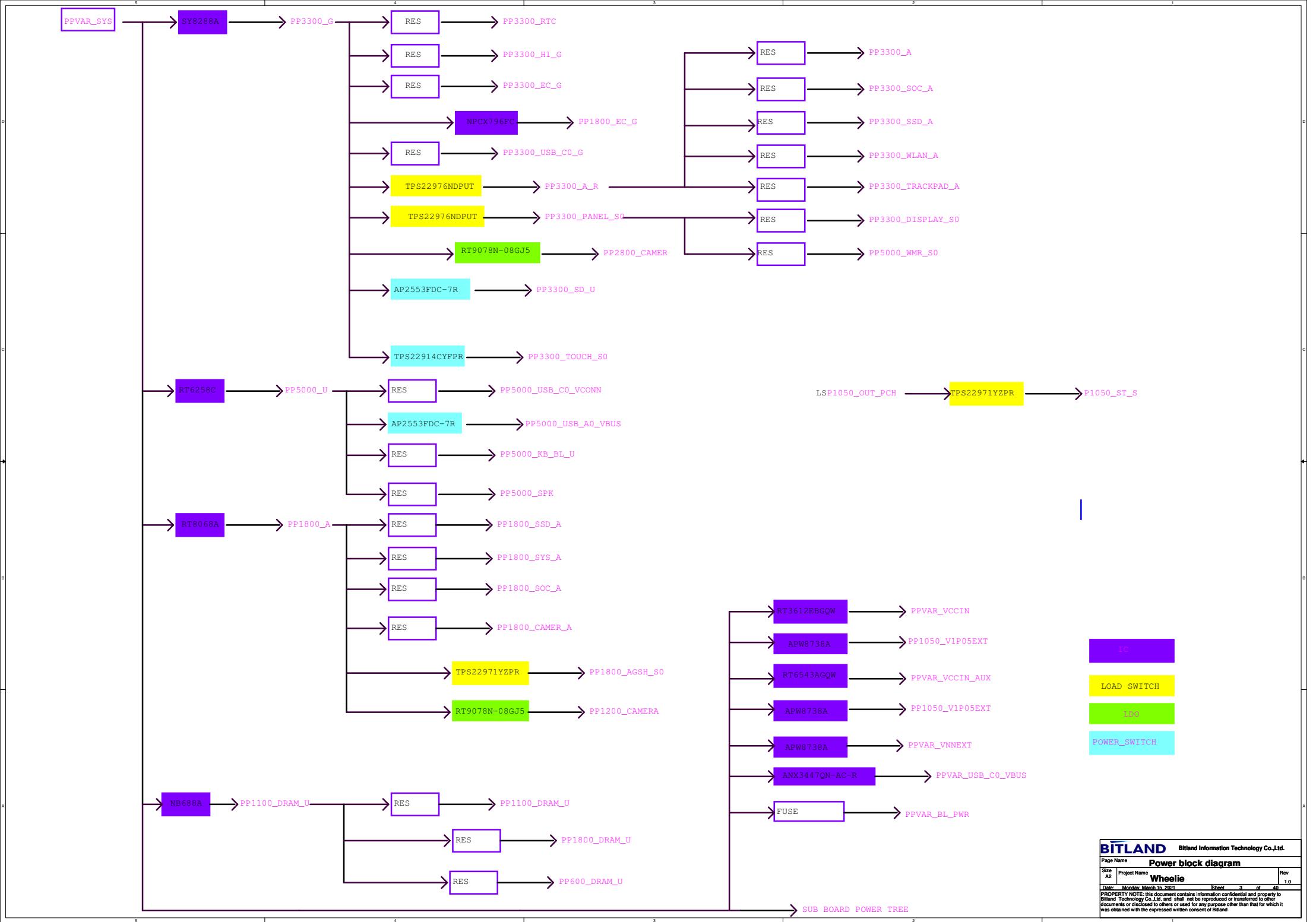
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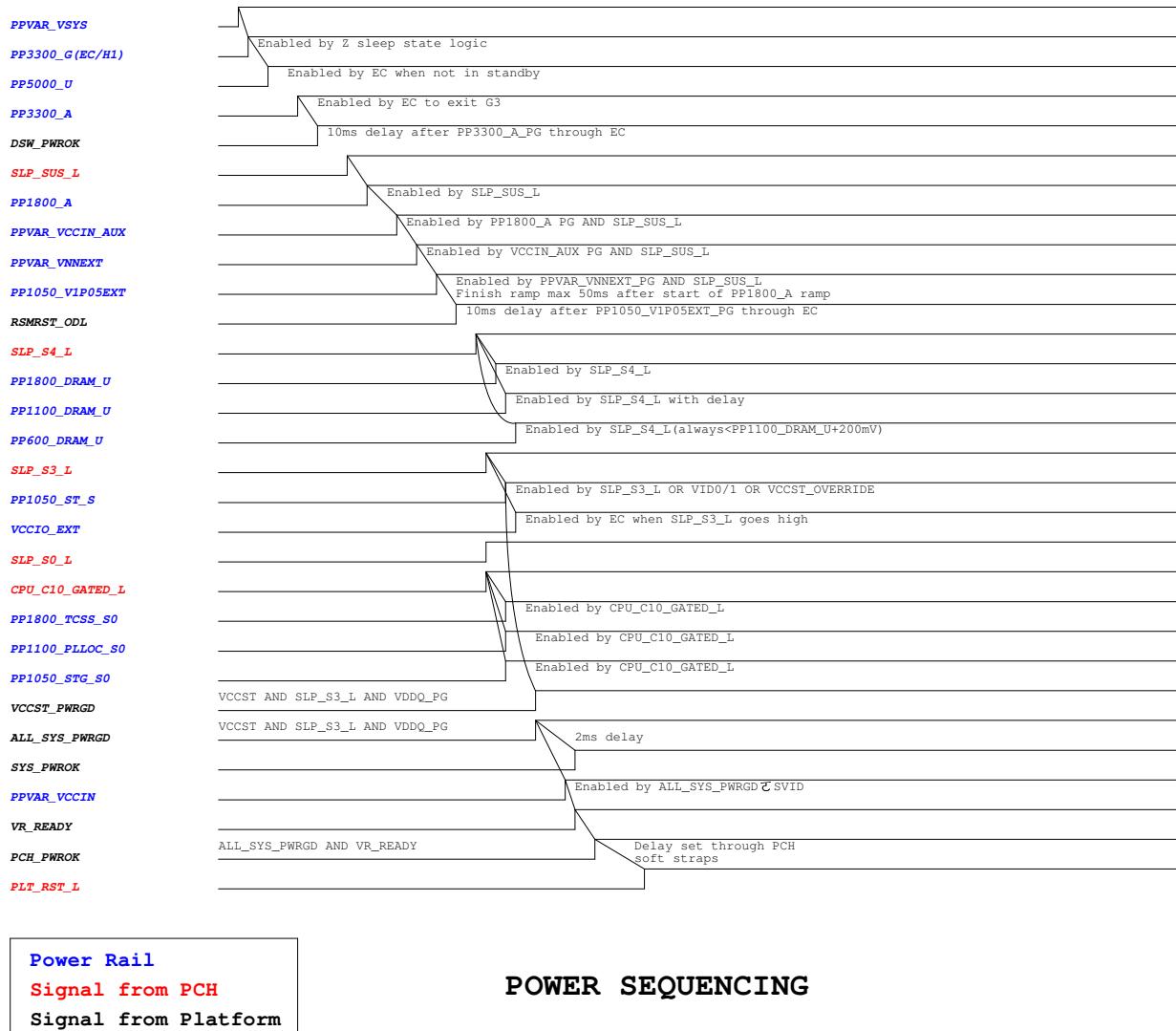
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## POWER SEQUENCING

Master	Prot	Voltage Domain	Bus name	Bus Speed	Slave device	7-bit Addr(s)	Int Priority	Noye
AP (JSL+)	0	PP3300_A	AP_I2C_TP	400KHz	Trackpad	Project-specific	0	Trackpad
	1	PP1800_A	AP_I2C_EMR	400KHz	SUBE-xxxxxMI	0x09	0	NA
					HPDxxxx-920	?	0	NA
	2	PP3300_TOUCH_SD	AP_I2C_TS	400KHz	Touchscreen	Project-specific	0	Touchscreen
					Panel	Project-specific	-	MPI Panel
	3	PP1800_A	AP_I2C_CAMERA	400KHz	UFC	Project-specific	-	NA
					WFC	Project-specific	-	NA
	4	PP1800_A	AP_I2C_AUDIO		ALC1015	0x28(left), 0x29(right)	-	Speakers
					ALC5682I	0x1A	0	Headphone
					DA7219	0x1A	0	Headphone
	5	PP3300_A	AP_I2C_SUB	400KHz	SX9324	Project-specific	0	P seneor

Master	Prot	Voltage Domain	Bus name	Bus Speed	Slave device	7-bit Addr(s)	Int Priority	Noye
EC	IT8320:0	PP3300_EC_G	EC_I2C_EEPROM	400KHz	N24C02	0x30 (PSWP), 0x50 (memory)	-	CBI EEPROM
					IT8320	0x35, 0x5A, 0x79	-	EC flash
	IT8320:1	PP3300_EC_G	EC_I2C_BATTERERY	100KHz	Smart battery	Project-specific	-	Fuel gauge
					LSM6DS3TR	0x6A	-	Gyro/accel
					LIS2DWLTR	TBD	-	Lid accel
					KB backlight	Project-specific	-	KB backlight
					IMVP9	Project-specific	-	IMVP9
	IT8320:2	PP1800_A	EC_I2CSENSOR	400KHz	RAA489000	0x09(+0x0C SMBus ARA)	1	Charger
					RAA489000	0x22	0	TCPC
					IT5205	0x48	-	MUX
					PI3USB9201	0x5F	2	BC1.2

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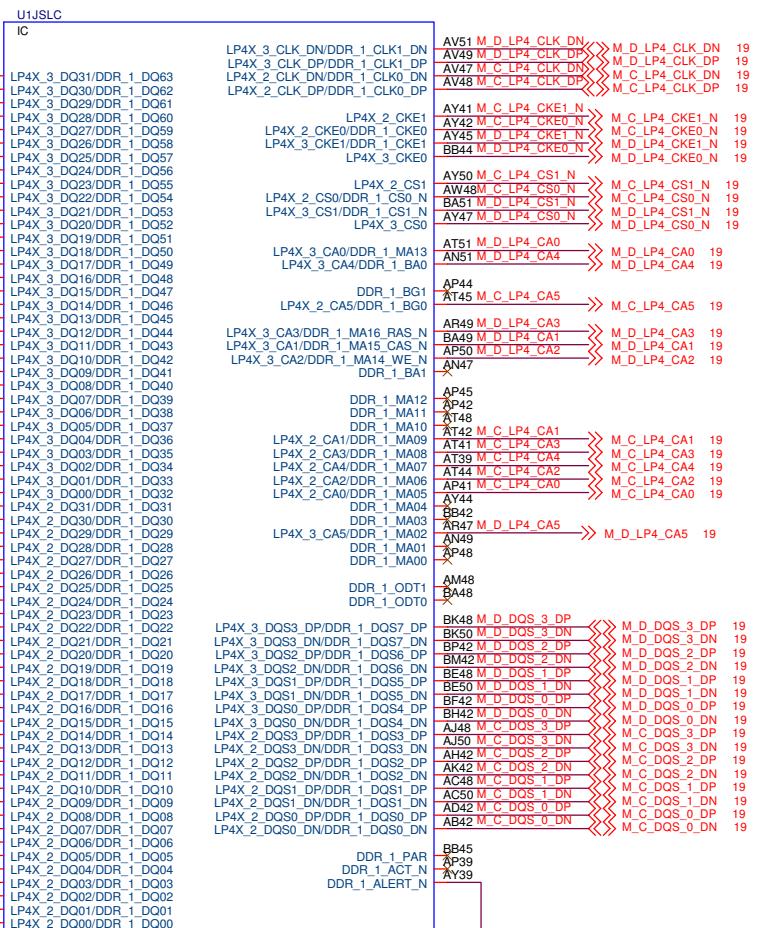
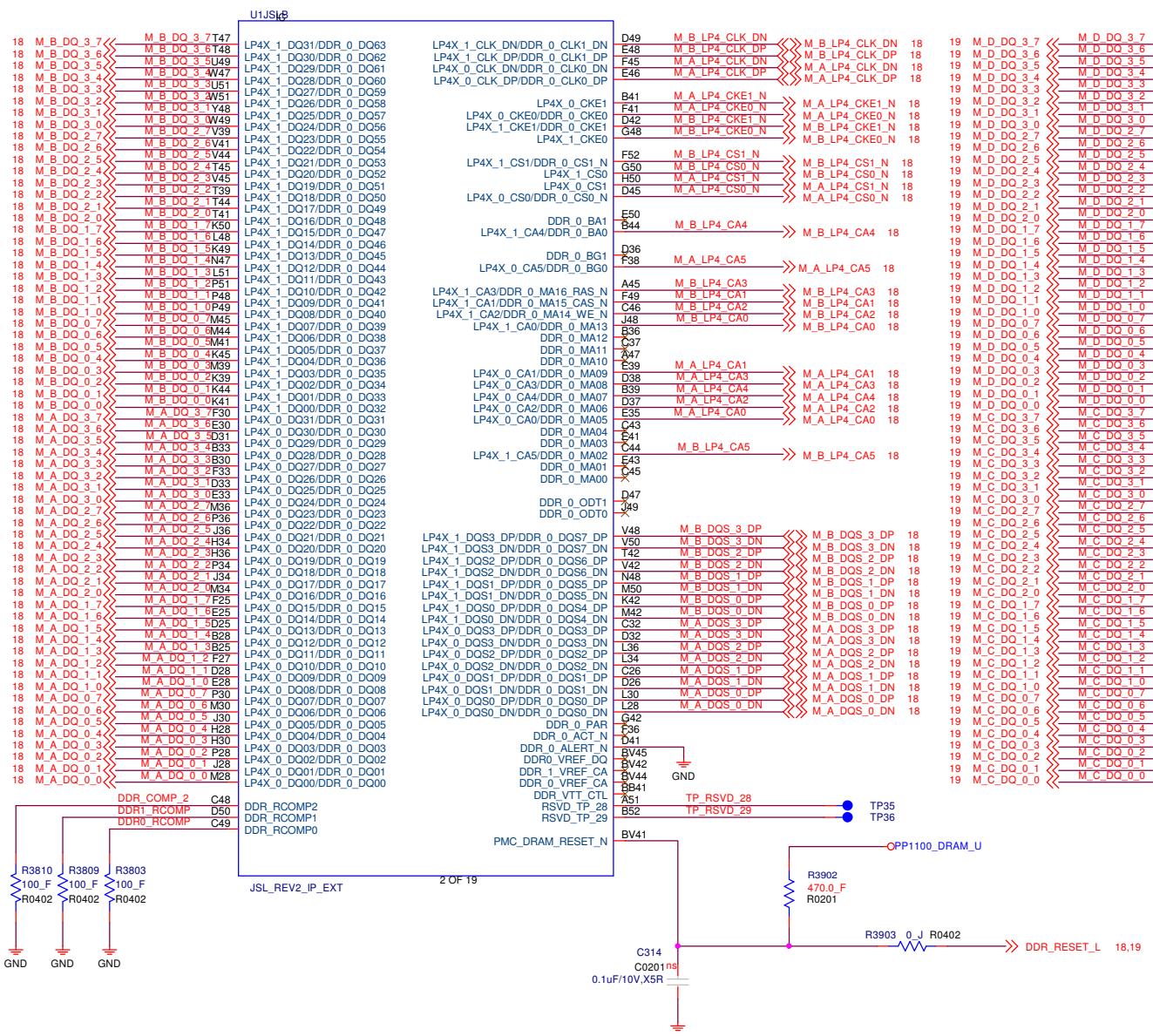
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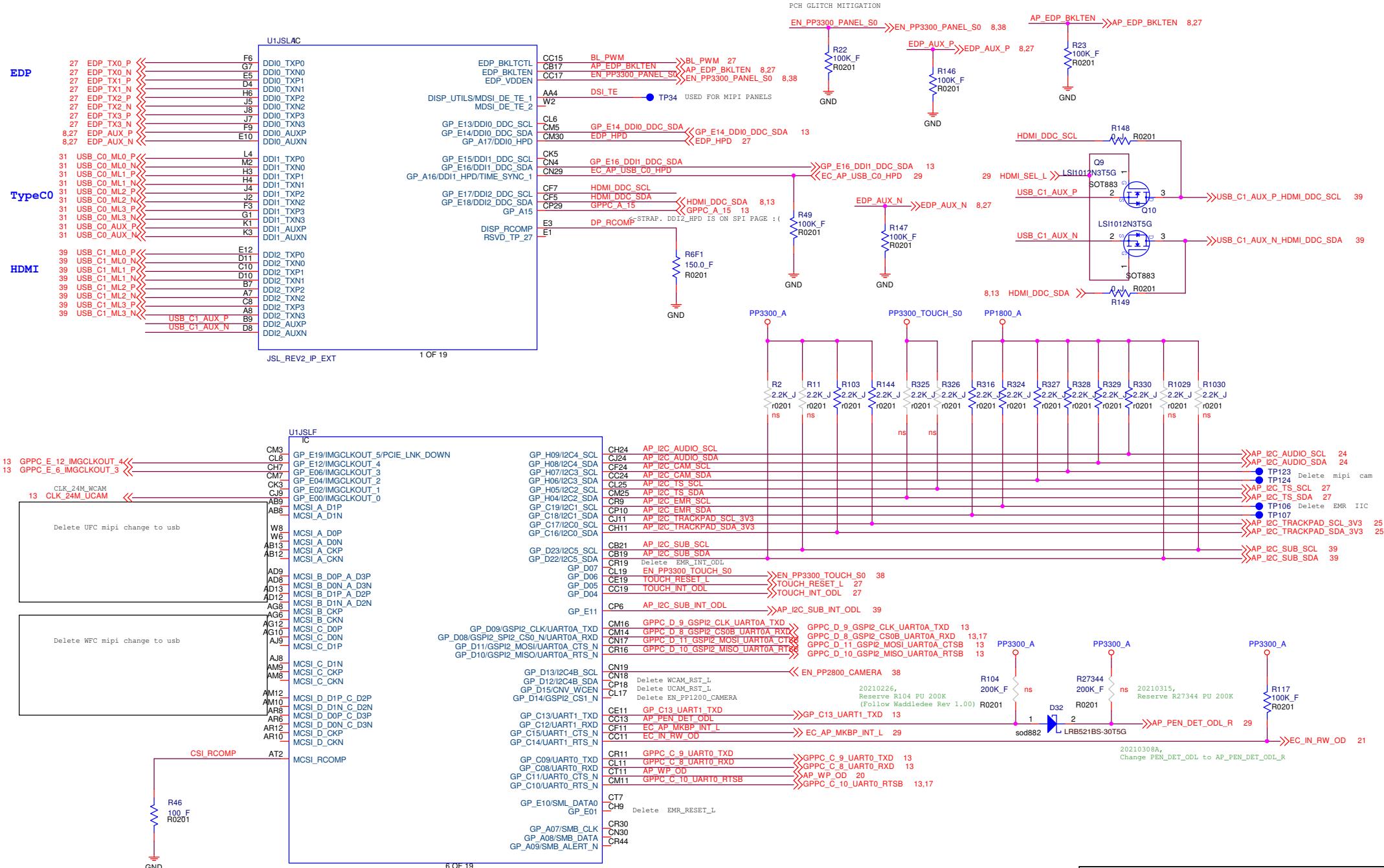
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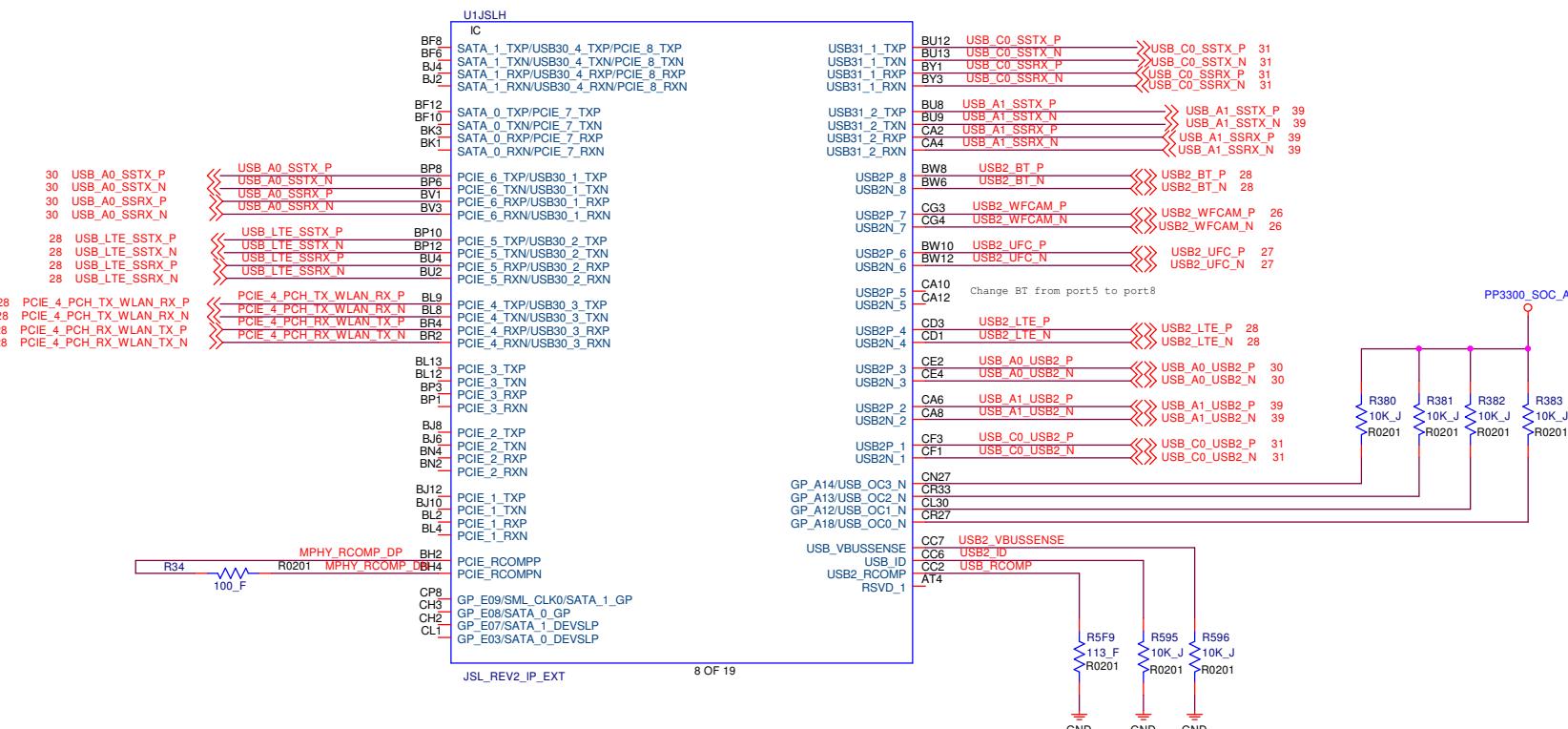
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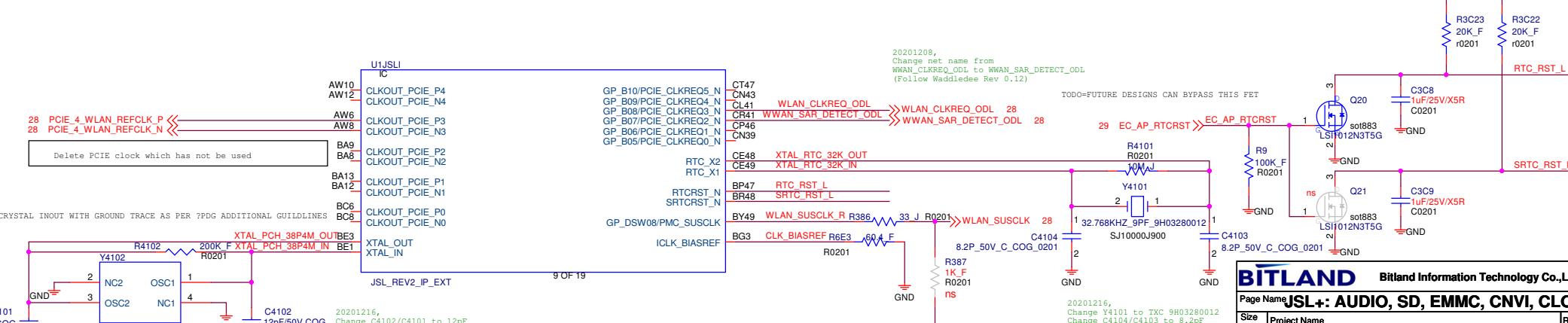
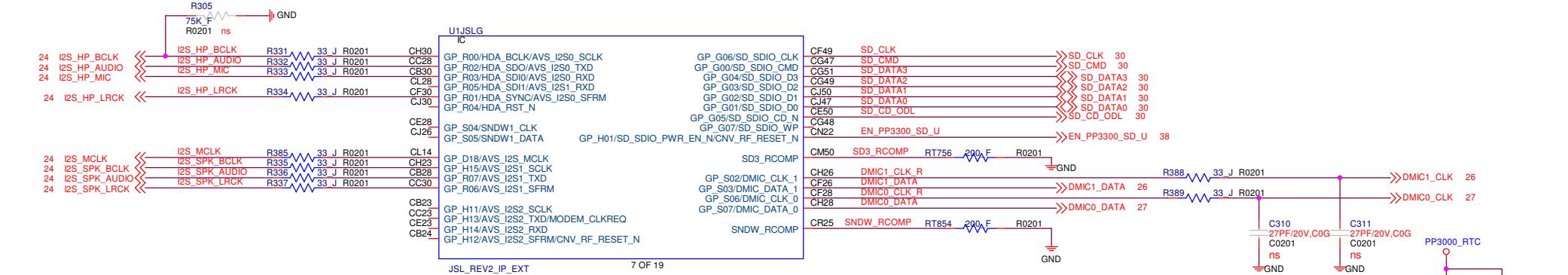
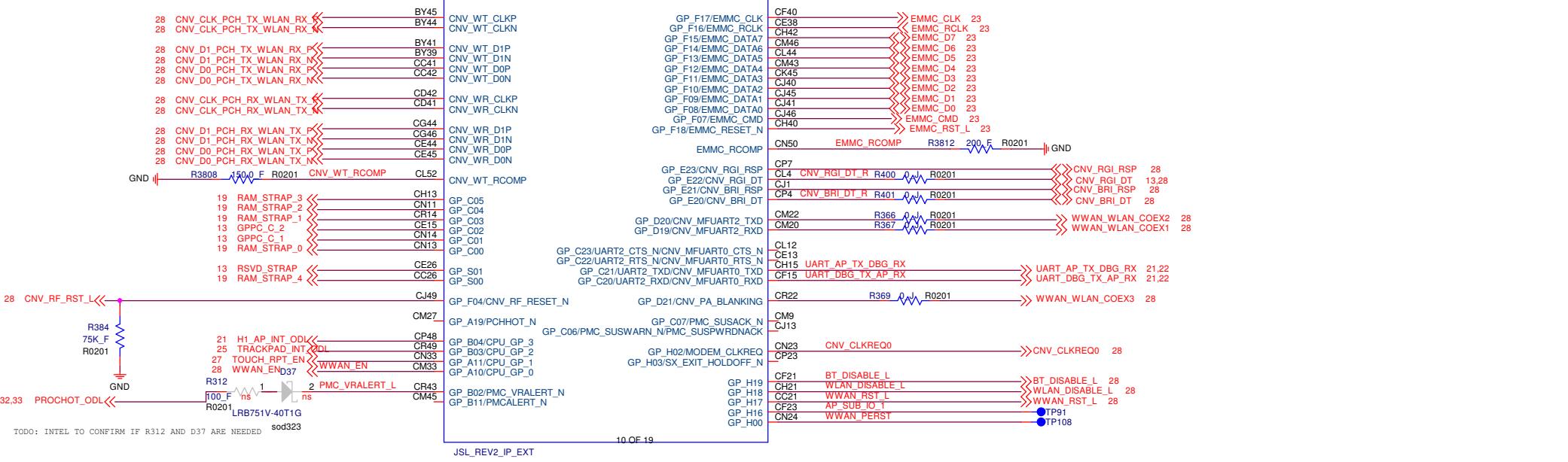
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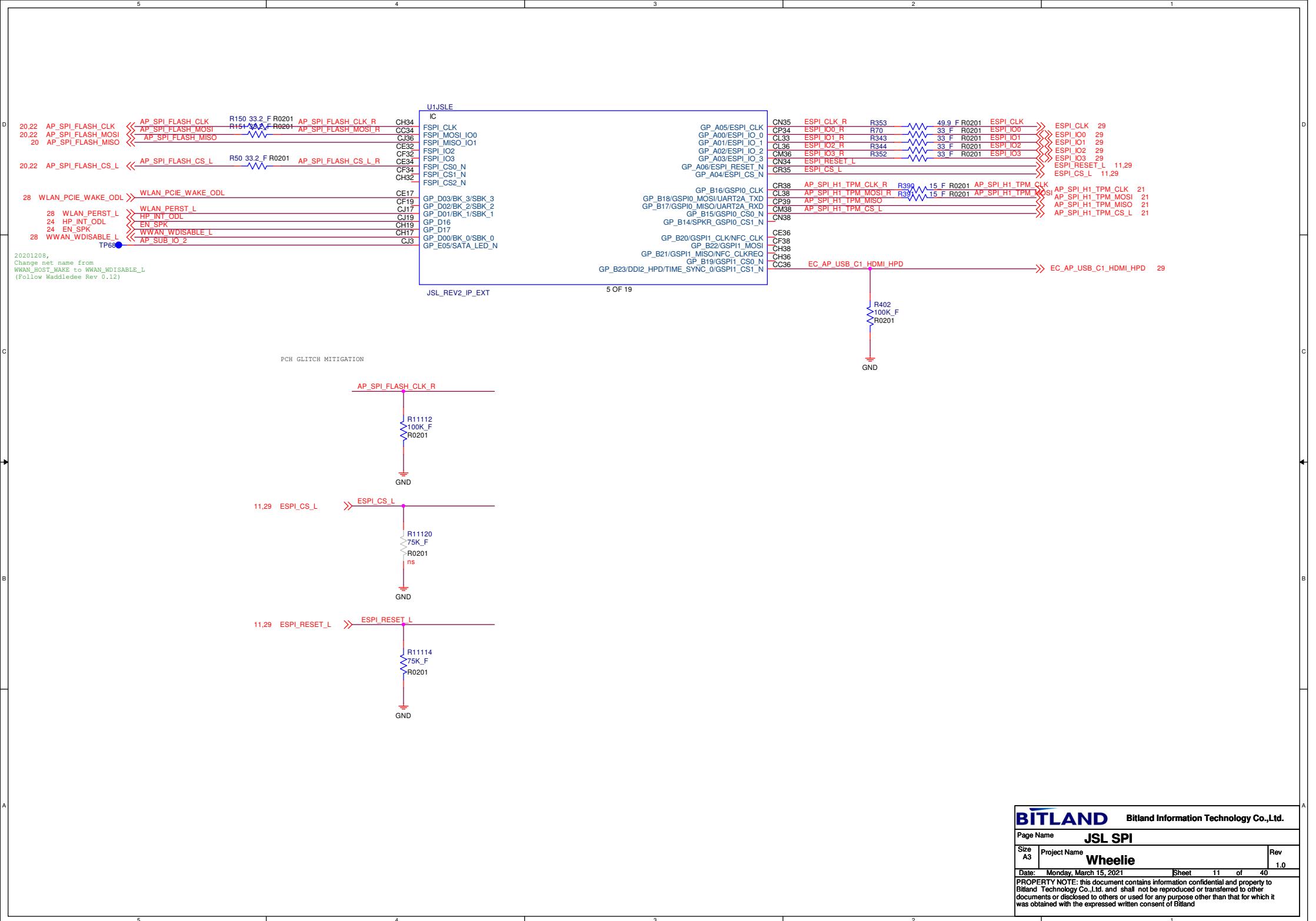


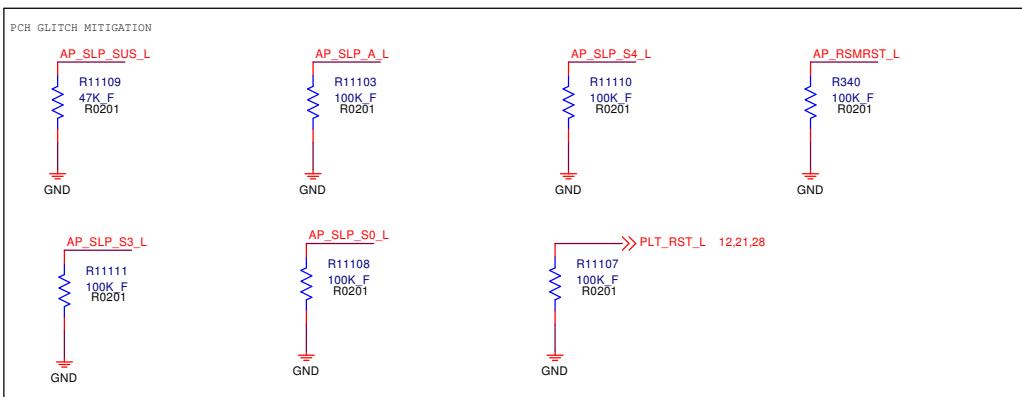
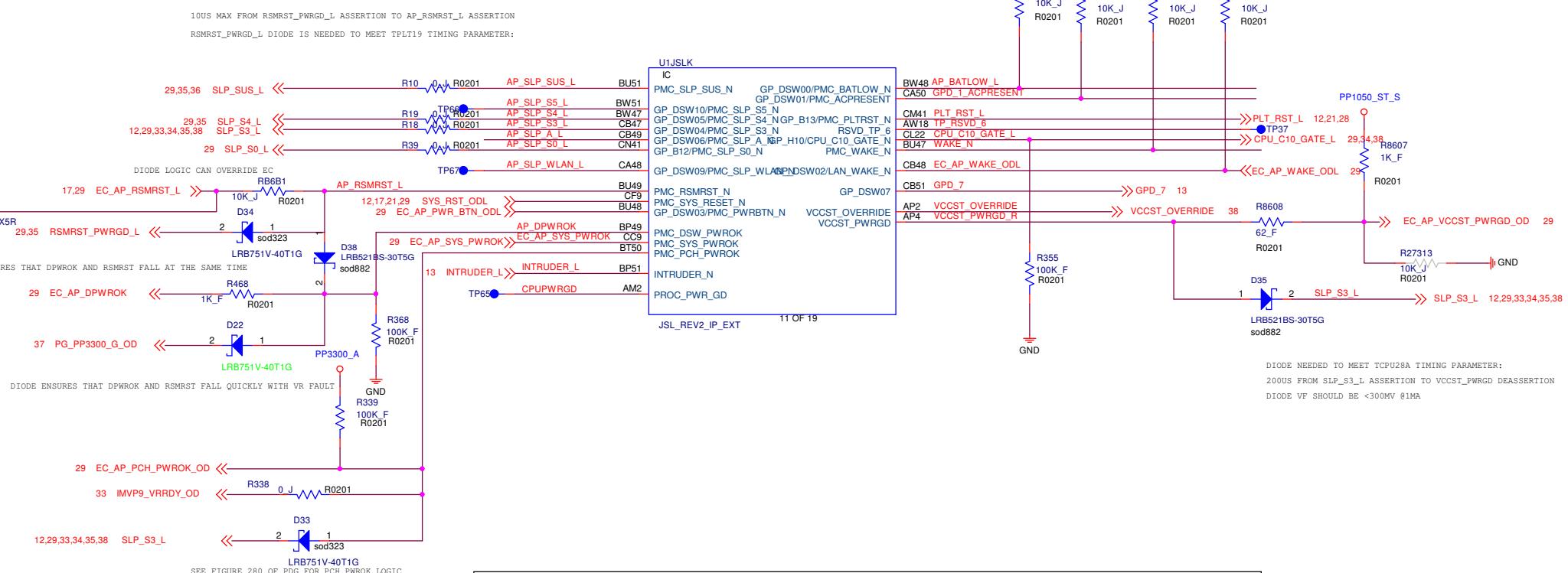
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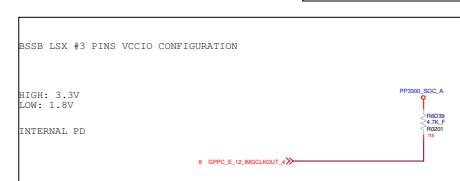
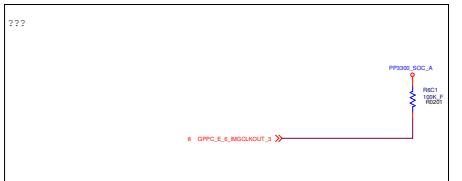
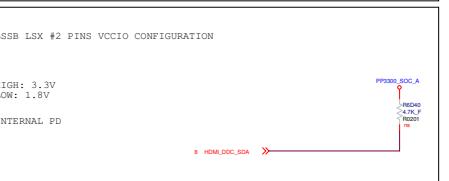
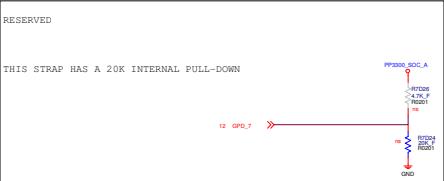
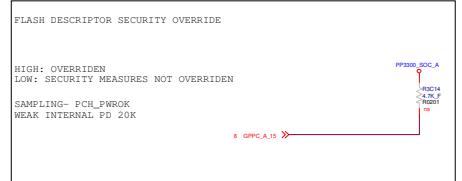
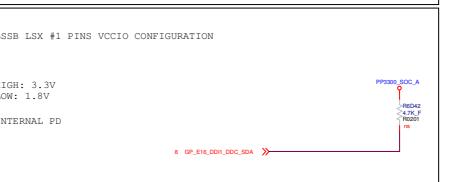
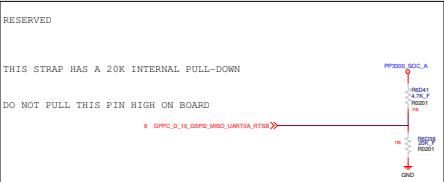
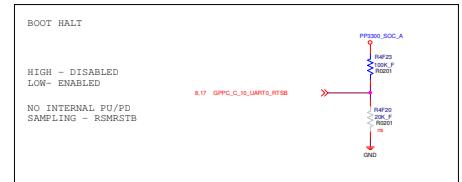
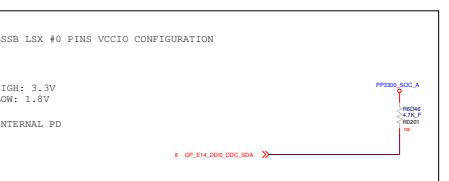
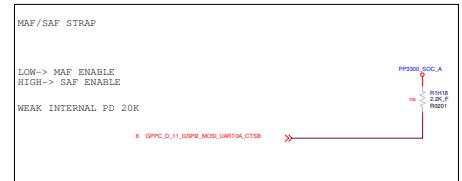
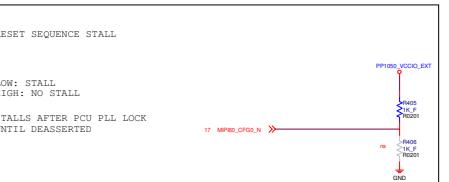
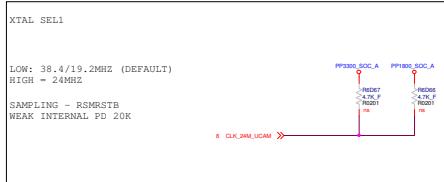
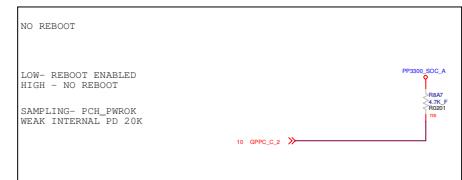
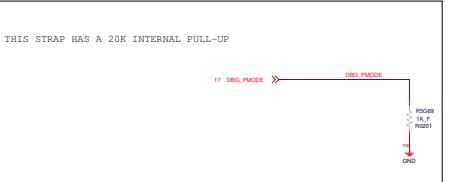
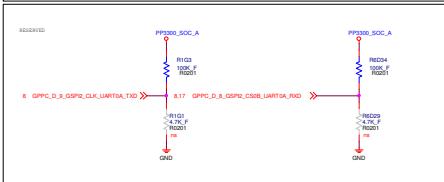
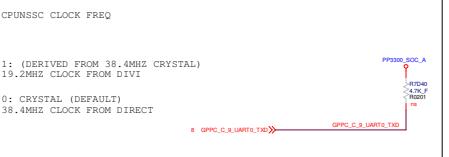
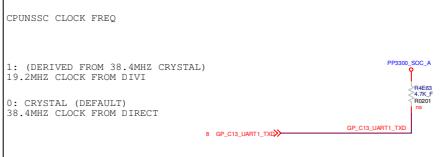
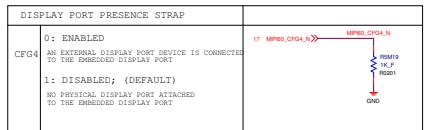


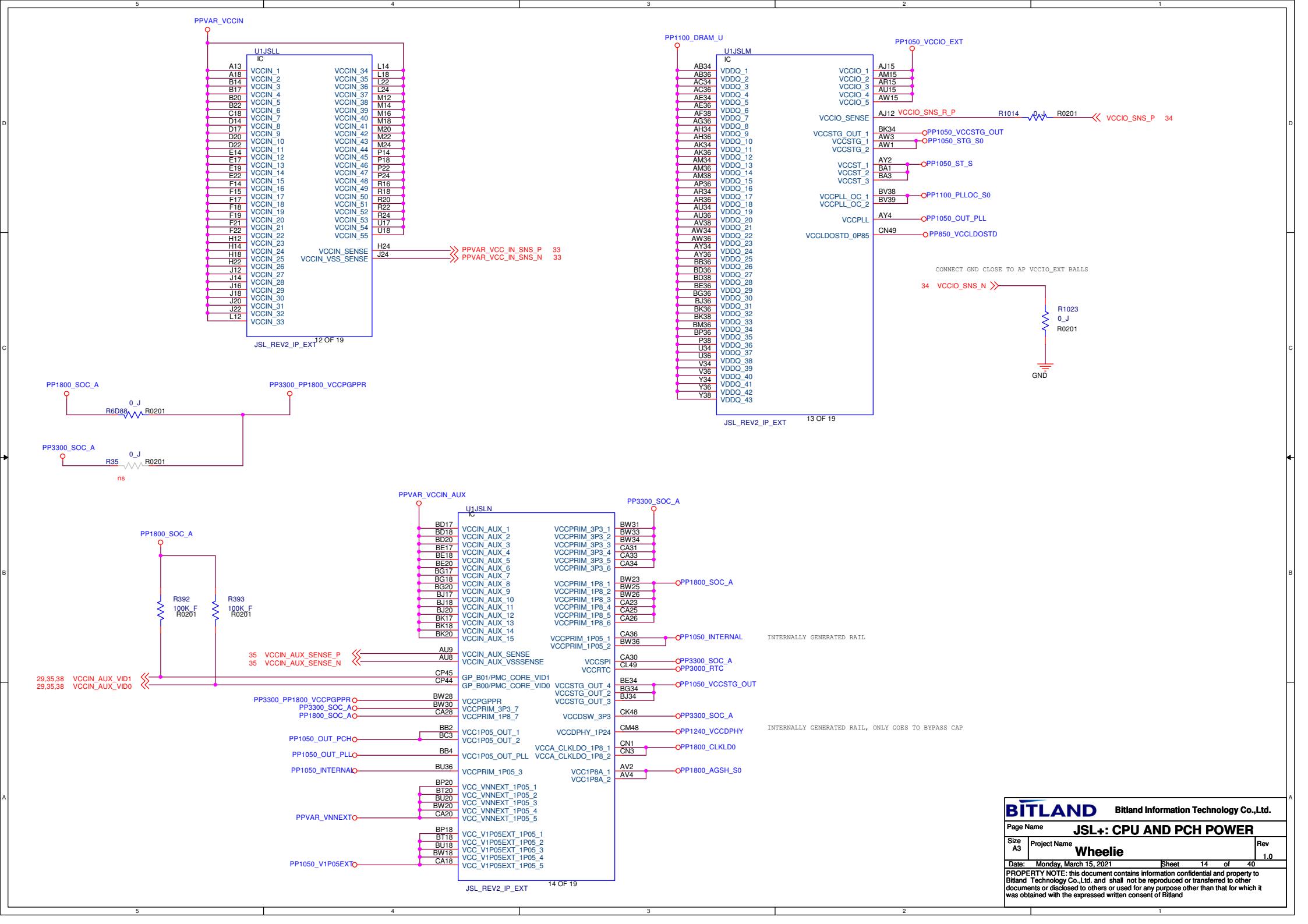
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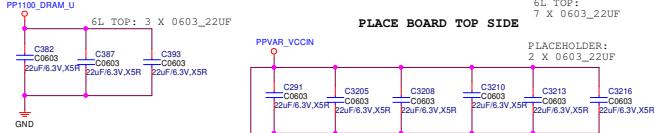
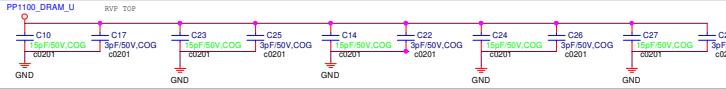




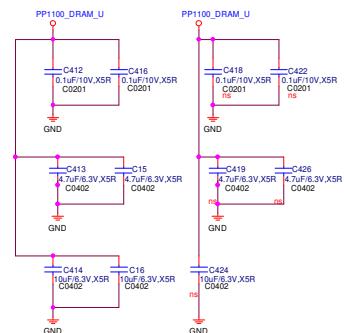




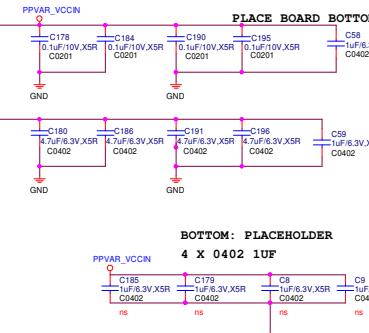
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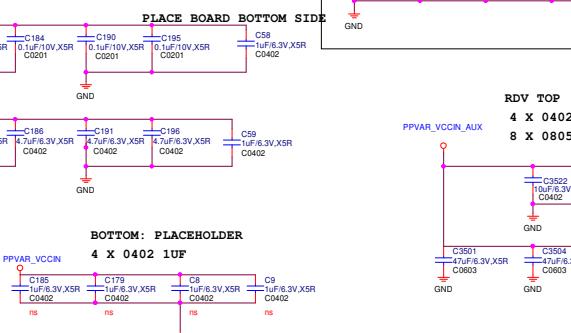
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2 X 0402\_4.7UF  
2 X 0402\_10UF



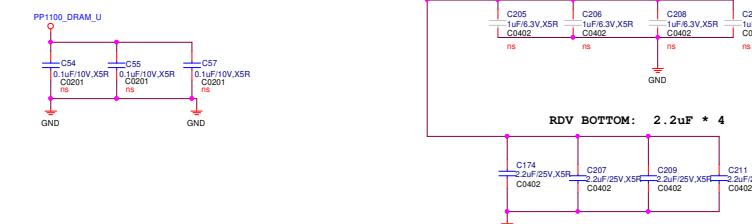
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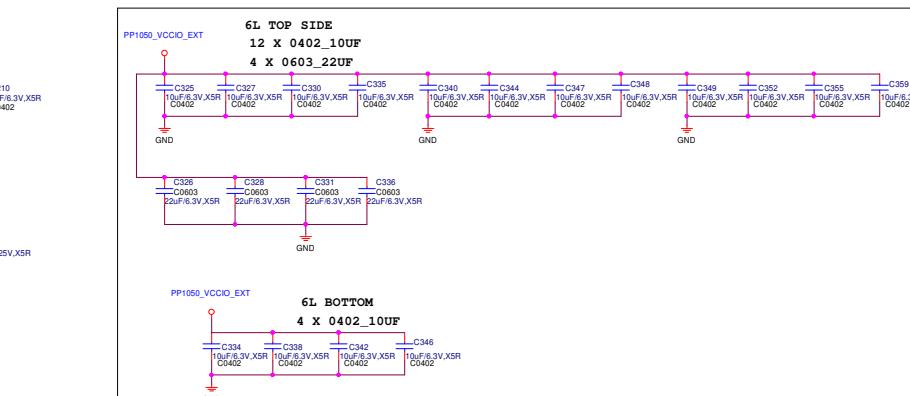
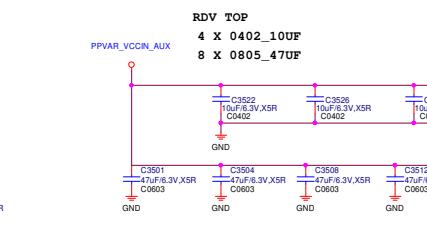
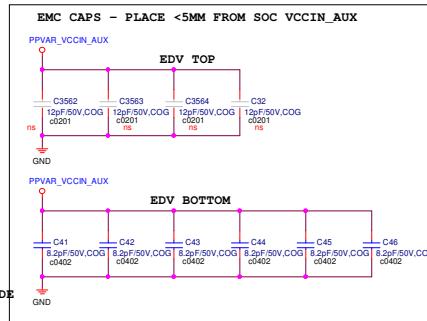
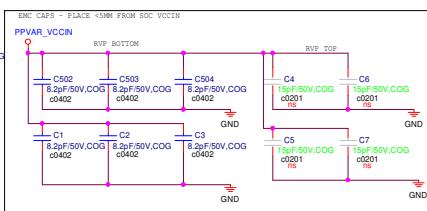
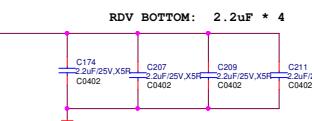
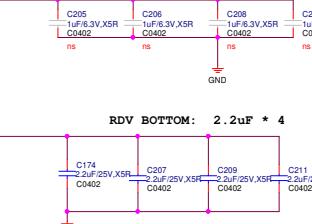
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2 X 0402\_10UF



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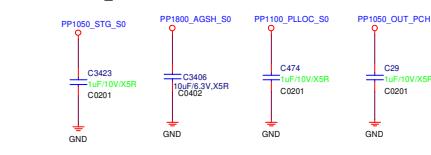
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4 X 0402\_1UF



+VCCSTG\_OUT

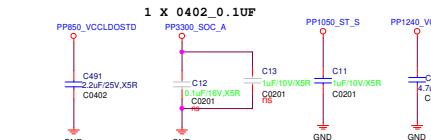
RVP TOP SIDE RVP TOP SIDE RVP TOP SIDE RVP TOP SIDE RVP TOP SIDE

1 X 0402\_1UF X 0402\_10UF X 0402\_1UF 1 X 0402\_1UF



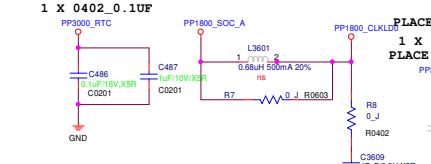
RVP TOP SIDE 6L TOP SIDE RVP TOP SIDERSRV TOP SIDE

1 X 0402\_1UF 1 X 0402\_1UF 1 X 0402\_1UF

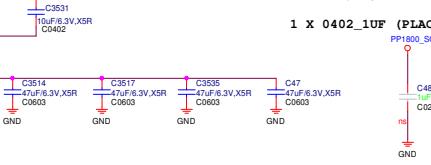


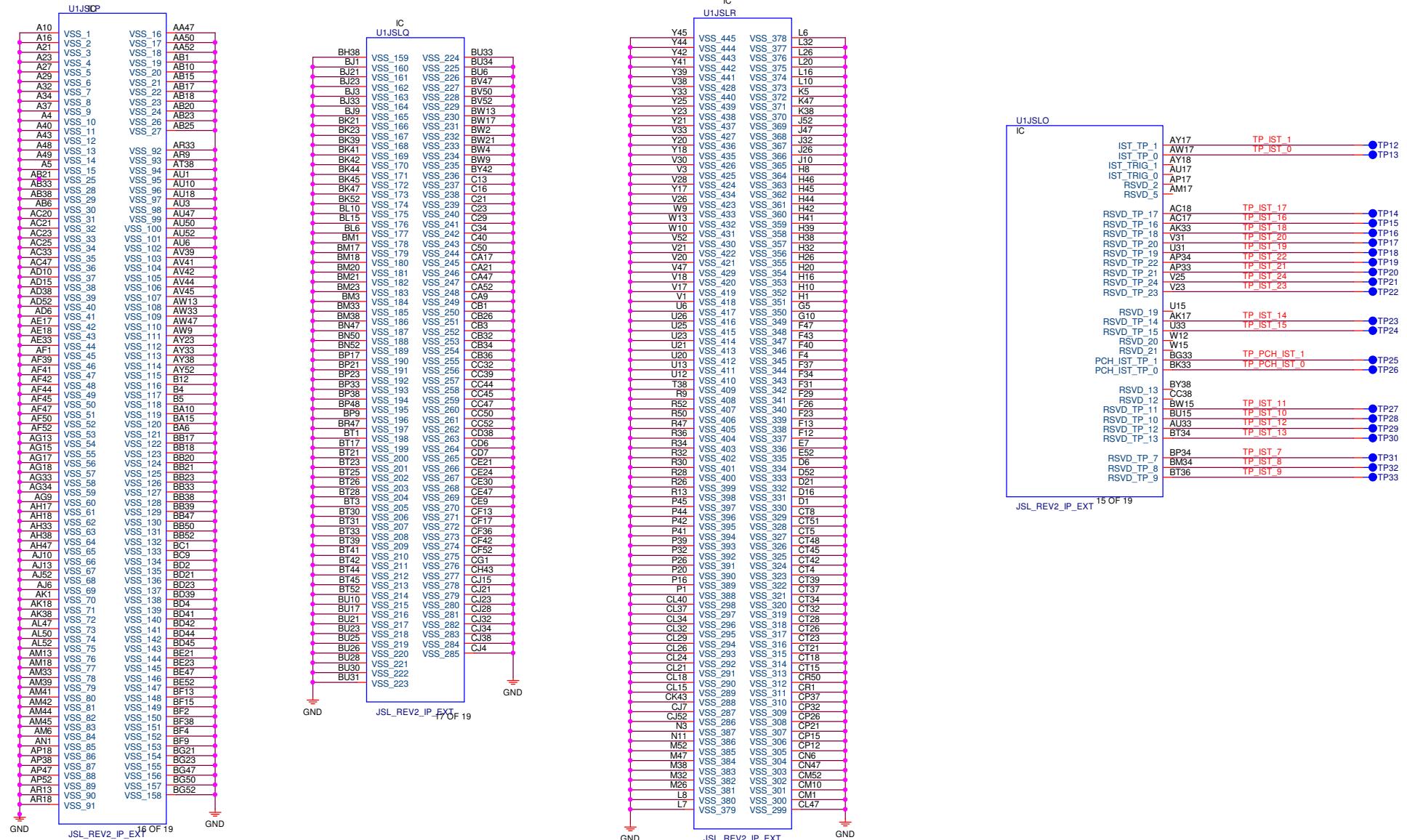
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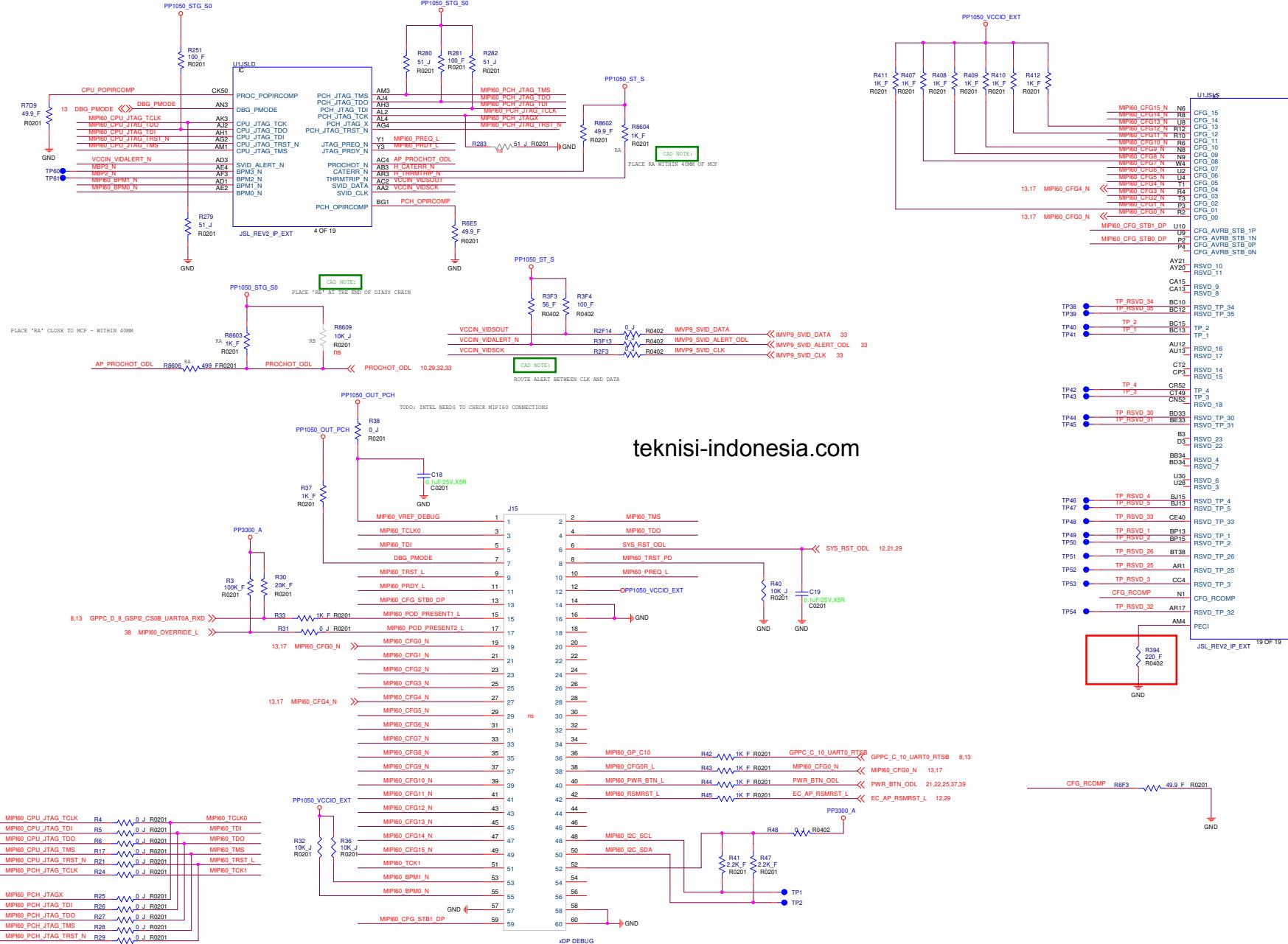
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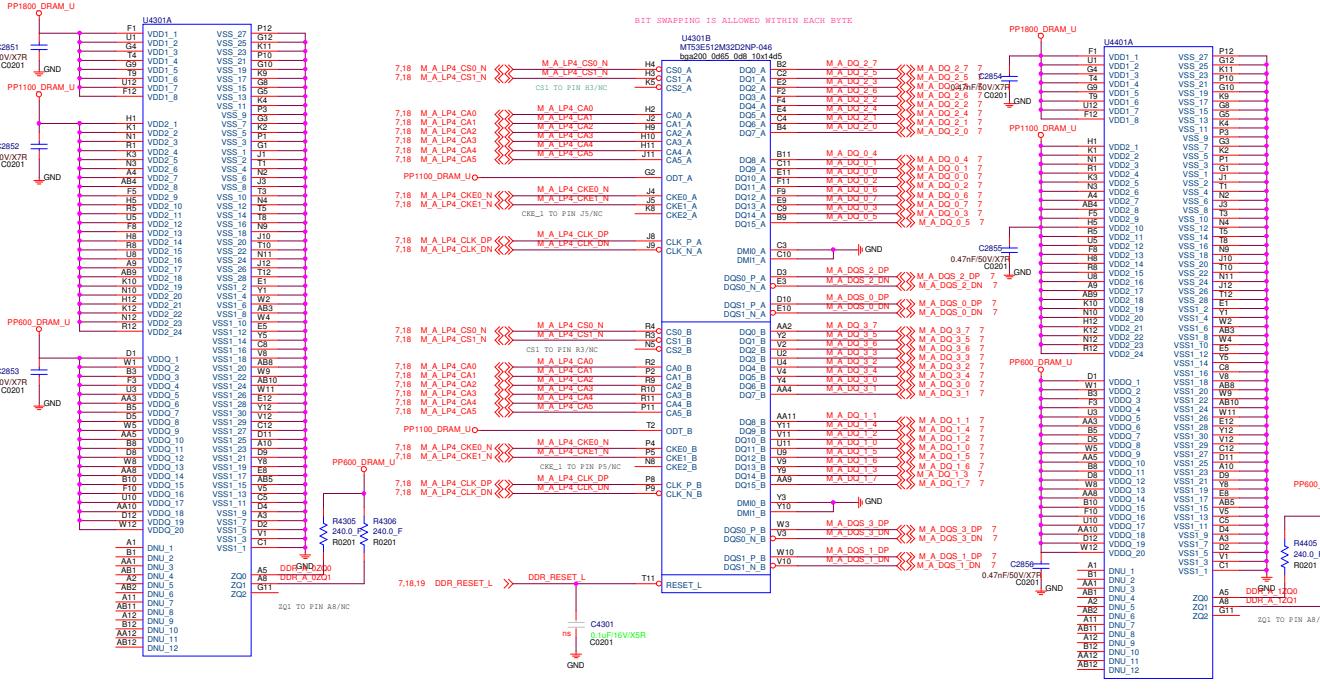


RVP TOP SIDE 1 X 0402\_1UF (PLACE HOLDER)





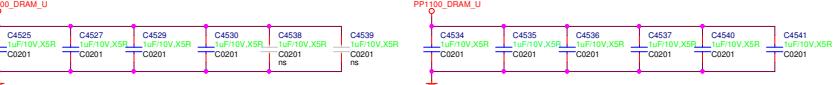




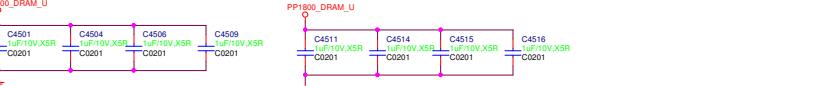
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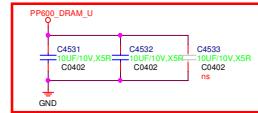
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**AD NOTE:** DISTRIBUTE EVENLY, 4 FOR EACH DRAM



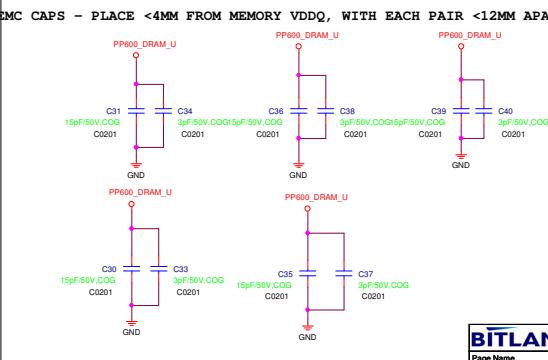
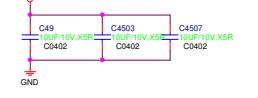
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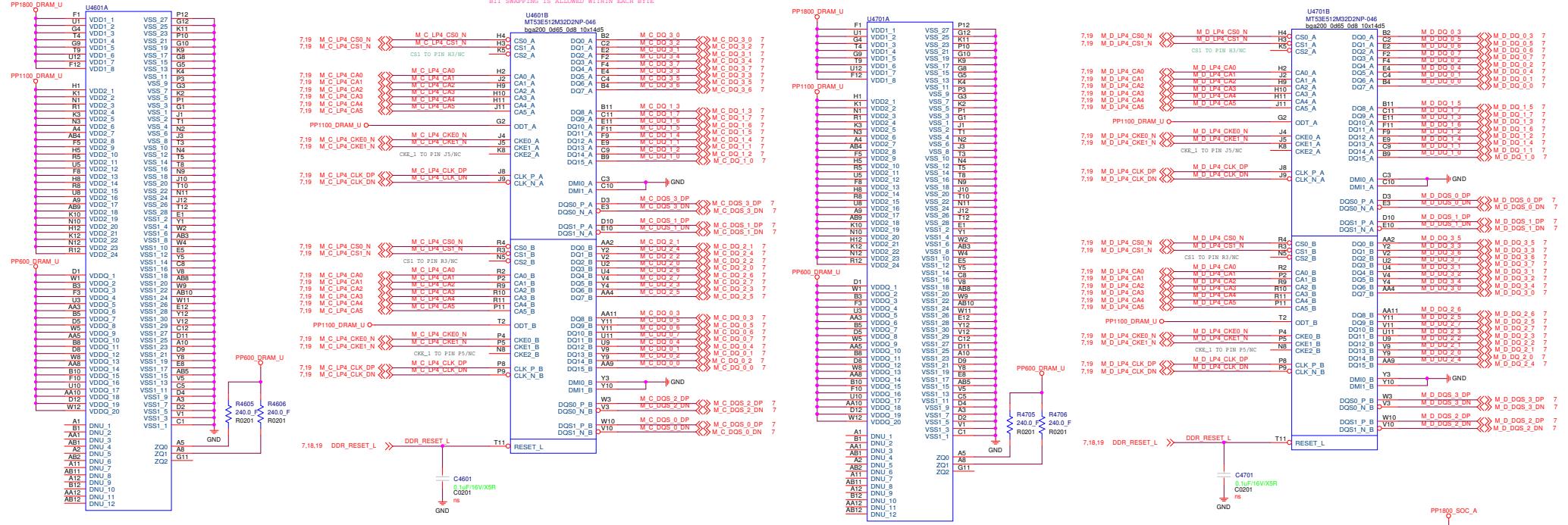
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CAD NOTE: DISTRIBUTE EVENLY FOR EACH DRAM

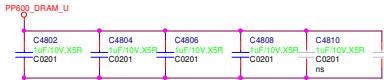


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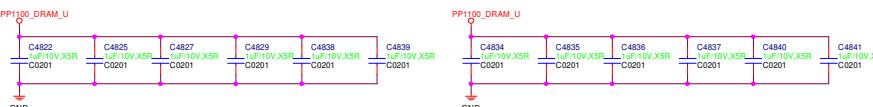
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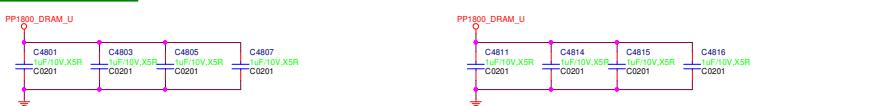
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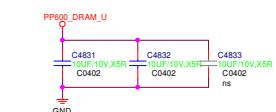
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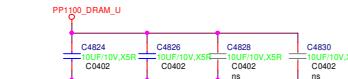
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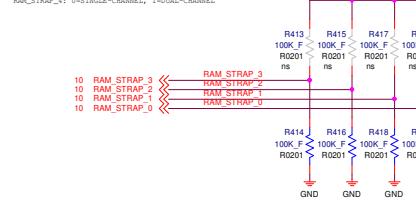
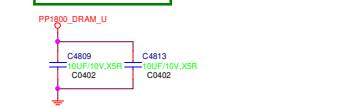
CAD NOTE:

DISTRIBUTE EVENLY FOR EACH DRAM



GAP NOTE:

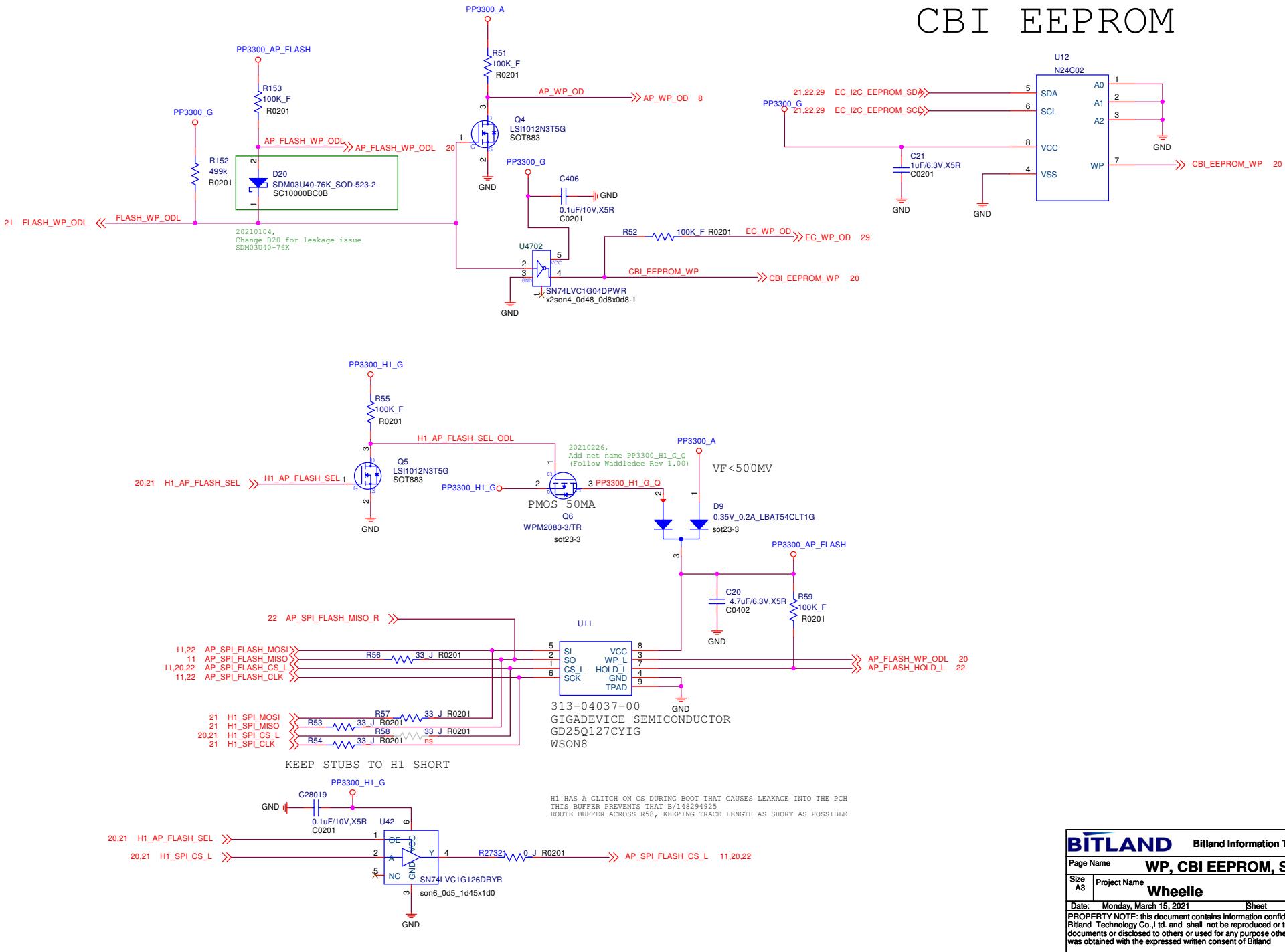
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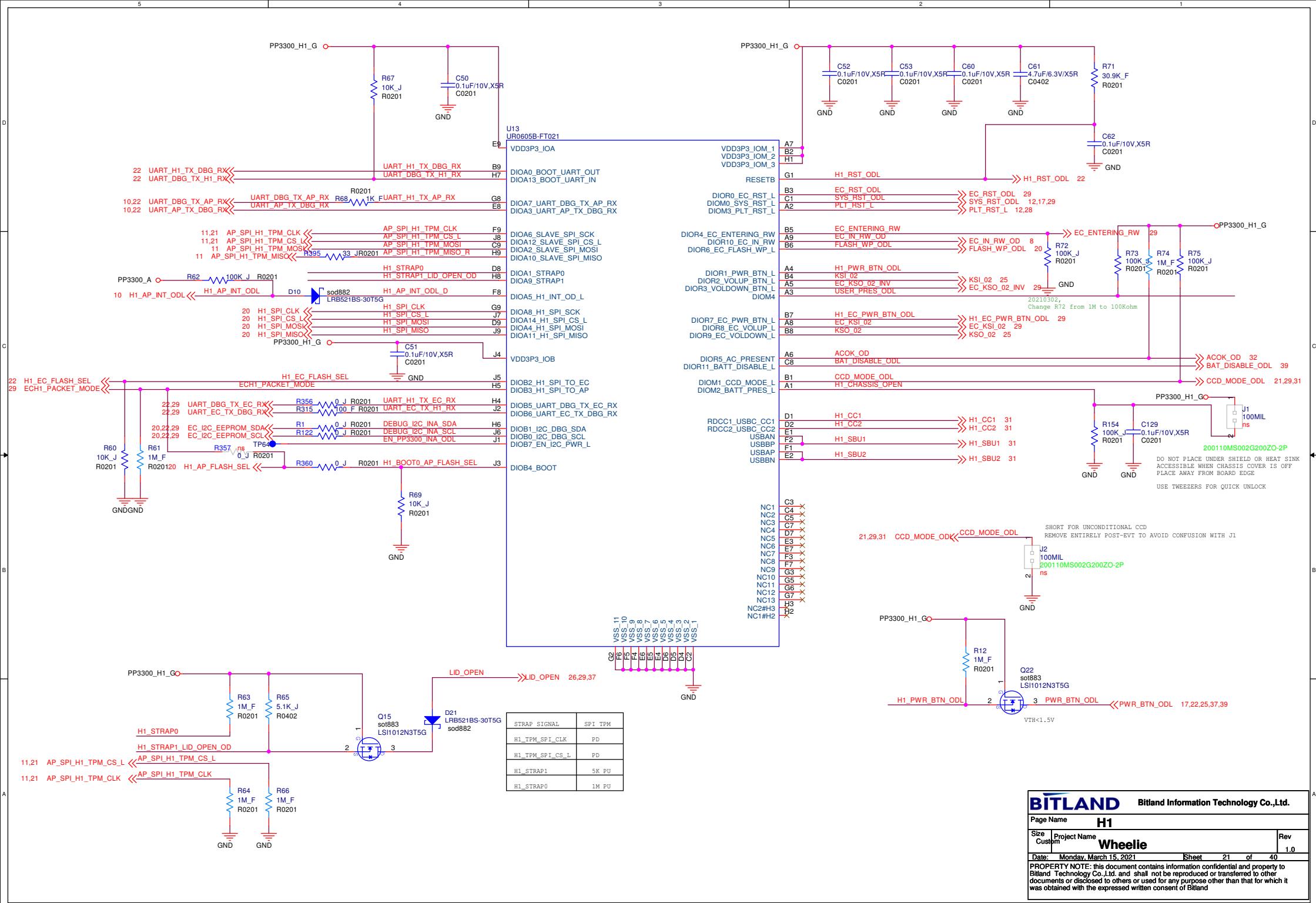


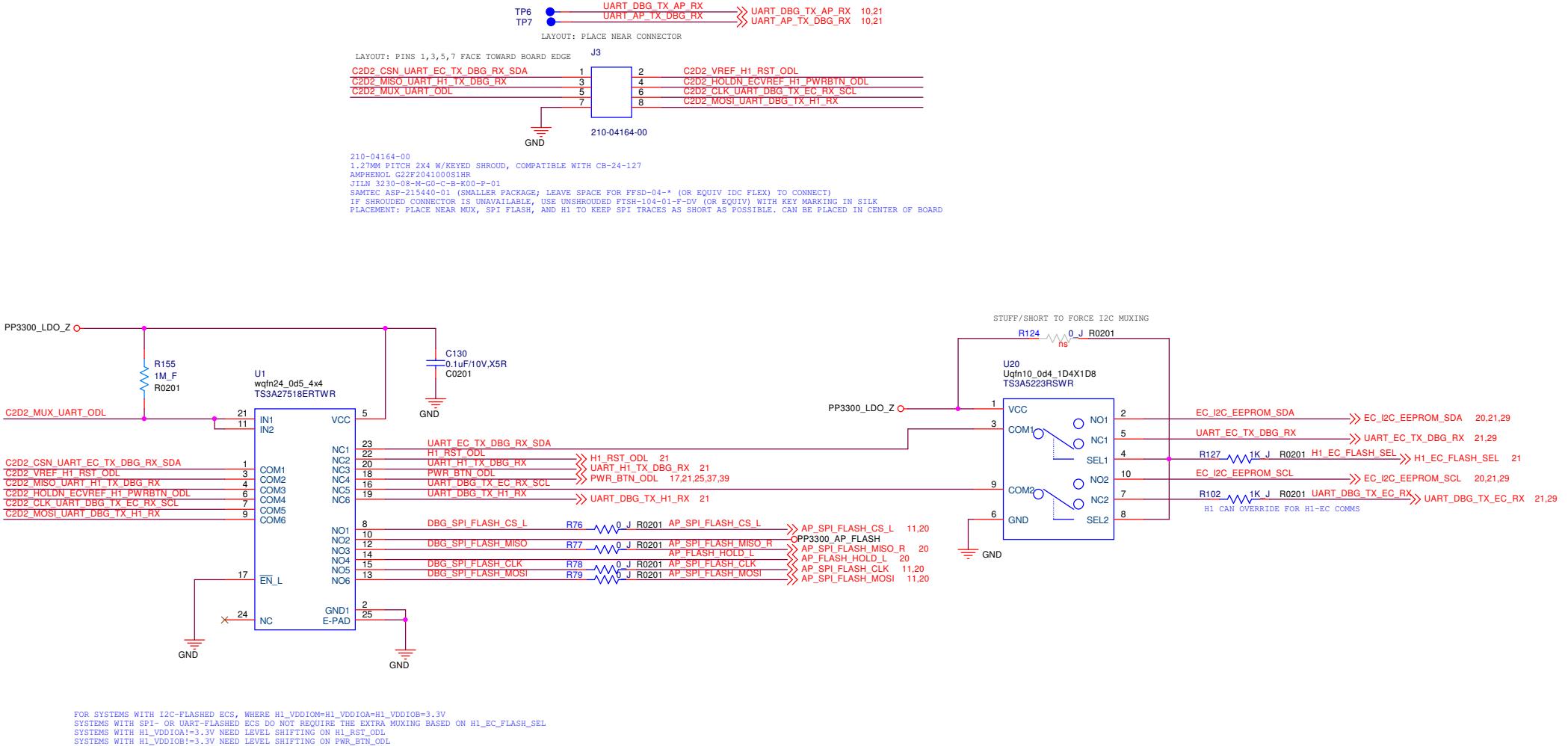
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# WP TREE

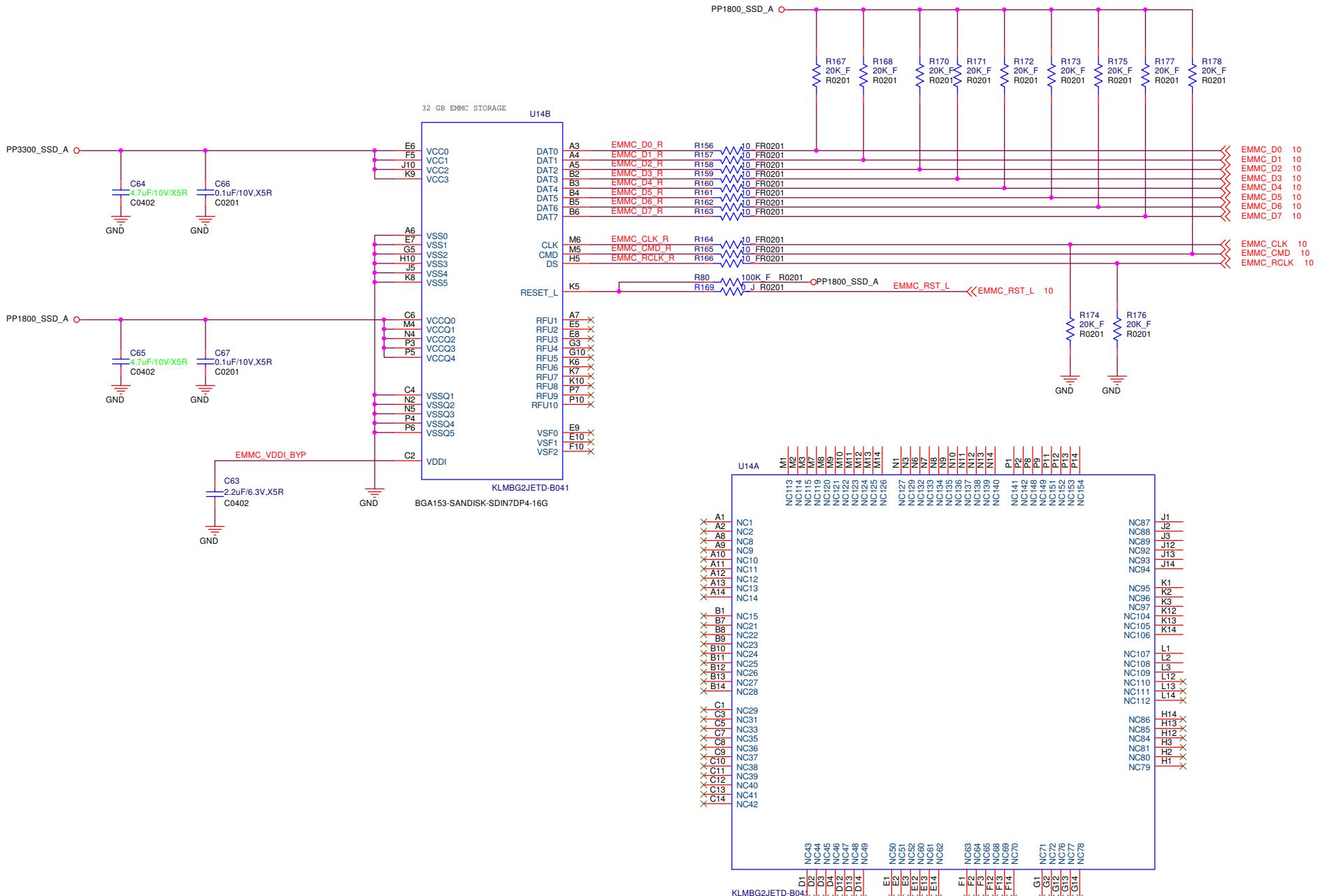
# CBI EEPROM

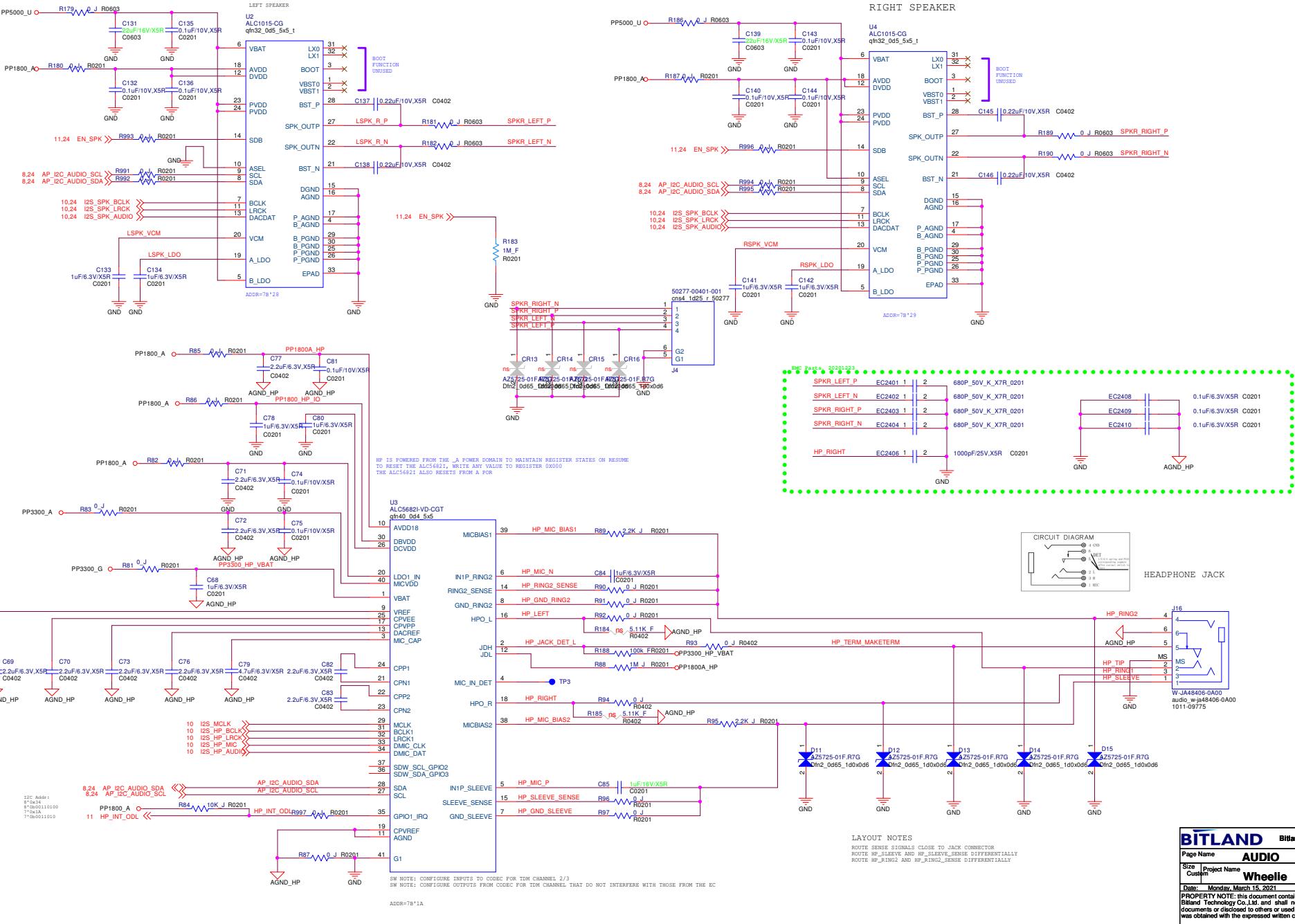




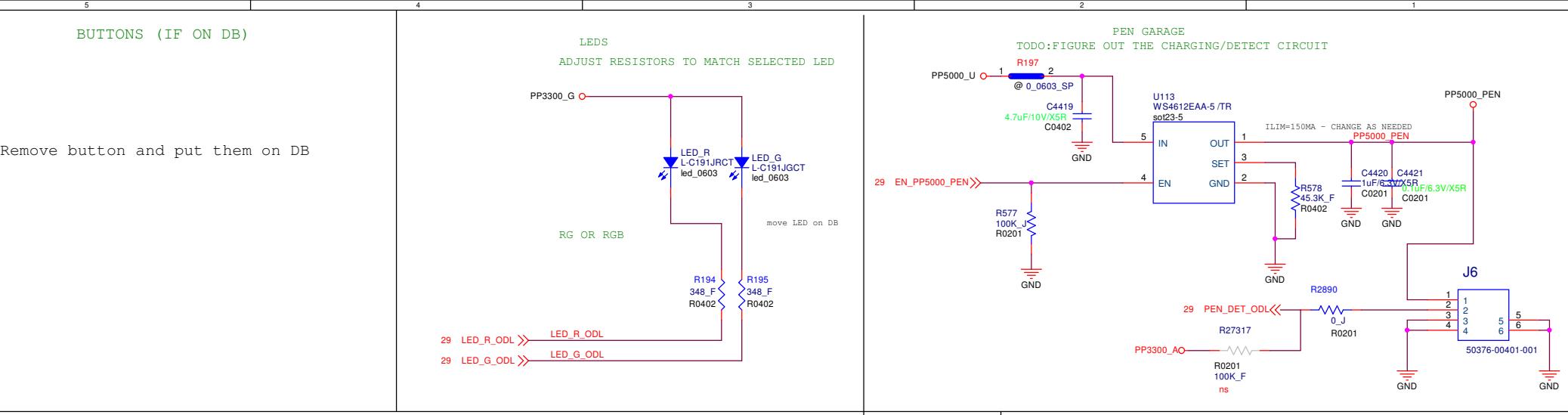


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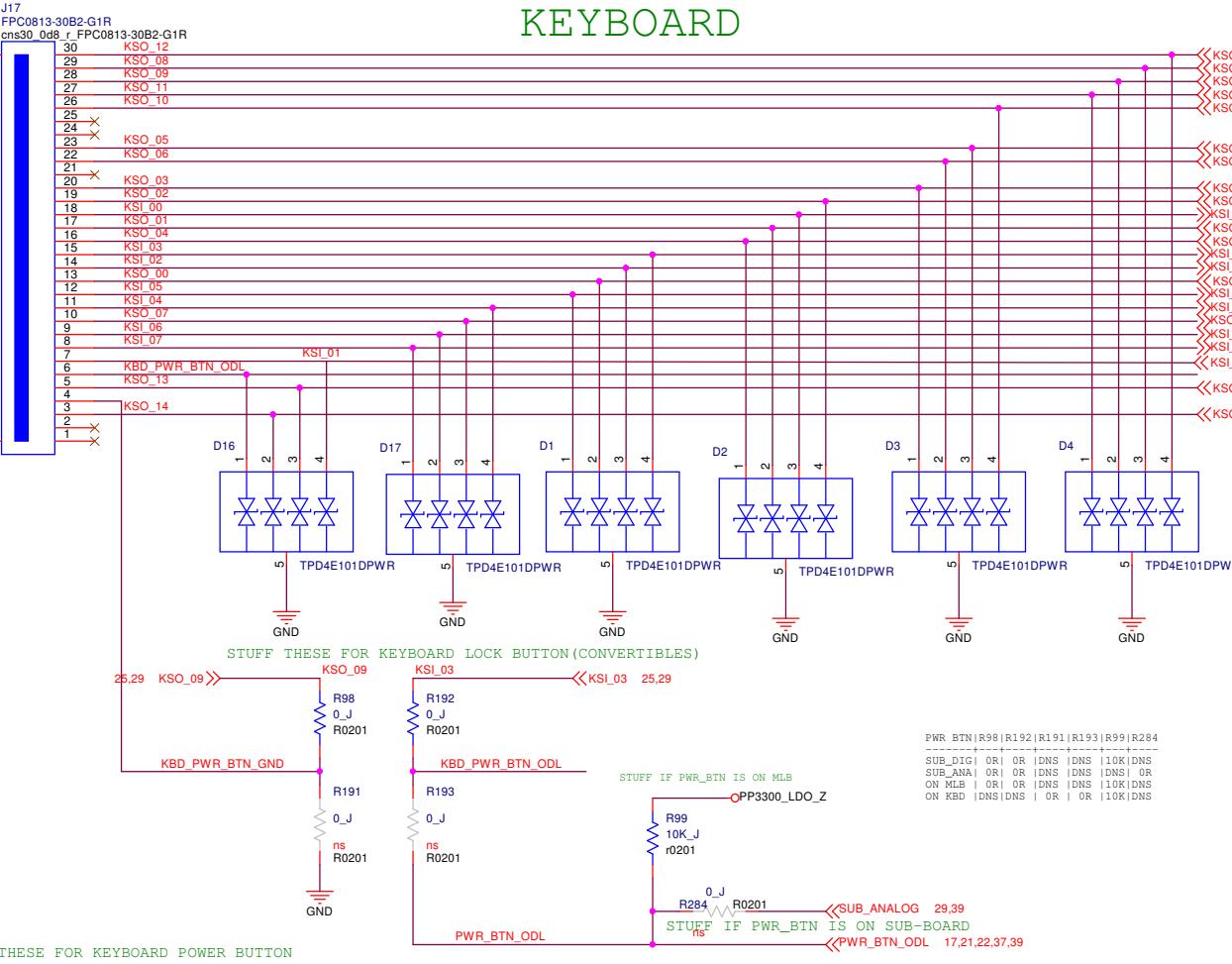




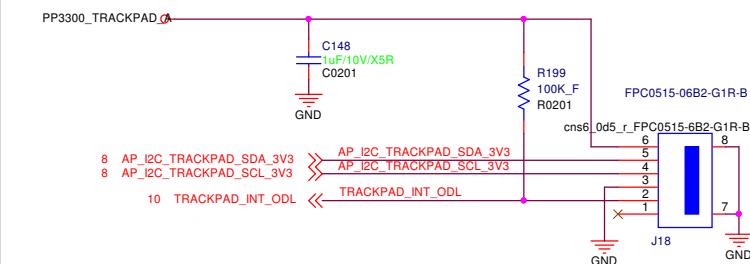
<b>BITLAND</b>		Bitland Information Technology Co.,Ltd.		
Page Name	<b>AUDIO</b>			
Size	Project Name			Rev
Custom	<b>Wheelie</b>			1.0
Date:	Monday, March 15, 2021	Sheet	24	of 40
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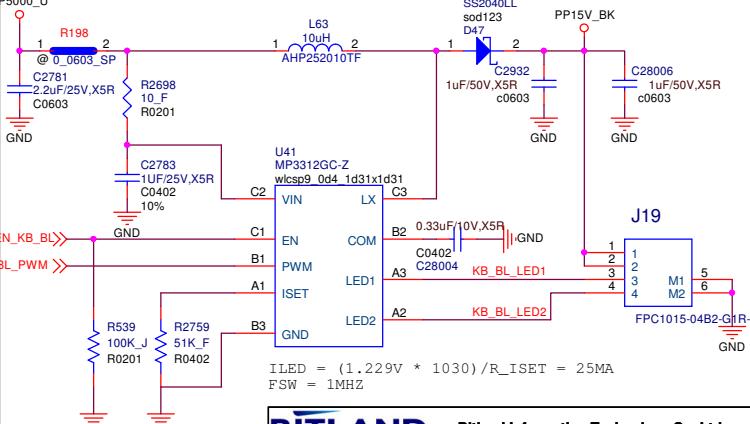
# KEYBOARD



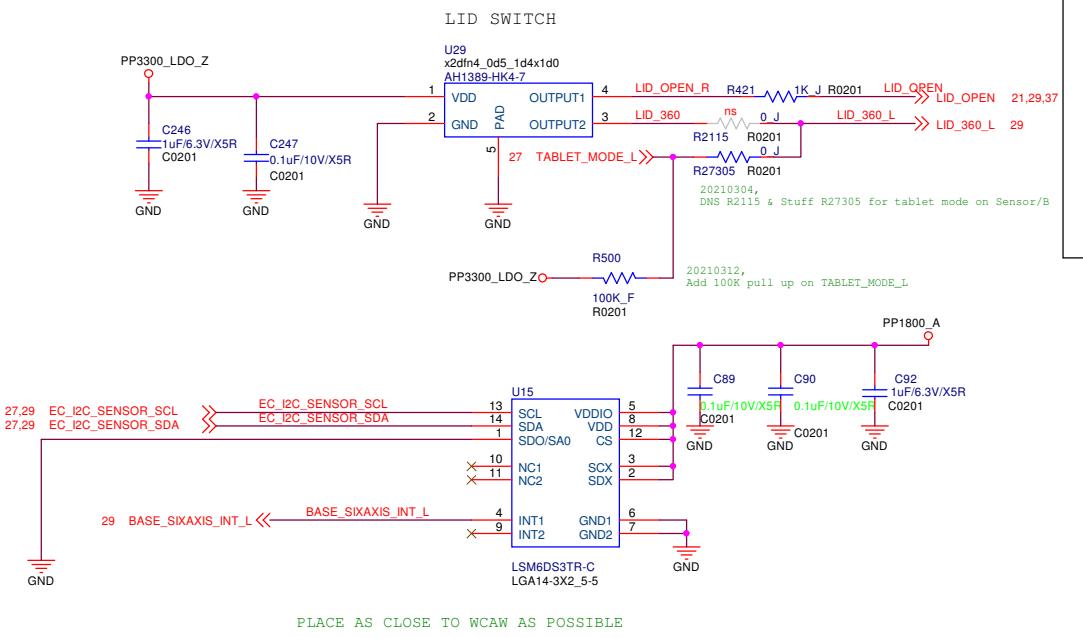
TRACKPAD



## KEYBOARD BACKLIGHT (op)



Page Name		<b>KBD,BTNS,LEDS,TRACKPAD,PEN GARAGE</b>		
<b>Size</b>	<b>A3</b>	<b>Project Name</b>	<b>Wheelie</b>	<b>Rev</b>
<b>Date:</b>	<b>Monday, March 15, 2021</b>		<b>Sheet</b>	<b>25</b> of <b>40</b>
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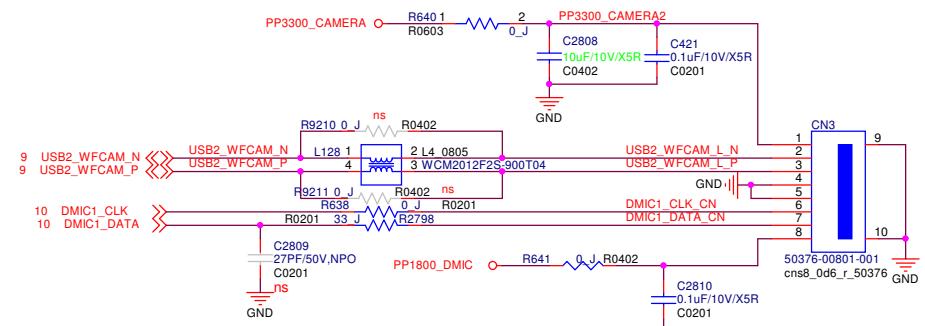
### LID-SENSOR (GMR)

Only place one sensor  
Clamshells use Single output sensor  
Convertibles use Dual output sensor

20201214A,  
Delete APX8132HAI-TRG

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WCM change mipi to USB



USER-FACTNG CAMERA

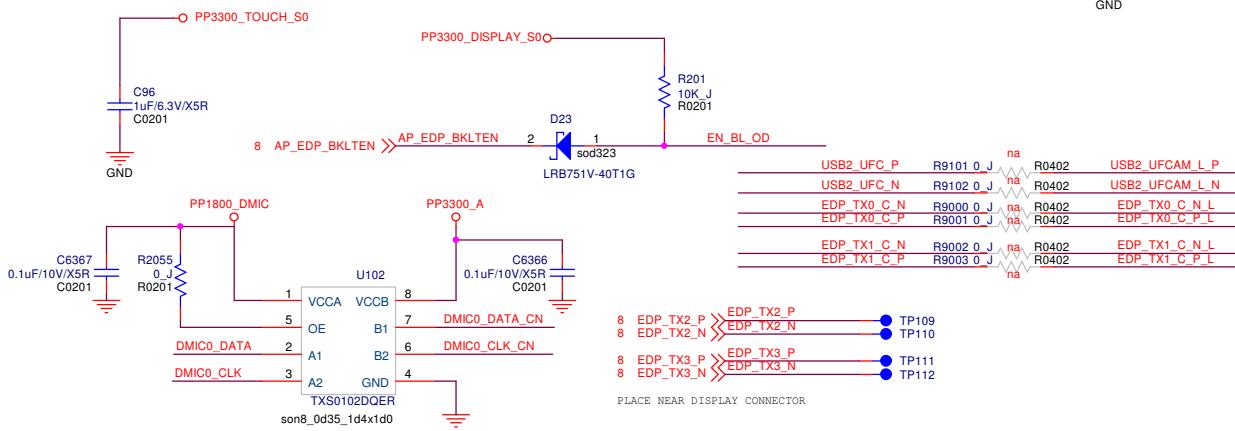
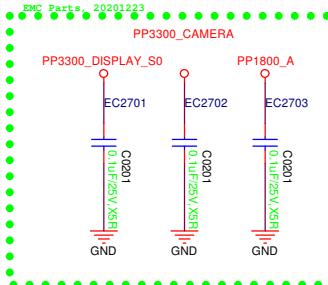
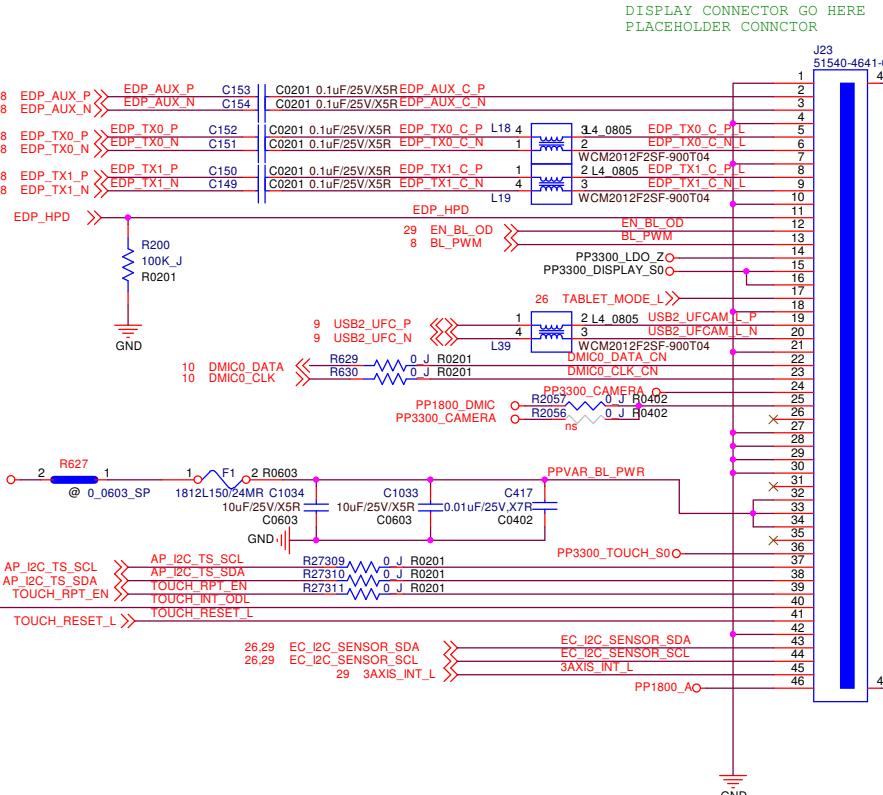
ADDRESS =0X??

PLACEHOLDER/CONNECTOR DOESN'T MATCH ANYTHING

Delete mipi for UFC.

Delete EMR

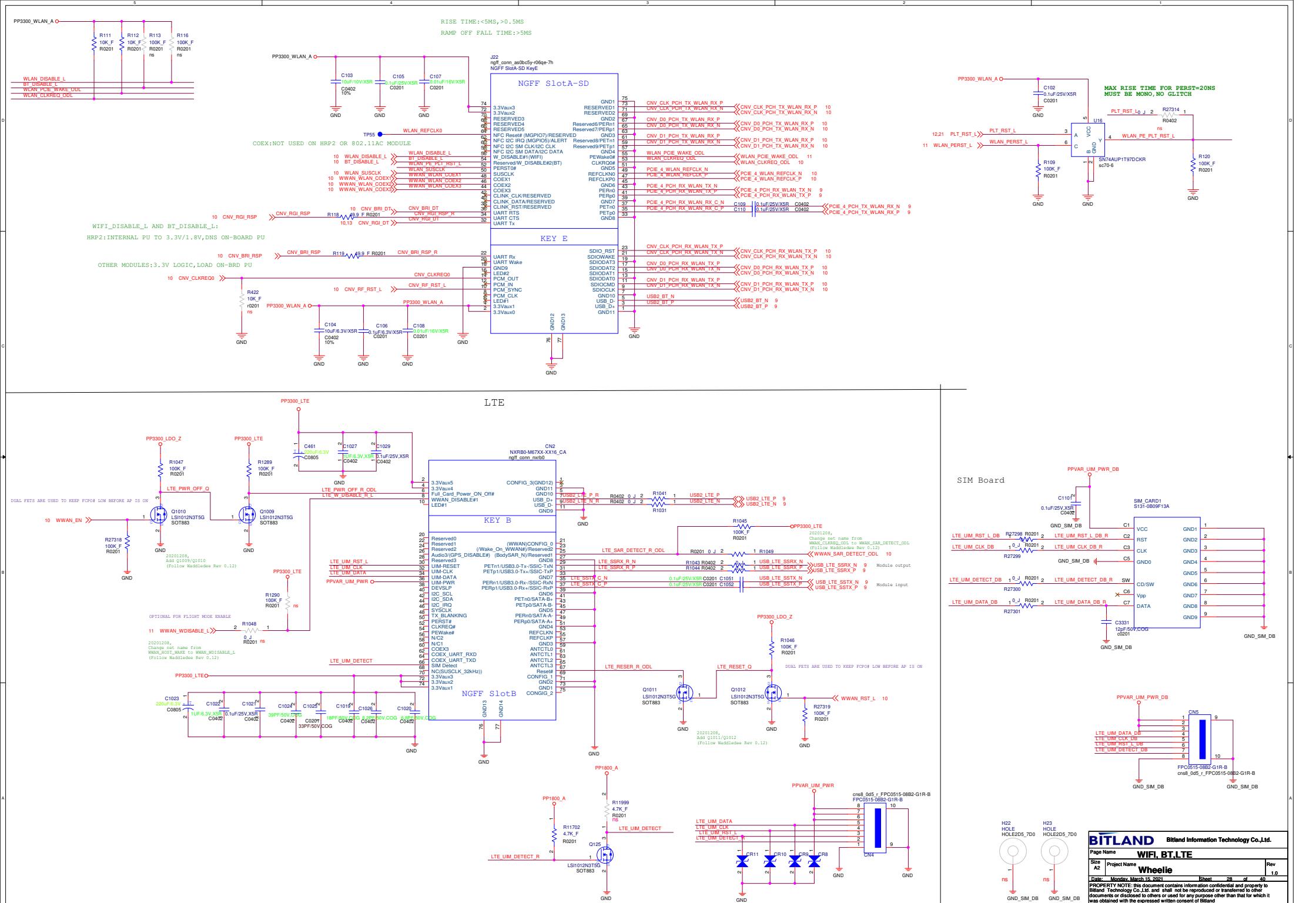
Remove Touchscreen and put it on EDP connector

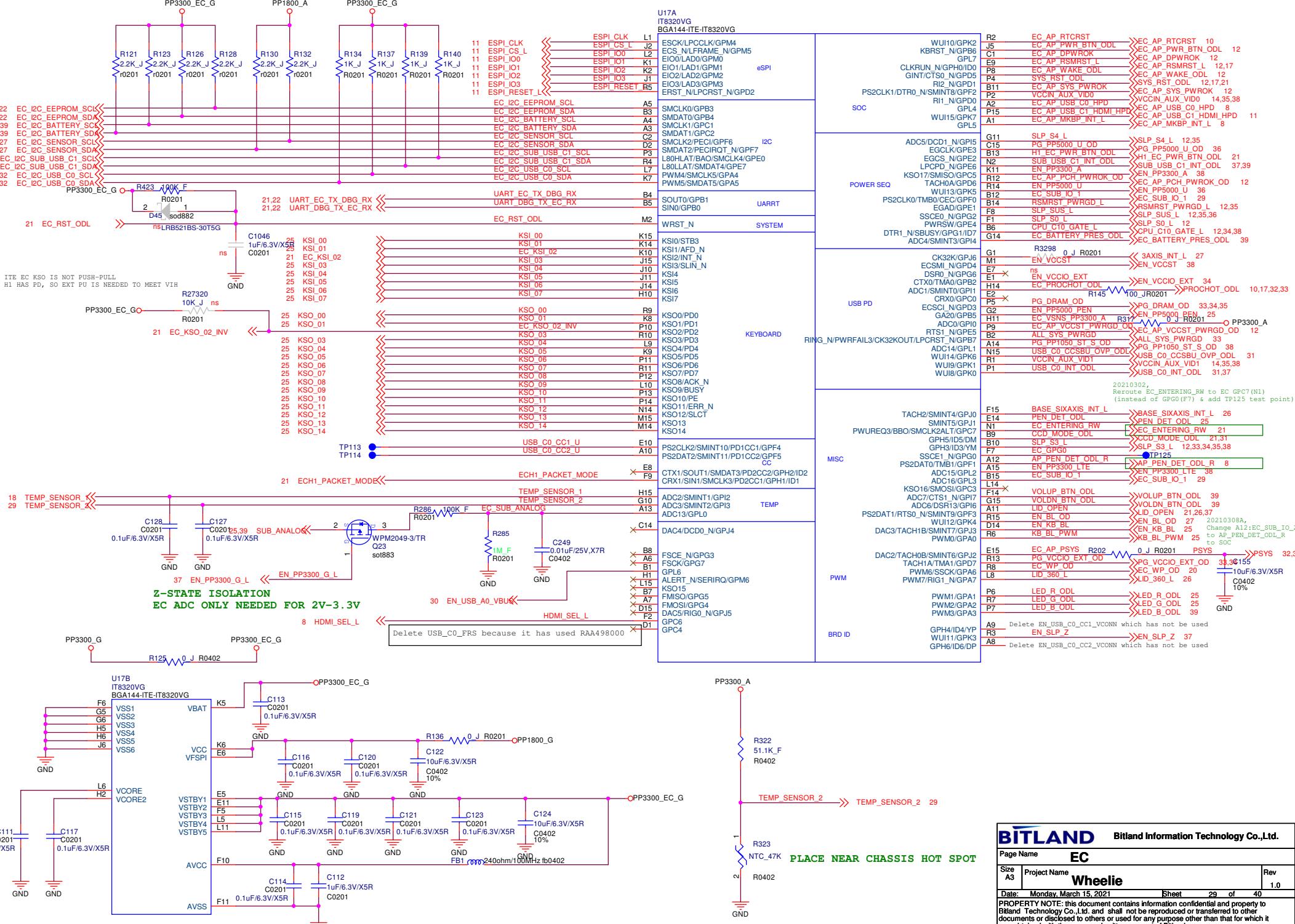


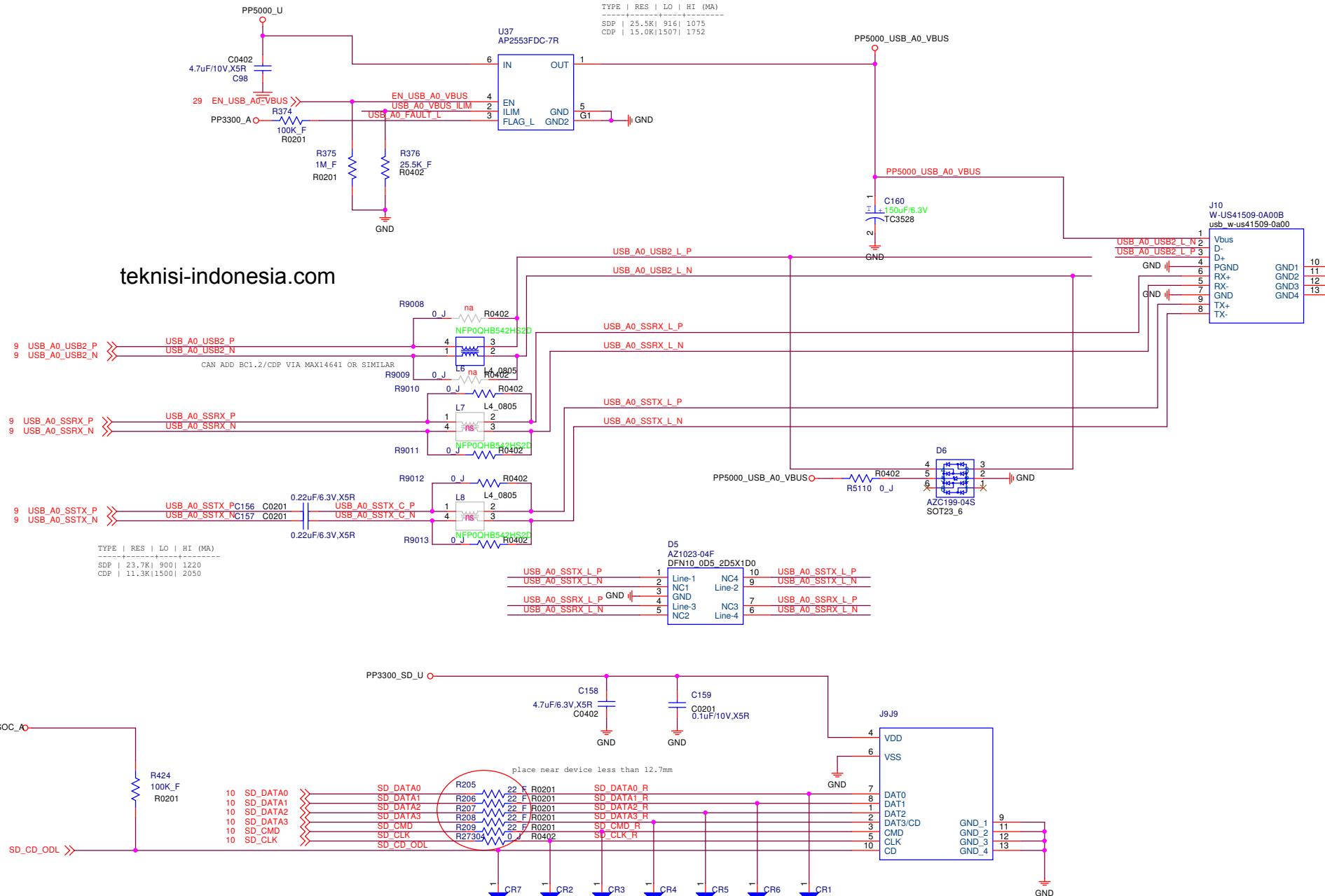
DMTC level shift for 1.8V and 3.3V

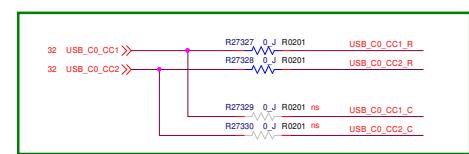
20201208,  
Swap U102 A/B net

20201209,  
Swap Pin 2/3, Pin 6/7 net

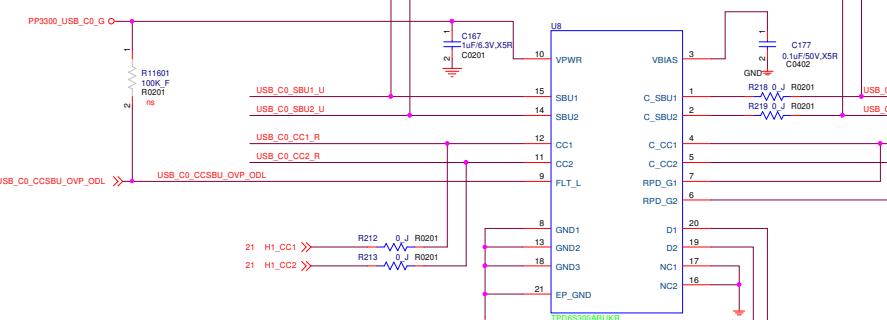






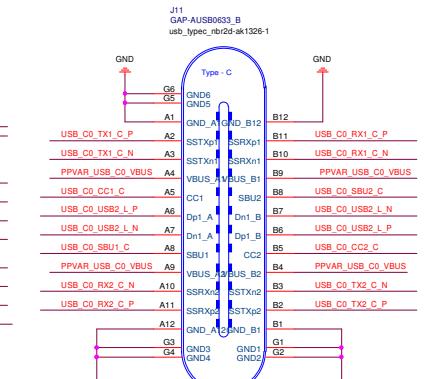
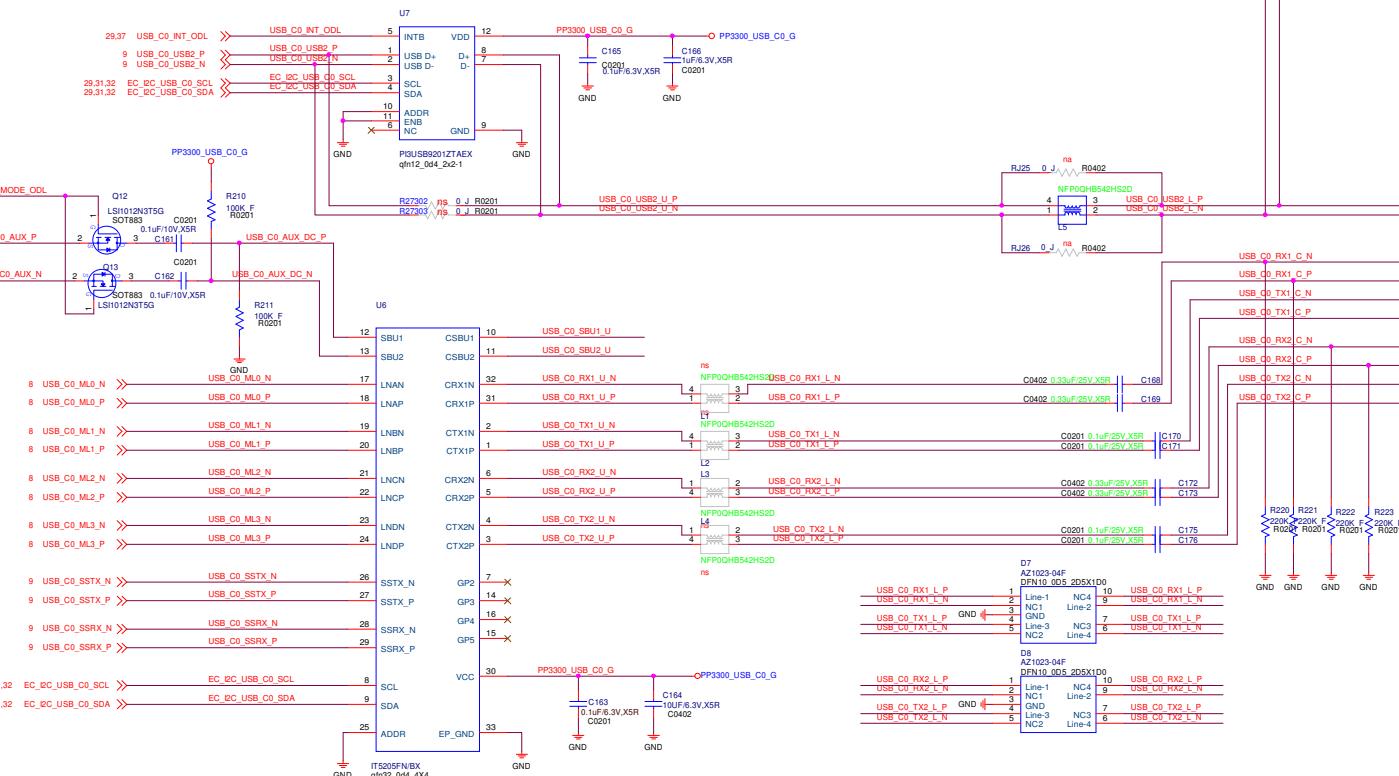


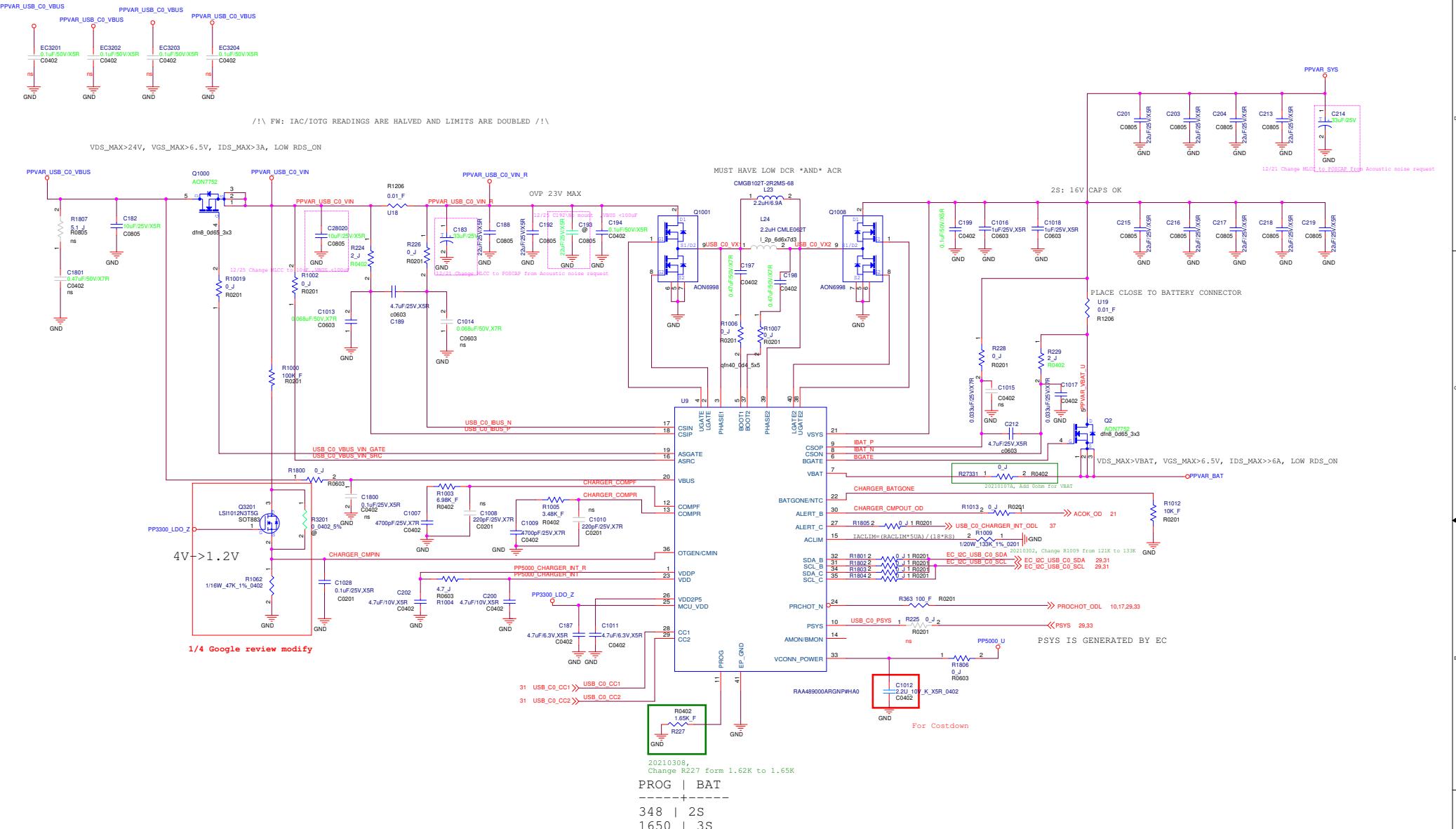
20210104,  
Add 0 ohm for CC protection

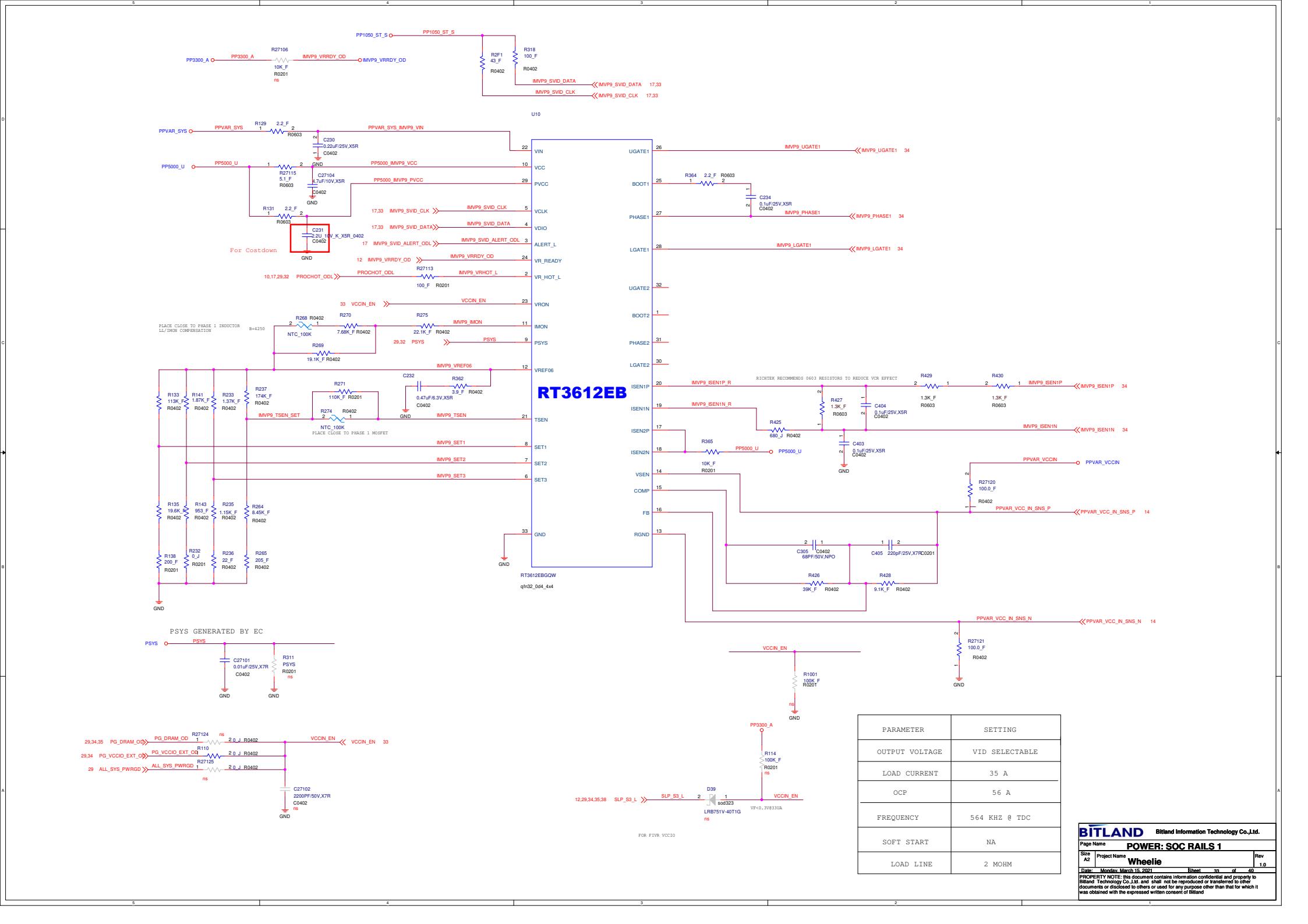


ANSWER

20210203,  
Add TVS Diode for ESD

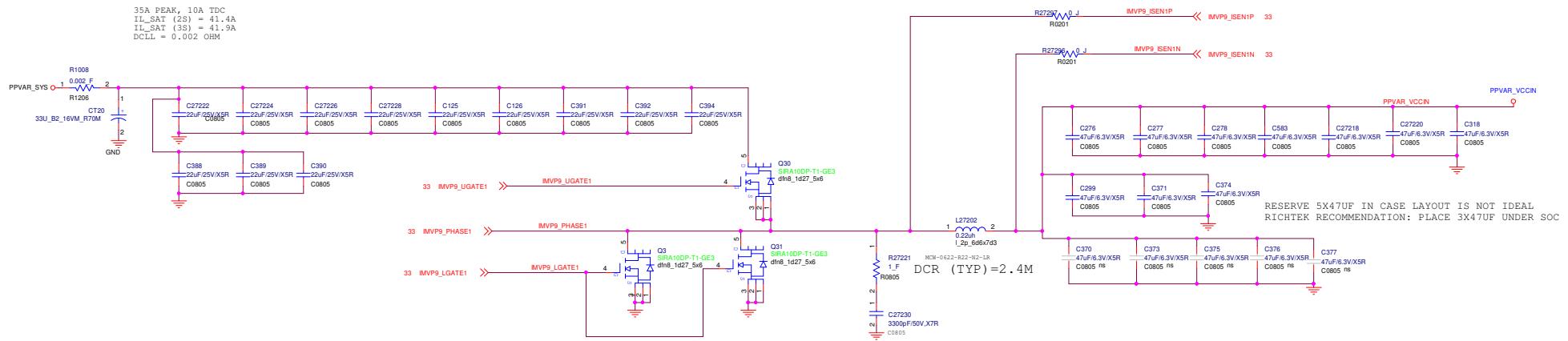




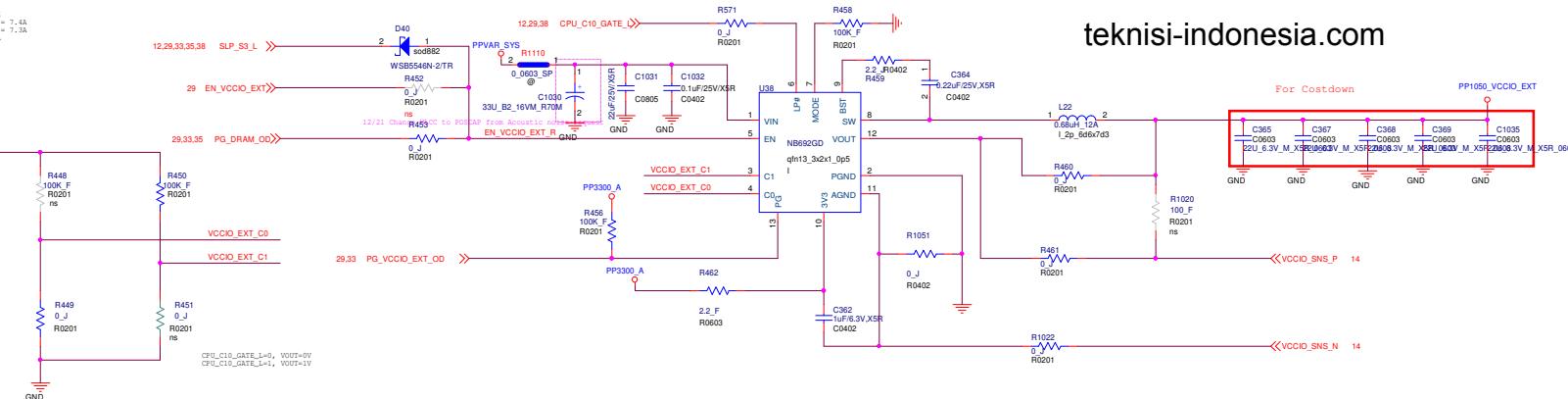


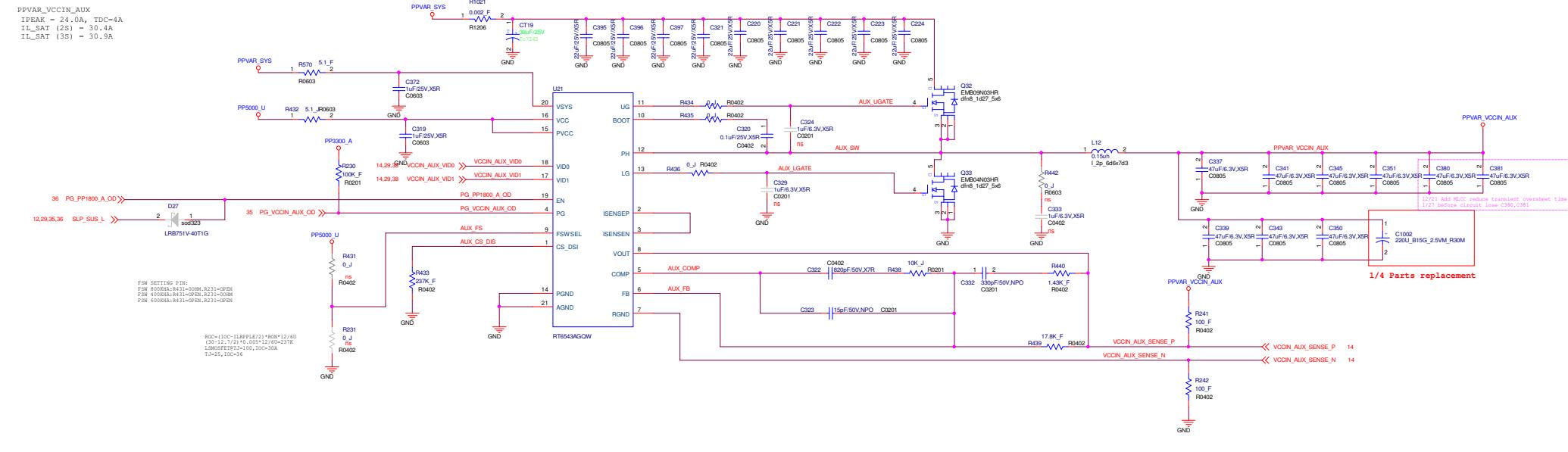
## VCCIN POWER CONVERSION

35A PEAK, 10A TDC  
 IL\_SAT (2S) = 41.4A  
 IL\_SAT (3S) = 41.9A  
 DCLL = 0.002 OHM



FSW = 650KHZ  
IL\_SAT (3S) = 7.4A  
IL\_SAT (2S) = 7.3A  
IPEAK = 6.5A  
VCCIO EXT

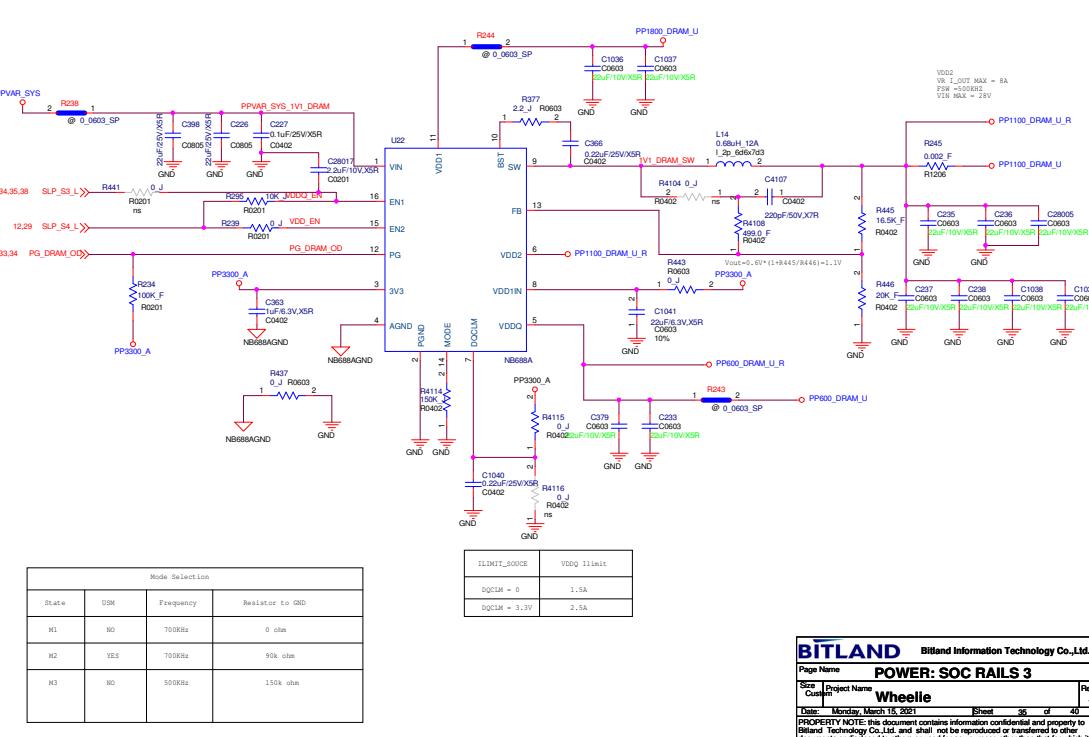
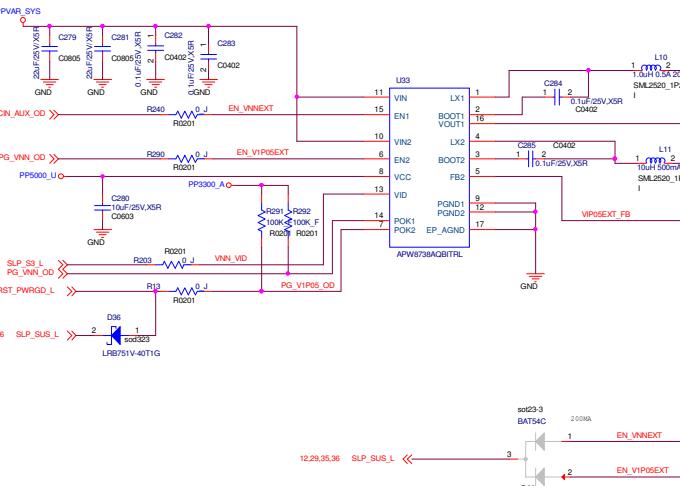




PPVAR\_VNNEXT & PP1050\_V1P05EXT BYPASS RAILS

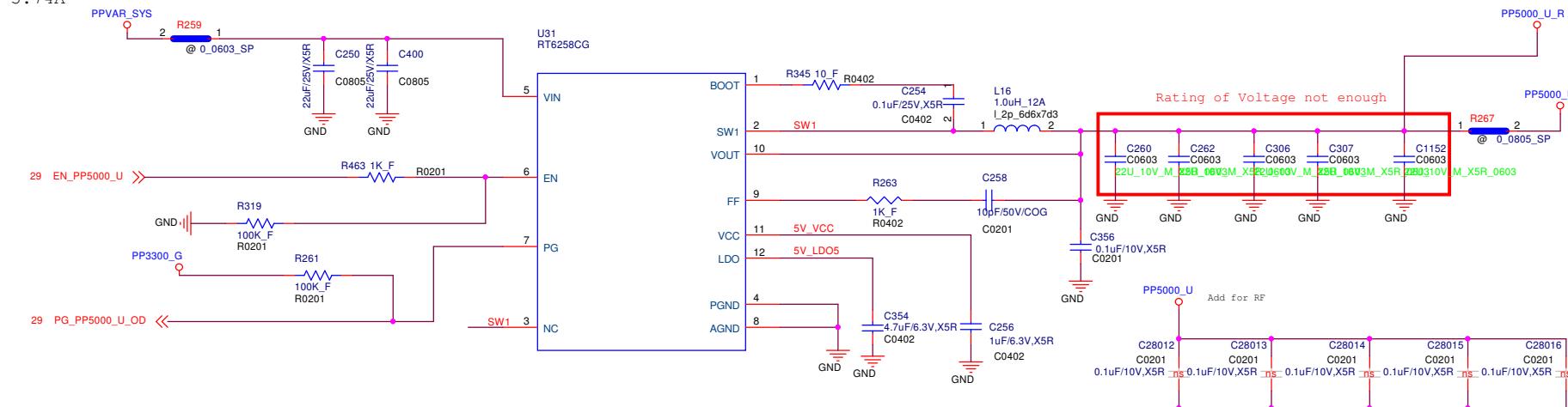
IMAX = 0.5A  
IL\_SAT (2S/3S) = 1A  
FSW = 800KHZ

IMAX = 0.5A  
IL\_SAT (2S/3S) = 0.6  
FSW = 800KHZ



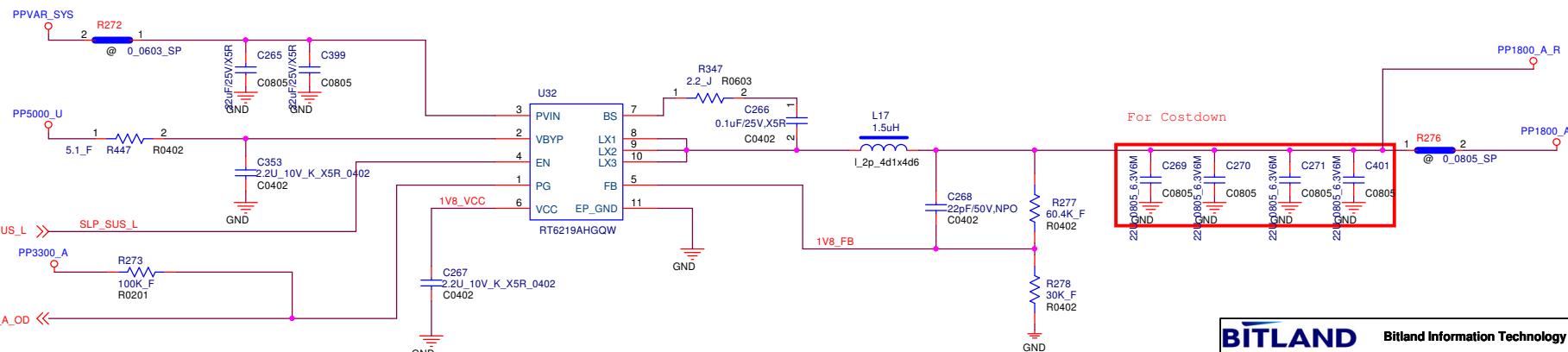
## PP5000\_U

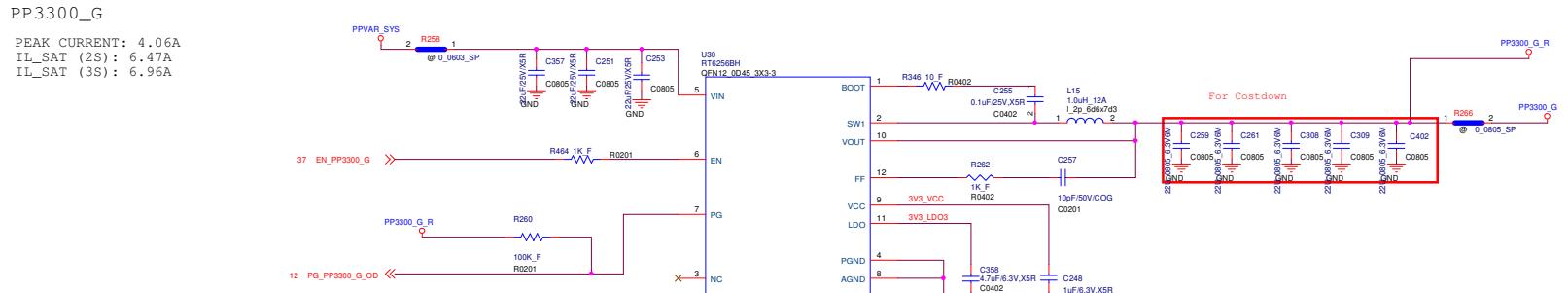
PEAK CURRENT: 3.3A  
IL\_SAT (2S): 4.99A  
IL\_SAT (3S): 5.74A



## PP1800\_A

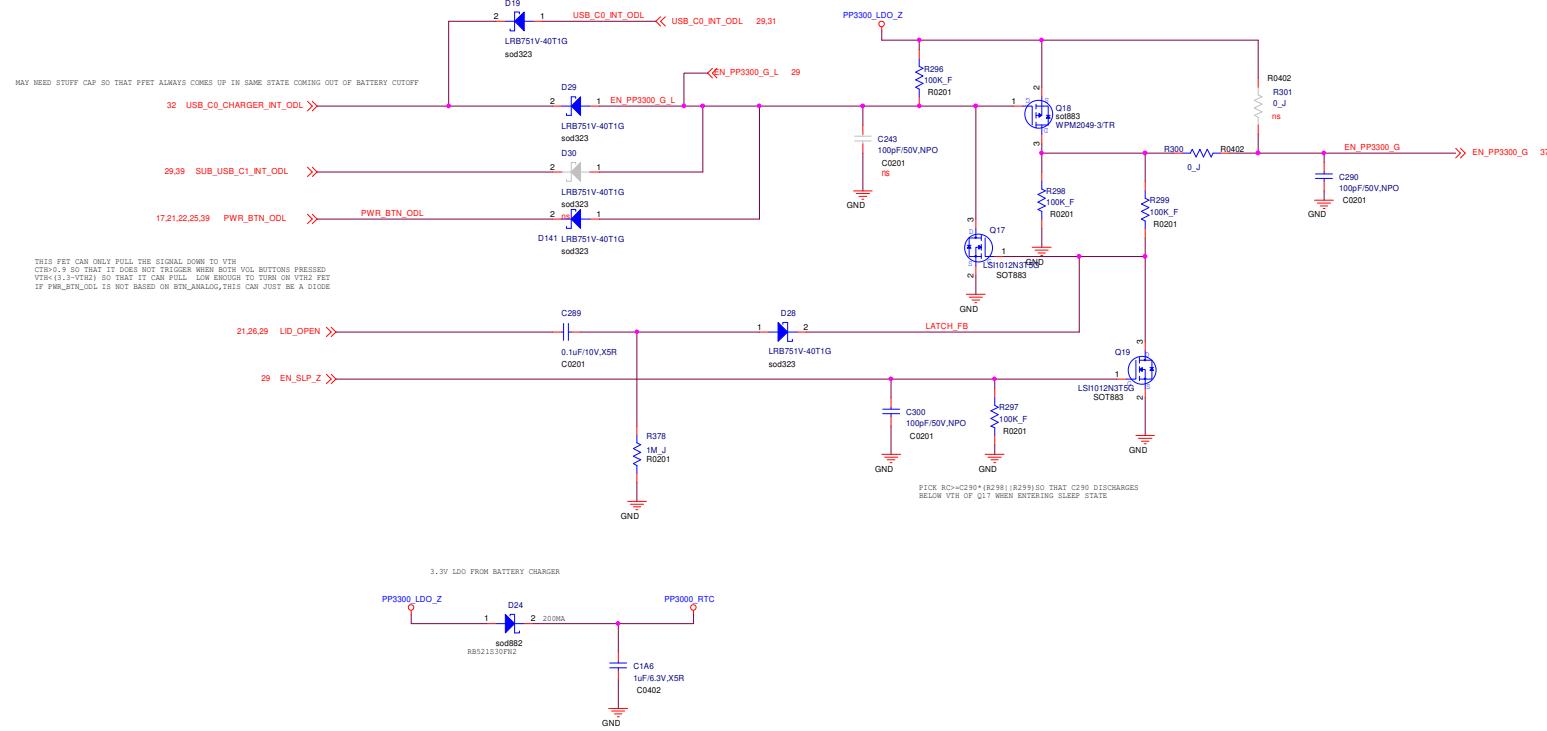
PEAK CURRENT: 2.94A  
IL\_SAT (2S): 4.06A  
IL\_SAT (3S): 4.16A

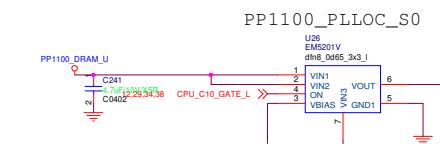
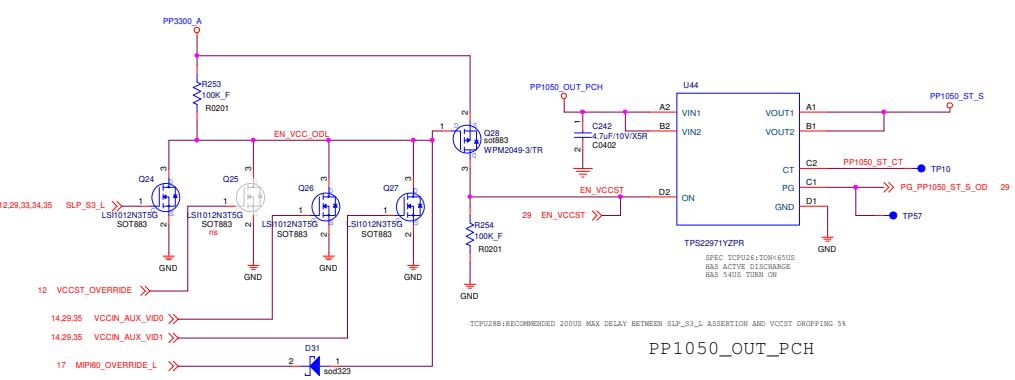
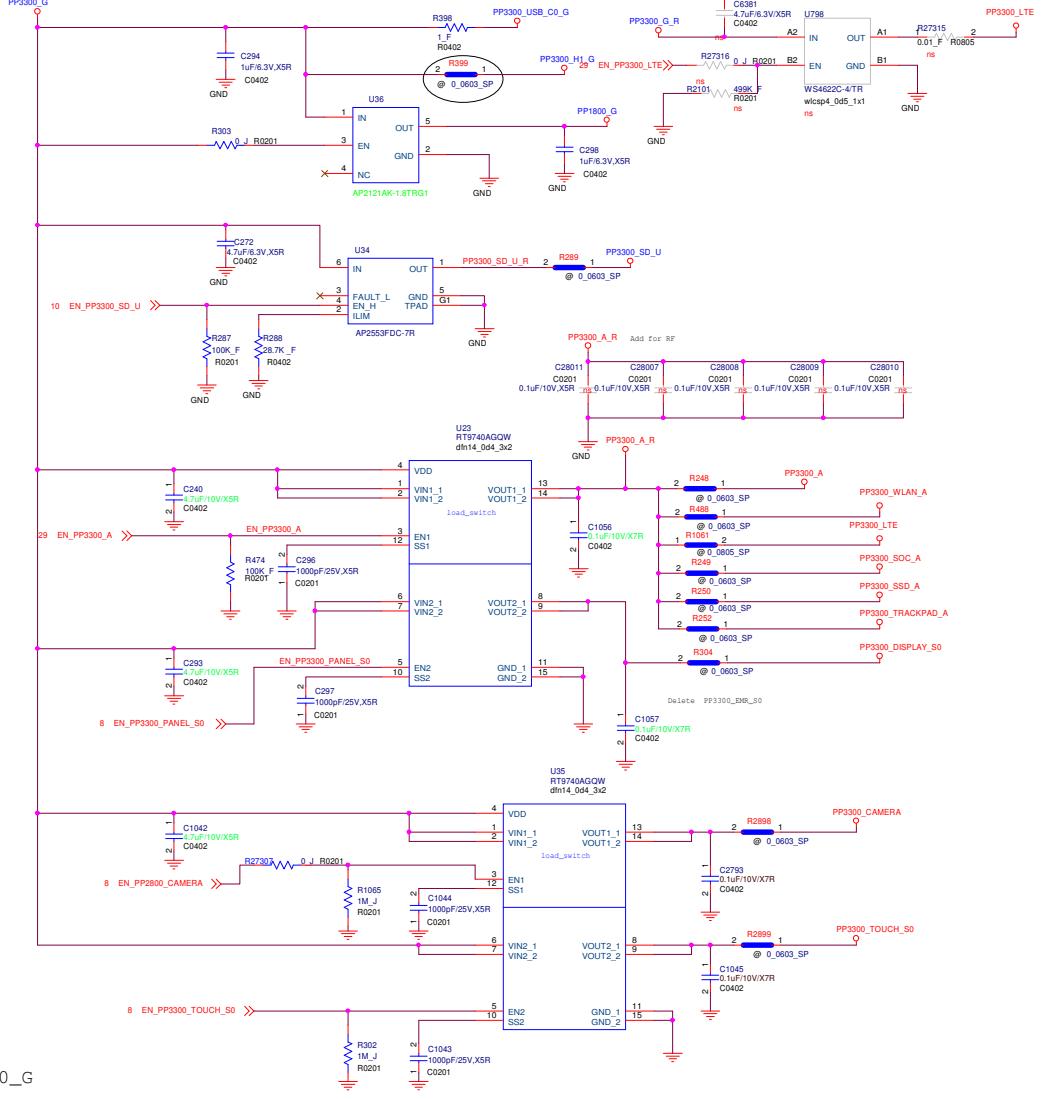




## Z SLEEP STATE LOGIC

SUPER LOW POWER STATE WITH ONLY AP RTC ON, WAKES FROM PWR BTN, LIT USB-C EVENT





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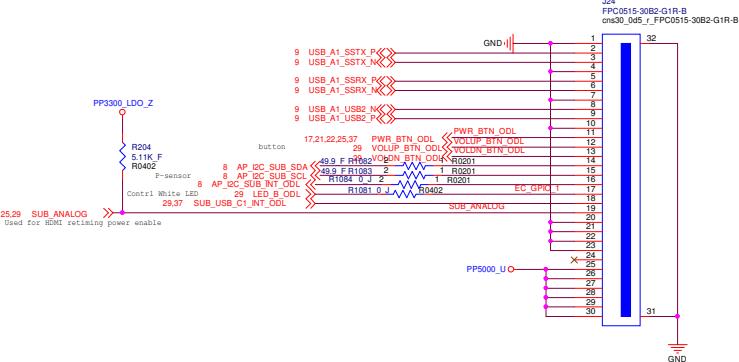
PP1800\_A

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Size A2	Project Name
Rev 1.0	Wheelle

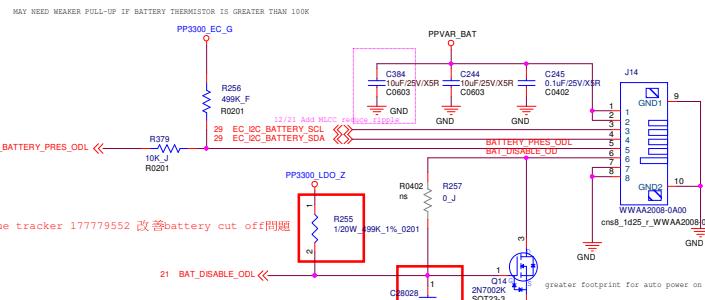
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## SUB-BOARD CONNECTOR

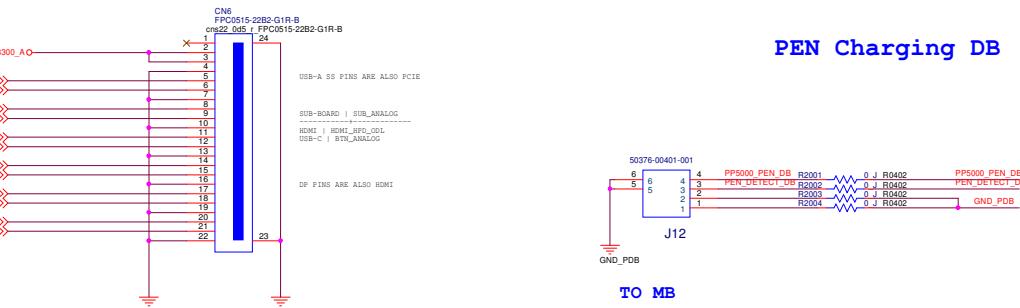
DETACH OR DISABLE BATTERY WHEN SWAPPING SUB-BOARDS



SMART BATTERY



## PEN Charging DB



G-sensor DB (Same as Treeya solution)

