

DN/DQ15 INTEL IO Board Schematics Document

2010-12-28

REV : A00

DY : Nopop Component
GIGA:Only for GIGA LAN installed
WWAN:when WWAN Installed

<Core Design>



Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Cover Page

Size
A3

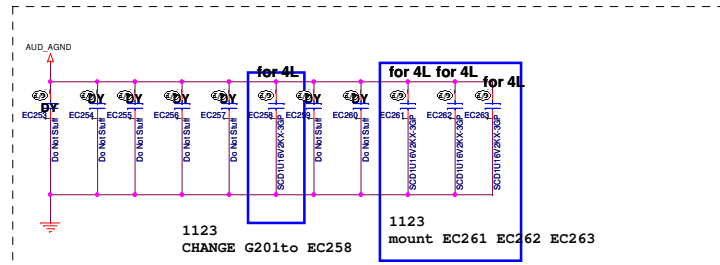
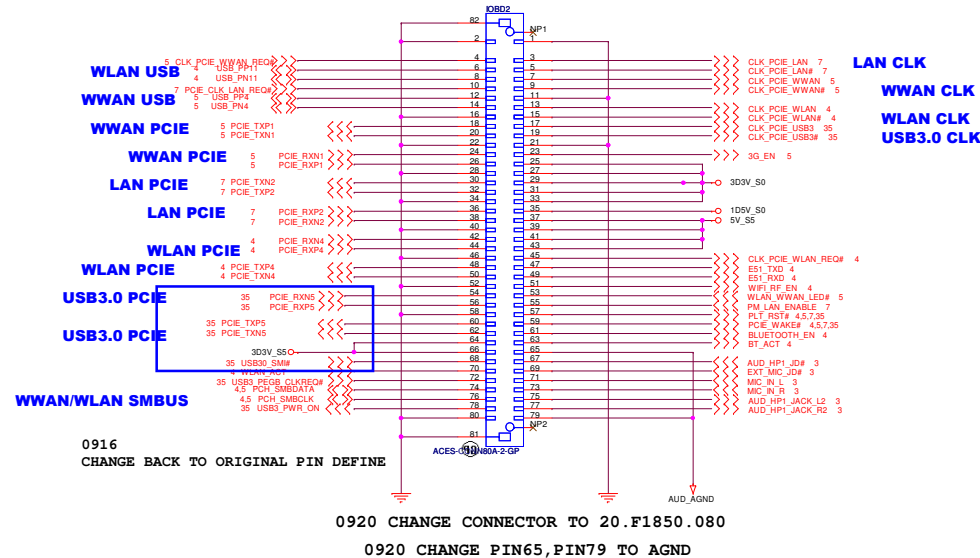
Document Number

QUEEN IO Board

Rev
X00

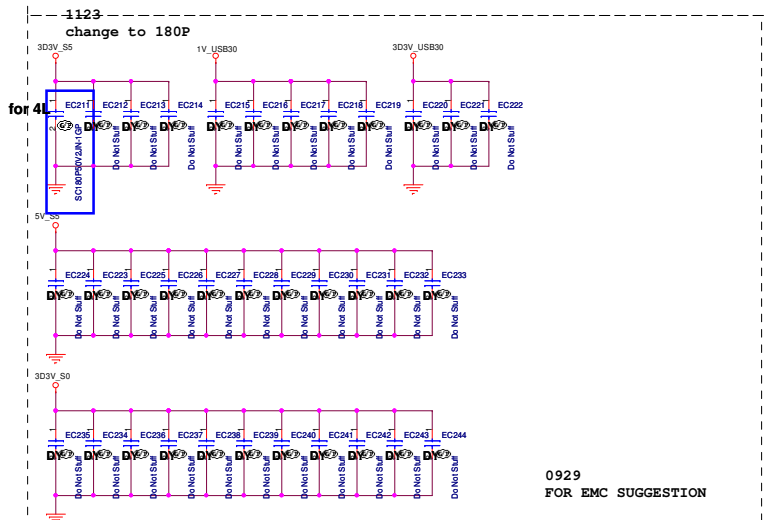
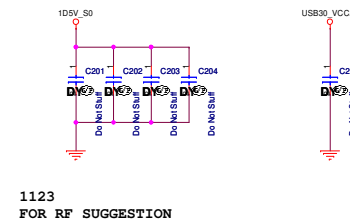
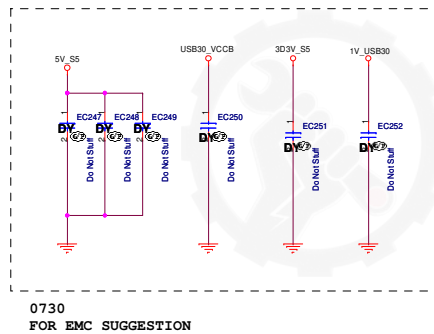
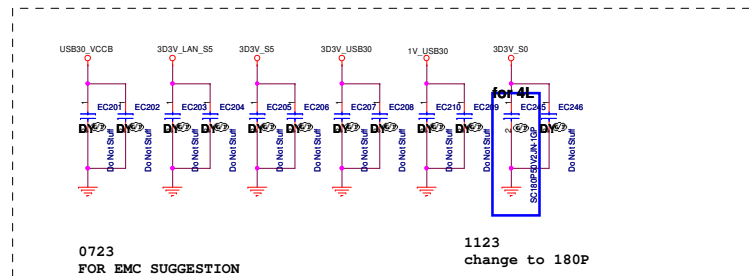
Date: Wednesday, December 29, 2010

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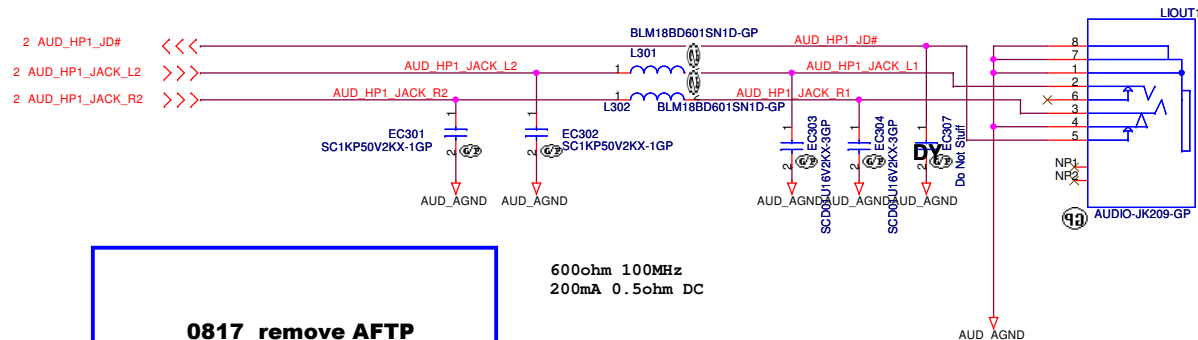
0928
FOR EMC SUGGESTION

5V_USB FOR USB3.0 POWER at least ?A
 1D05V_VTT FOR USB3.0 POWER at least ?A
 3D3V_S5 FOR LAN POWER at least over 3pin amount.
 1D5V_S0 FOR USB3.0 POWER at least ?A



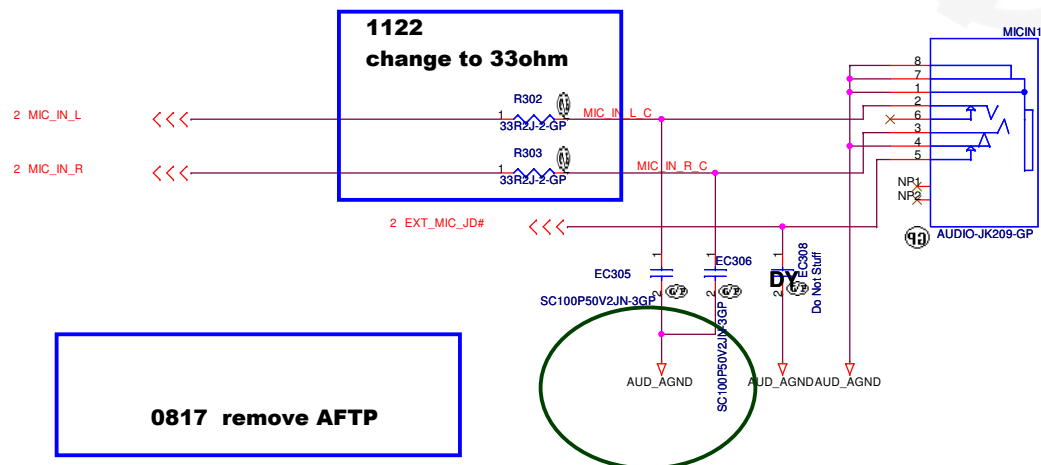
SSID = AUDIO

LINE1
OUT



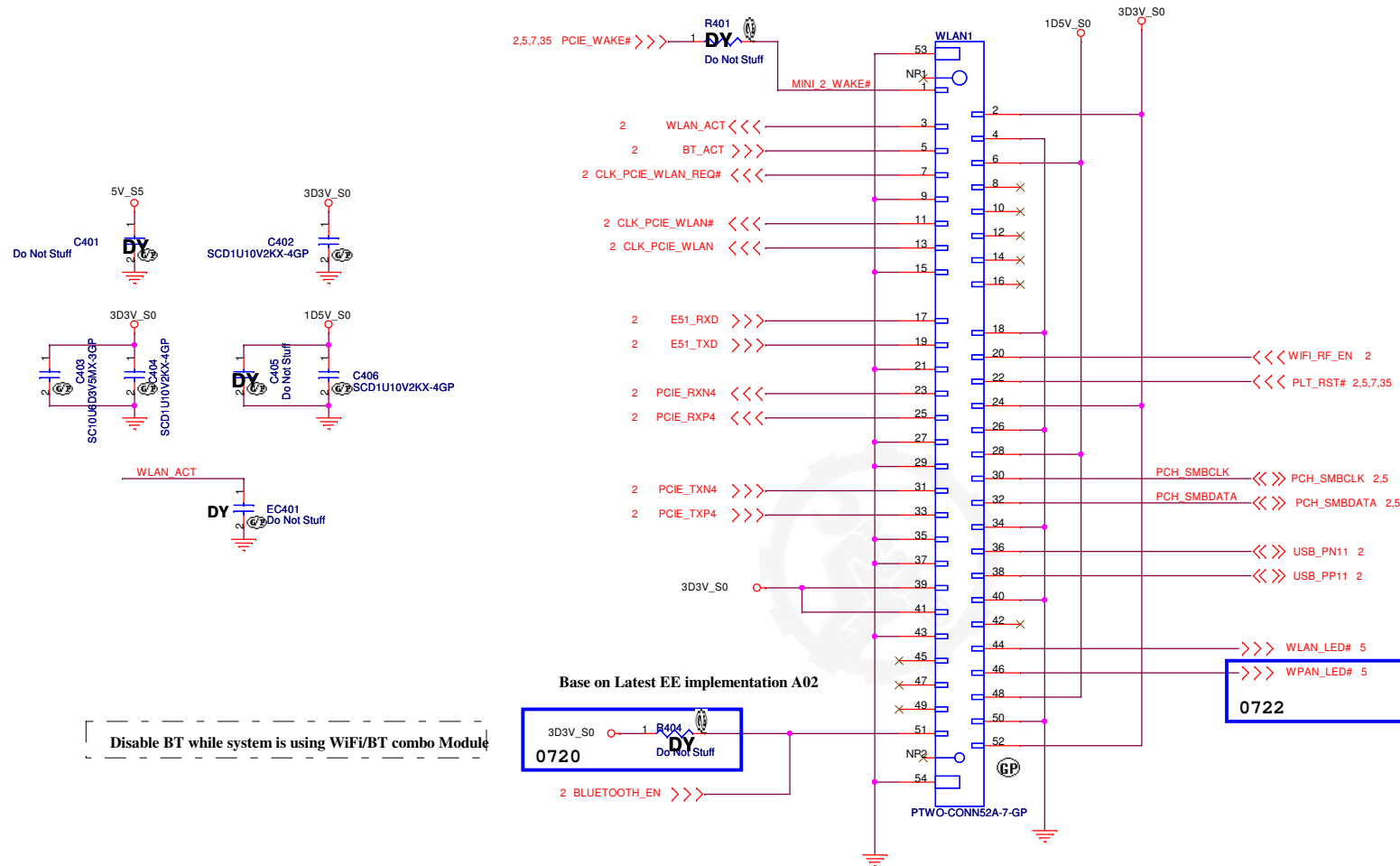
0916 change to AGND

MIC IN



SSID = Wireless

Mini Card Connector(802.11a/b/g)



<Core Design>

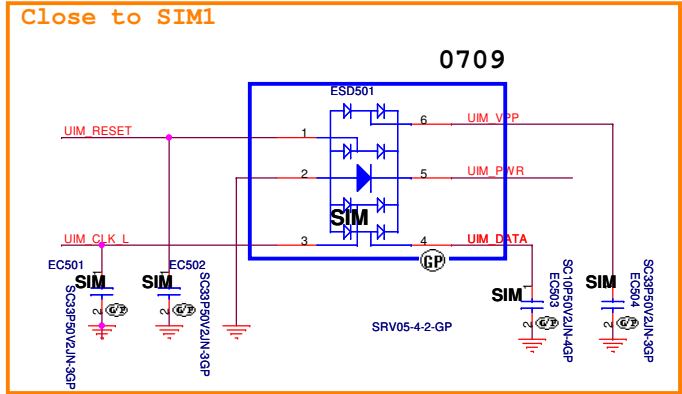


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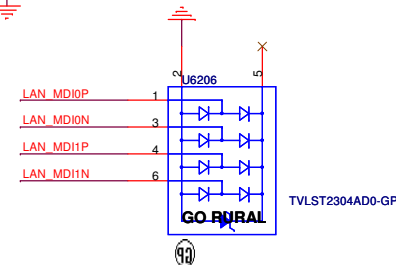
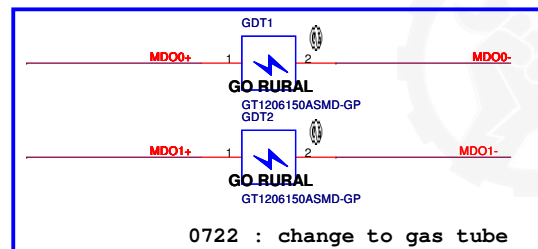
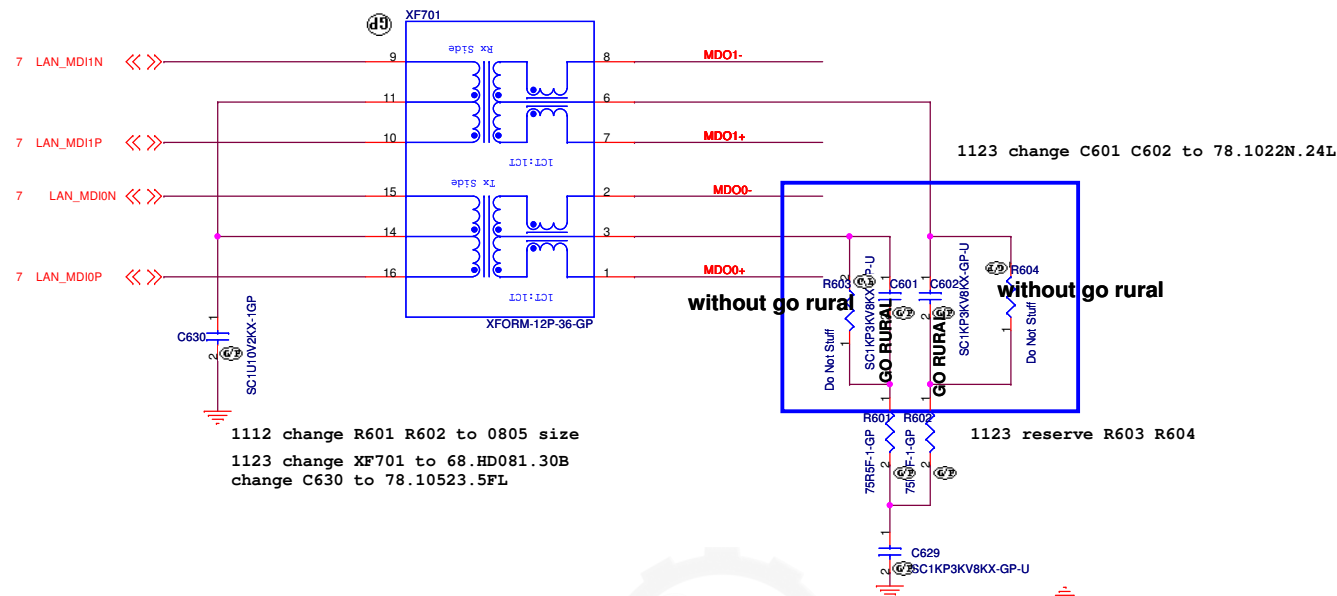
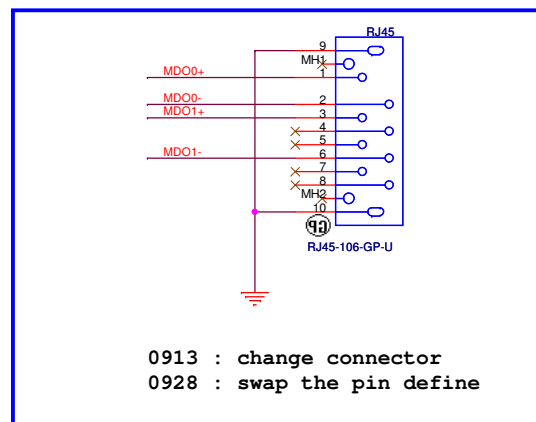
```
0927
remove common mode choke
```

MiniCard WWAN connector




VINAFIX³.COM

LAN TransFormer

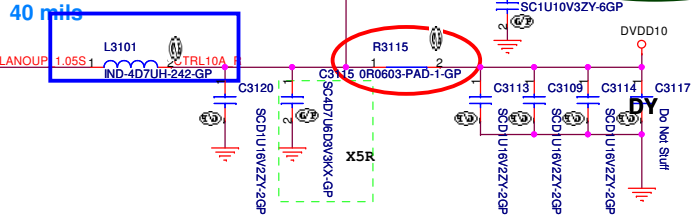
***RJ45***



<Variant Name>			
		Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Change List			
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A3	QUEEN IO Board		X00
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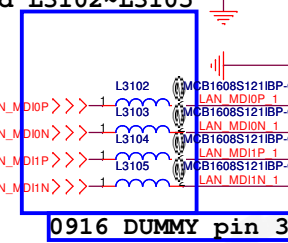
1228
CHANGE TO short pad

0927
change L3101 to 68.4R71E.10G



40 mils

1229
add L3102~L3105

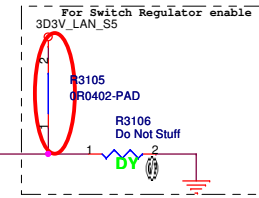


LAN CHIP

0916 DUMMY PIN42

0906
CHANGE TO 84.2N702.J31

1228
CHANGE TO short pad



0720

0916
DUMMY C3128

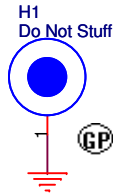
1117
change Xtal to 82.30020.D41

<Variant Name>

DELL Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

File
LAN Chip
Size A3 Document Number
QUEEN IO Board
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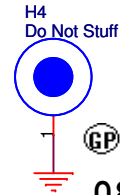
footprint change to
HOLE268X957R126X205-S



0721

REMOVE H2

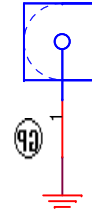
0813



0813

change P/N

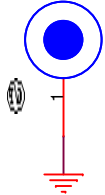
H5
Do Not Stuff



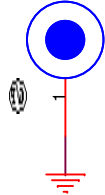
0813

change P/N

HIO1
STF237R117H284-GP



HIO2
STF237R117H284-GP



0813

change P/N

HWW1
STF237R148H67-GP



HWL1
STF256R117H119-GP



SPR1
SPRING-67-GP



SPR2
SPRING-68-GP



1122

mount SPR1&SPR2 (34.4AK01.001)

1224

change SPR2 to 34.4AK04.001

io



Wistron Corporation

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Taipei Hsien 221, Taiwan, R.O.C.

Title

RESERVED

Size

A4

Document Number

DN/DQ15 IO Board

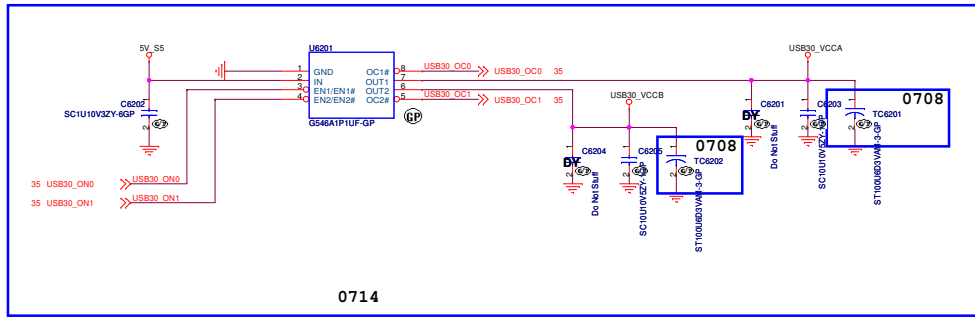
Rev

A00

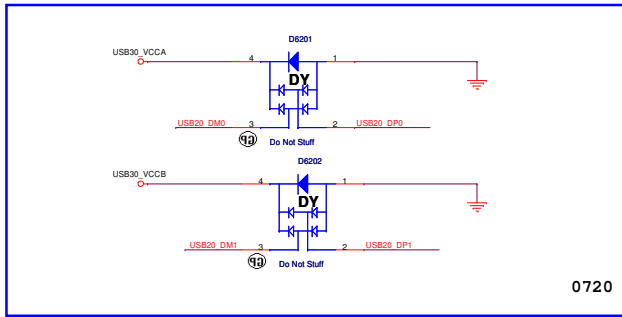
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Left Side USB x 2 Power Switch



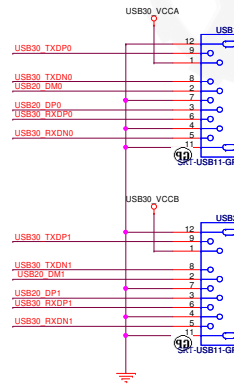
0714



0720



USB3.0 CONNECTOR



0817 remove AFTP

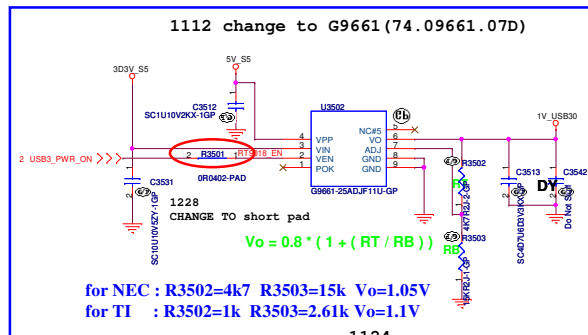
USB 3.0 Connector Pin definition

Pin	Definition
1	POWER
2	USB 2.0 D-
3	USB 2.0 D+
4	GND
5	StdA_SSRX-
6	StdA_SSRX+
7	GND
8	StdA_SSTX-
9	StdA_SSTX+

<Variant Name>

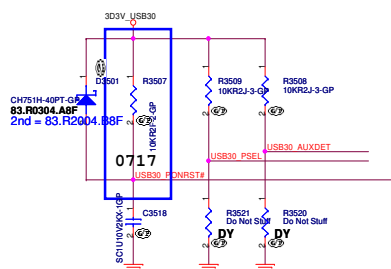
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsuehshui, Taipei Hsien 221, Taiwan, R.O.C.

File: USB 3.0 Port
Size: A2
Document Number: QUEEN IO Board
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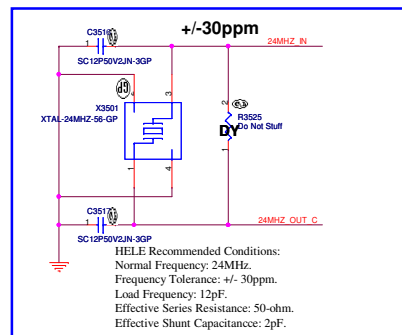


1125
change R3502 to 15K
change R3503 to 4.7K
stuff C3512

1124
change C3513 to 4.7u reserve C3542
BOM OPTION:
24MHZ XTAL: FOR NEC
48MHZ XTAL: FOR TI



1112 reserve Q3504 and R3515



1117
change Xtal to 82.30004.641

0914
change Xtal to 82.30023.821
change C3516 C3517 to 12p

