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LINE	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE		

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SHEET BLOCK	SCHEMATIC	TITLE	SHEET BLOCK	SCHEMATIC	TITLE	SHEET BLOCK	SCHEMATIC	TITLE
1			51	Reserved_6				
2	TABLE_OF_CONTENTS		52	Reserved_7				
3	GPIO		53	CPA800				
4	BLOCK_DIAGRAM		54	DRX_QDM2302				
5	SM6150 CONTROL		55	DRX_Reserved_1				
6	SM6150 GPIO		56	DRX_Reserved_2				
7	SM6150_EBIOPIO		57	DRX_Reserved_3				
8	SM6150_EBIOP1		58	DRX_Reserved_4				
9	SM6150_MIPI-CSI/DSI		59	QAT3522				
10	SM6150_WCSS_RF		60	QAT3518_DRX_ANT1				
11	SM6150_PWR_1		61	WCN3950_SH				
12	SM6150_PWR_2		62	WLAN_BT2101				
13	SM6150_PWR_3		63	WCS_NFC_SN100U				
14	SM6150_DECAPS		64	SHIELDING_FRAME				
15	SM6125_GND		65	CHANGE_LIST				
16	Reserve_1							
17	PM6125_HKIO							
18	PM6125_Bucks_S1-S4							
19	PM6125_Bucks_S5-S8							
20	PM6125_LDOs							
21	DECAPS							
22	PM6162_Control							
23	PM6162_Charger/PD_PHY							
24	PM6162_LCD_VIB/RGB/Flash							
25	SMB1355							
26	eMCPI/UMCP							
27	eMCPOWER							
28	WCD9370							
29	AUDIO							
30	DISPLAY							
31	FRONT CAMERA							
32	REAR CAMERA							
33	CAM_eLDO							
34	Sensors							
35	PM6162_CAM_LDO							
36	SIM/SD							
37	KEYPAD/INDICATOR							
38	BATT/B2B							
39	Audio_Type C							
40	RCMDEBUG							
41	SDR480_POWER							
42	SDR480_GND							
43	SDR480_PRX_DRX_FBRX							
44	SDP480_PRX_FBRX							
45	QET4101							
46	Reserved_4							
47	Reserved_5							
48	PRx_RB							
49	PRx_MB							
50	PRx_LB							

TABLE_OF_CONTENTS



8	7	6	5	4	3	2	1

SM6125 CONTROL

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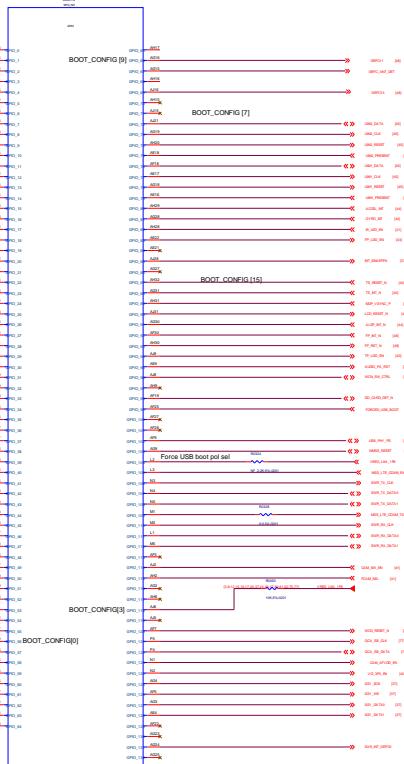
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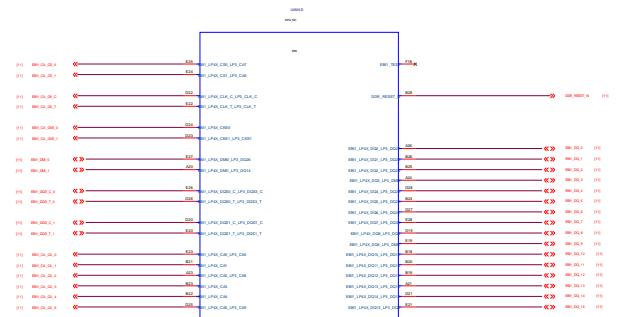
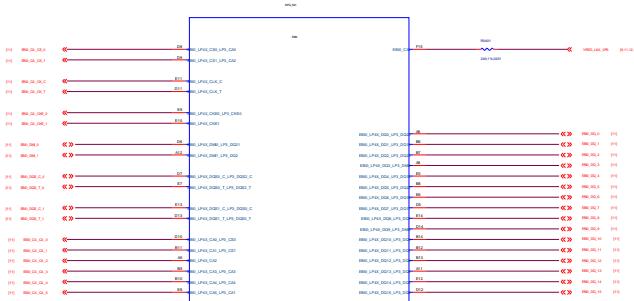
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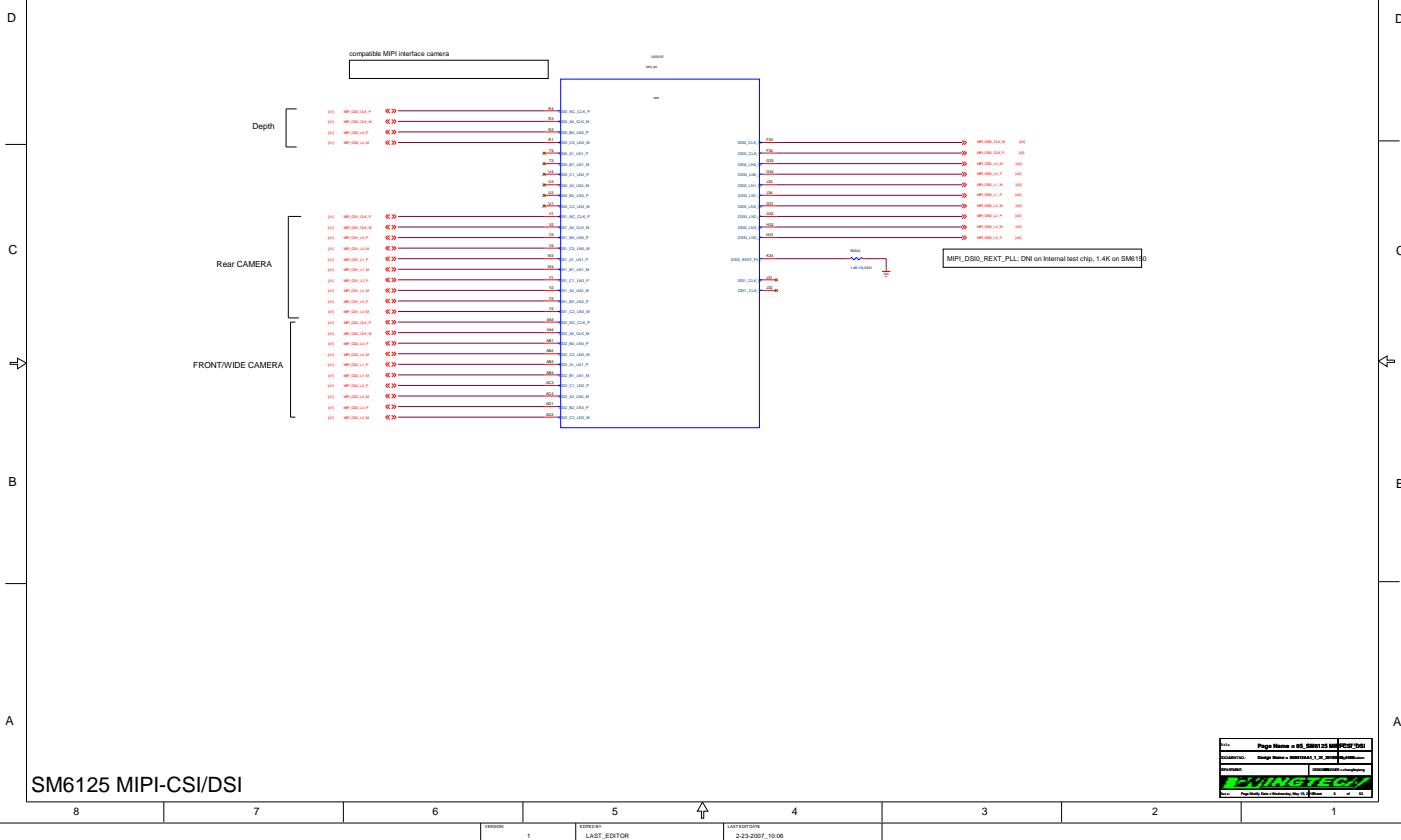


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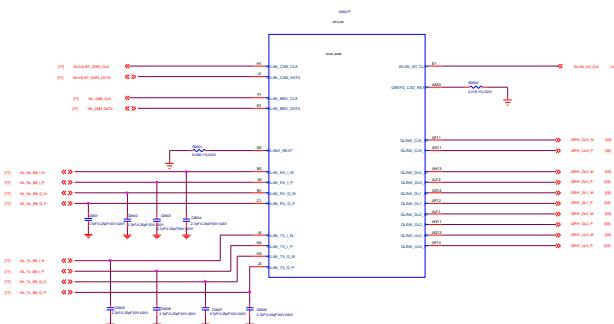
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Add C1001-C1008 caps place holder for CLK desense to improve sensitivity.

SM6125 WCSS/RF



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VERSION 1.0 LAST_EDITOR

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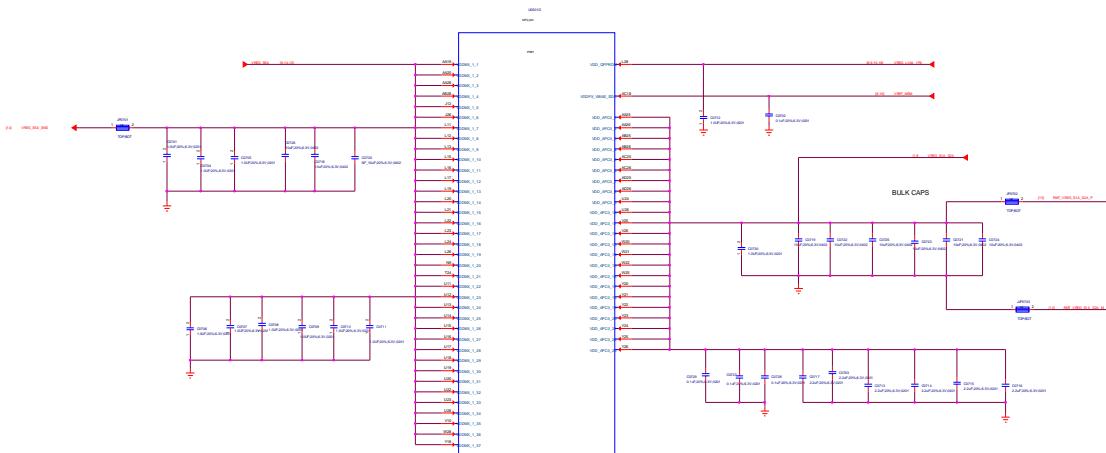
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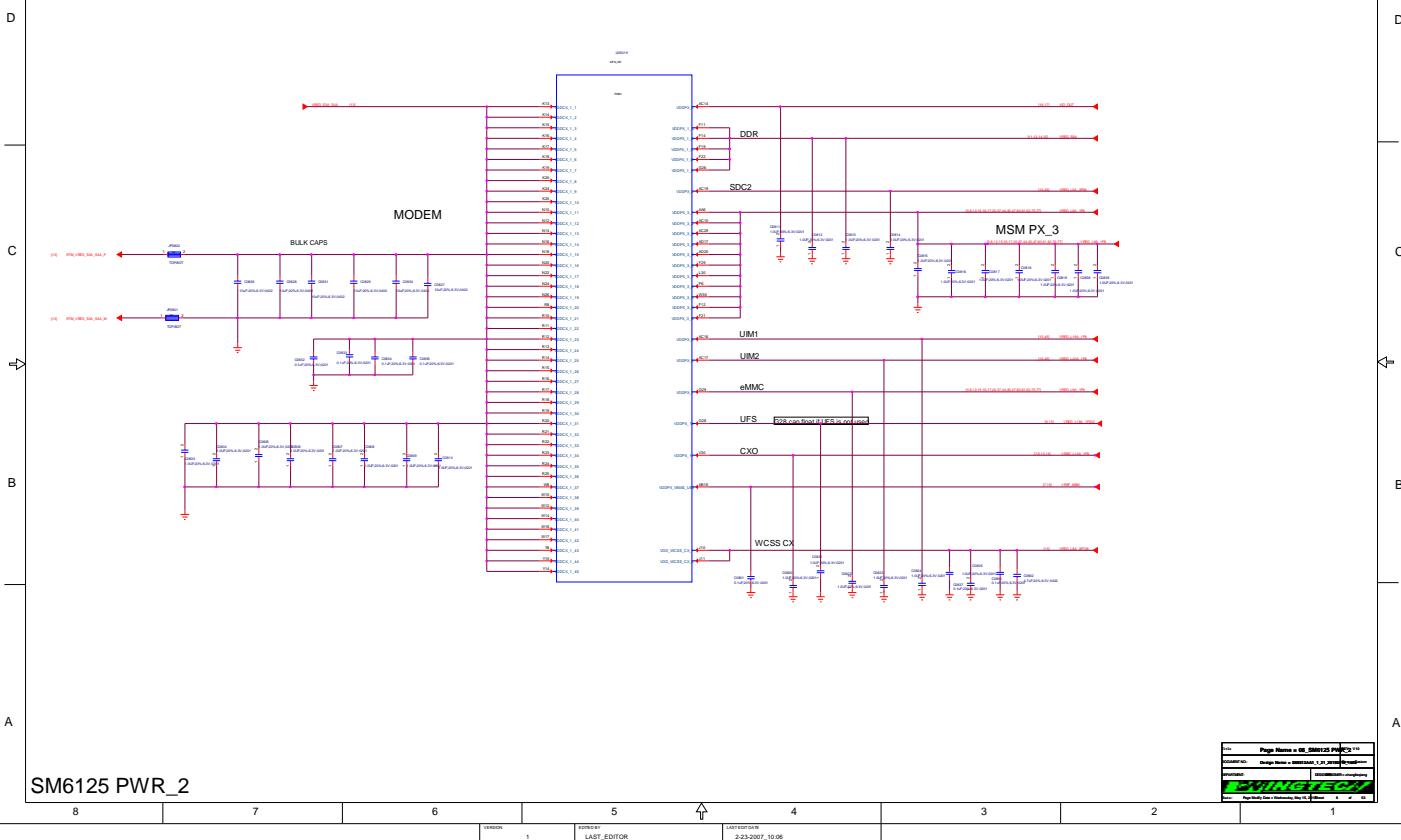


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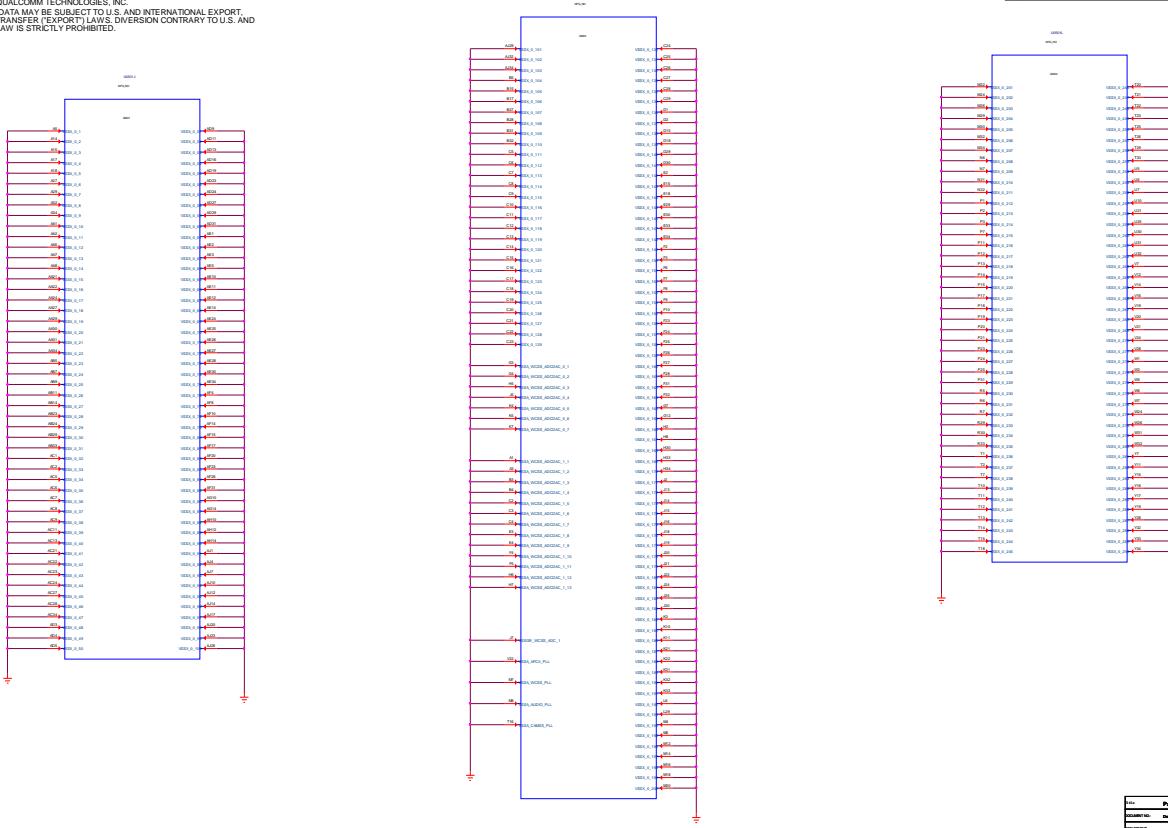
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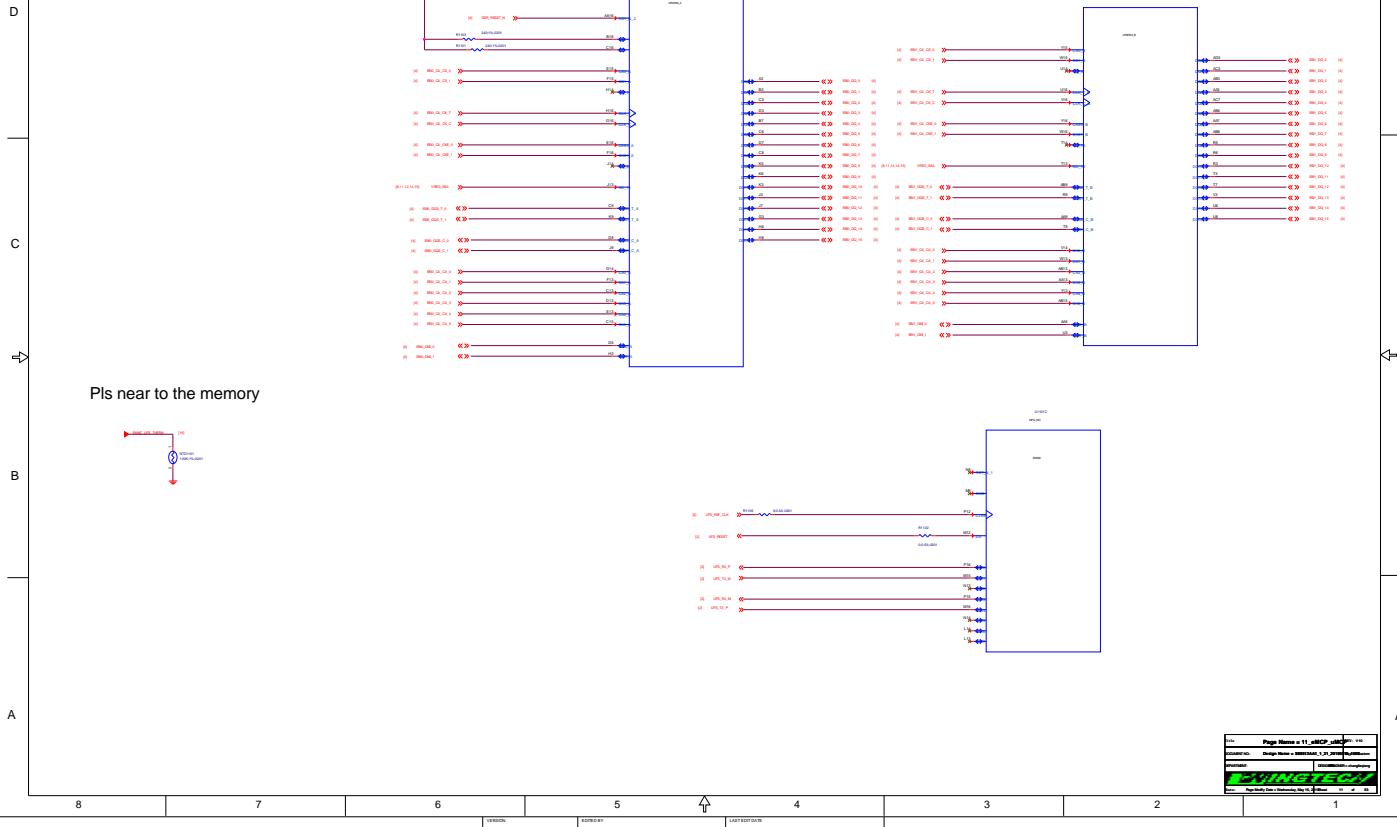
SM6125 GND

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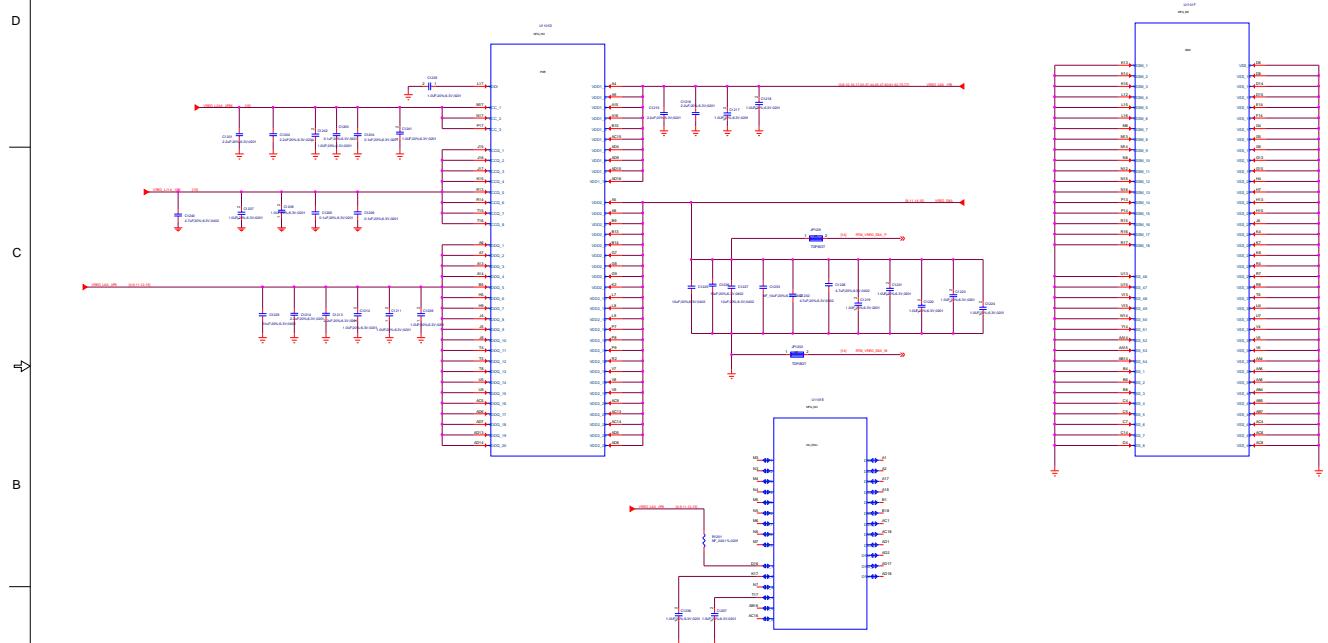
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eMCP and UFS compatible design



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eMCP_POWER



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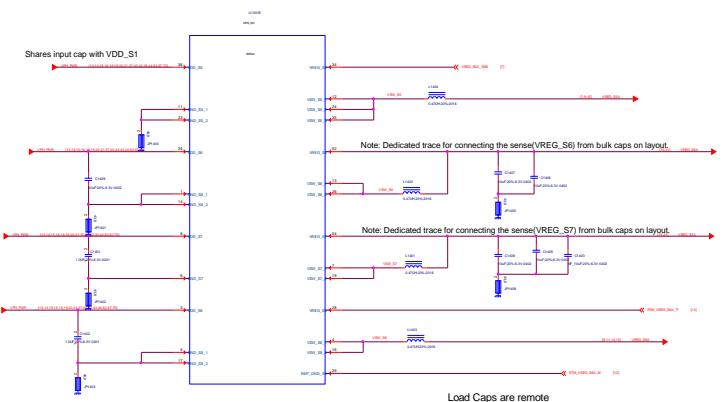
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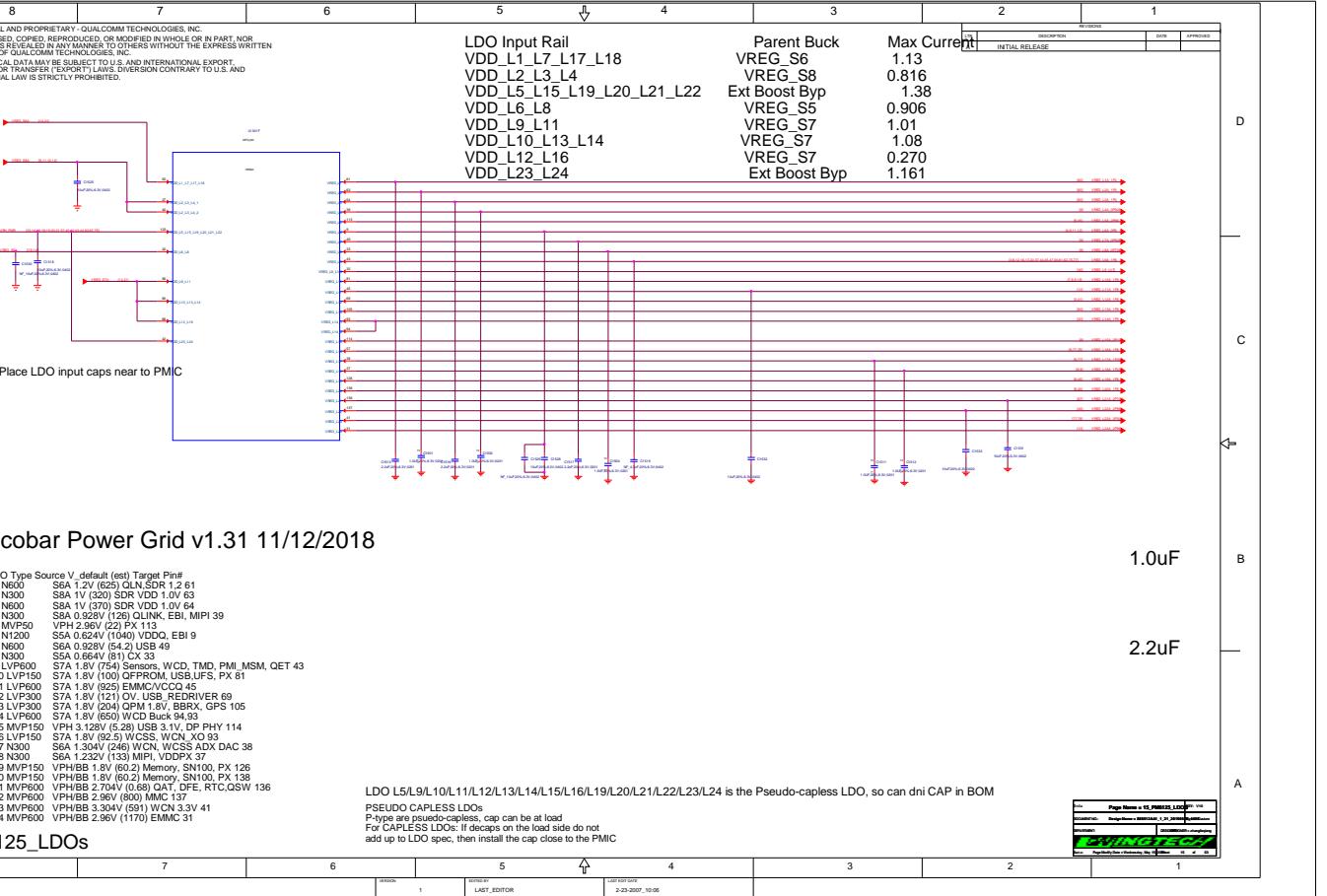
Place Cin as close to PMIC as possible to achieve this requirement.
Loop inductance from (VIN_Sx to Cin + Cin to GND_Sx) must be < 3nH

Route diff pair out from Cap back to appropriate pins at PMIC

VREG_S8A is LOCAL diff sense.
VREG_Sx & RMT_GND_Sx should be routed as a differential pair
Do not route VREG_Sx & RMT_GND_Sx near VIN_Sx
Do not route VREG_Sx & RMT_GND_Sx near VSW_Sx unless separated by ground shield

PM6125_Bucks_S5-S8



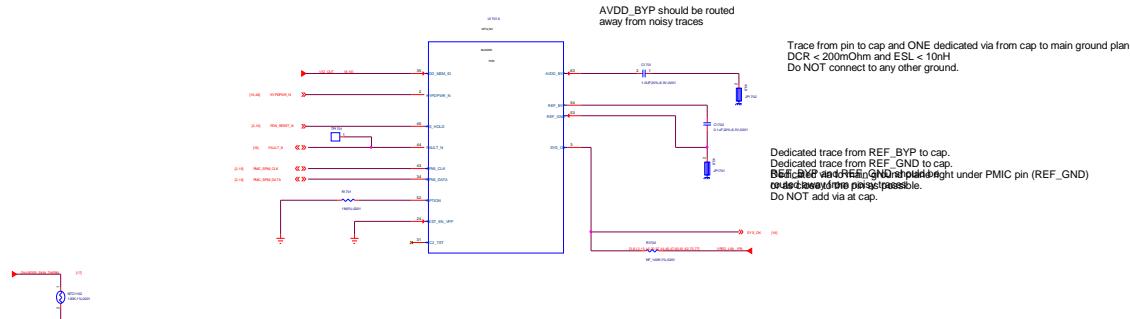


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PMI632_Control GPIO6 (LV) WLED_EN
GPIO7_AMUX (LV) SMB_VCHG_P
GPIO8_AMUX (LV) FMB2 (Output) / SMB_VHG_M

8 GPIO8_AMUX (LV) FMB2 (Output) / SMB₆.VHG_

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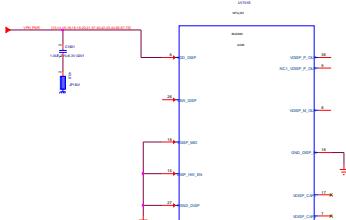
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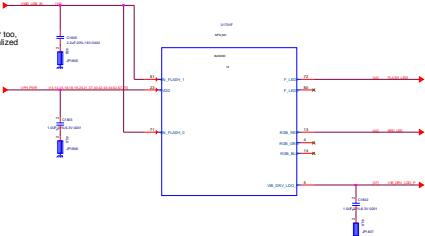
PMI632_LCD/VIB/RGB/Flash



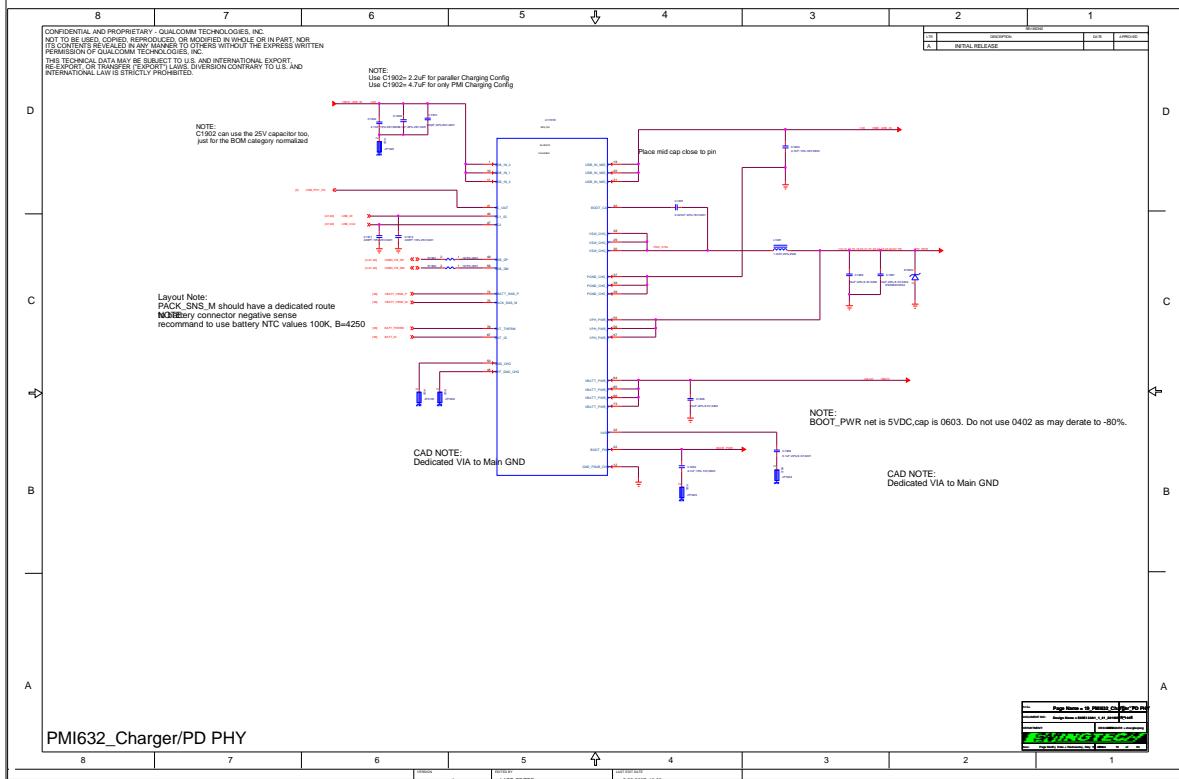
Layout Note: VDISP_CAPx
Place fly cap as close to the pin as possible.
Avoid routing VDISP_CAP1 trace under VDISP_M_OUT.

Layout Note: DISP_HW_EN
This signal is used for "Display Tap to Wake"
If unused in HW, this pin should be connected to GND

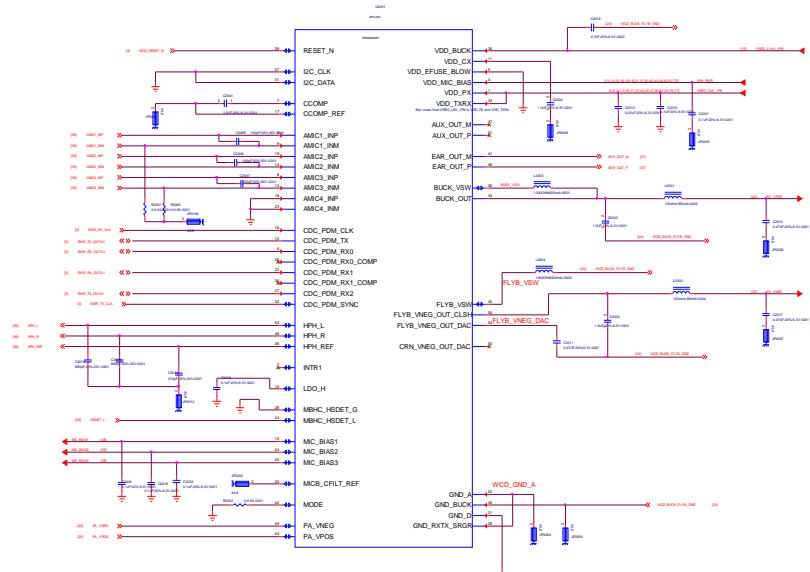
NOTE:
C2404 can use the 25V capacitor too,
just for the BOM category normalized



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VERSION	1	ASSEMBLER		LAST EDITOR	2-23-2007_10:06		

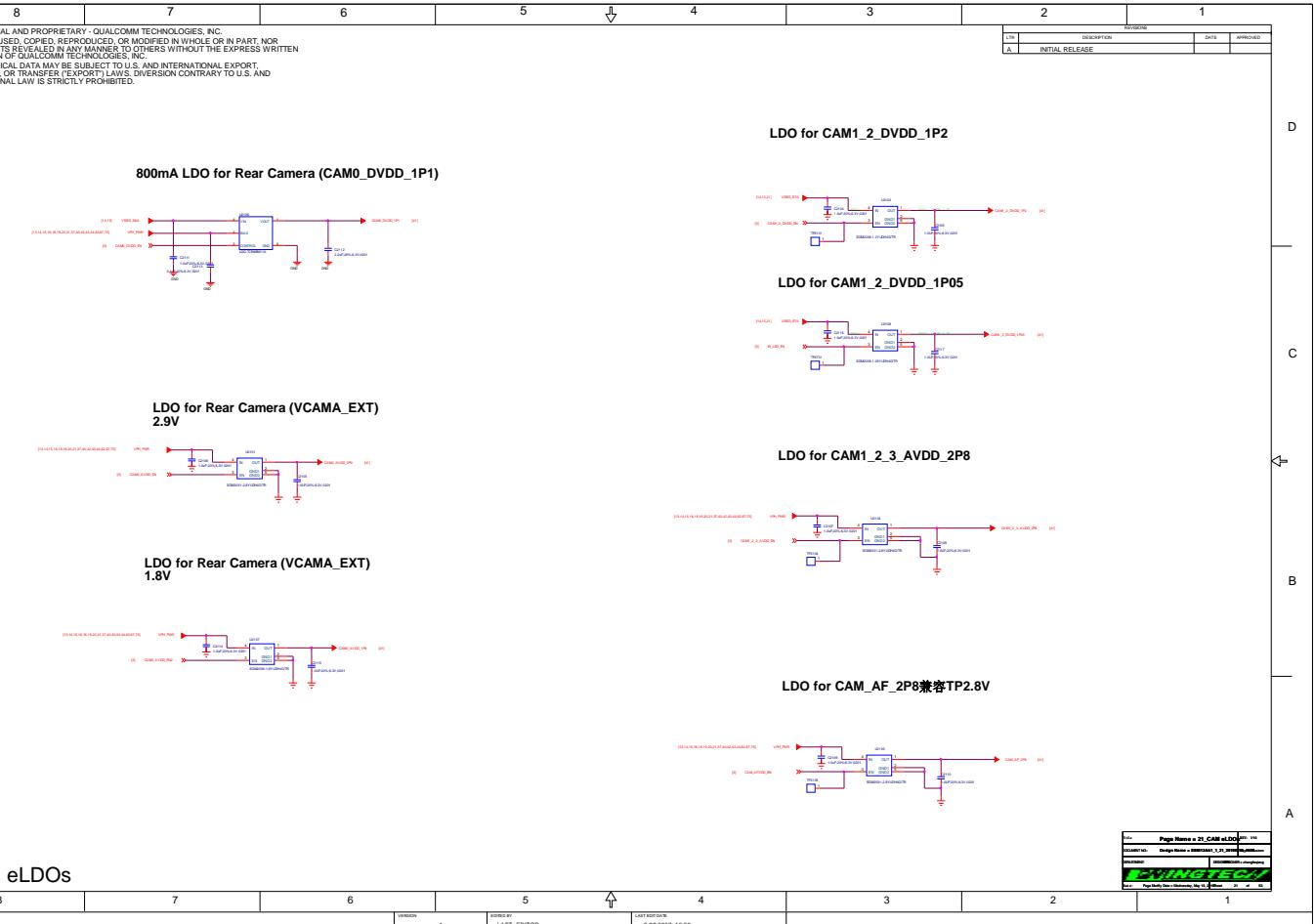


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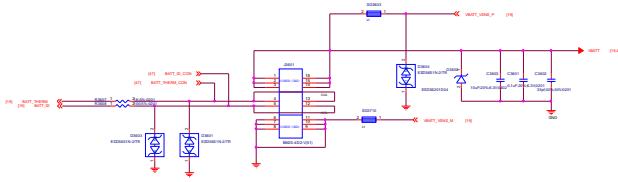


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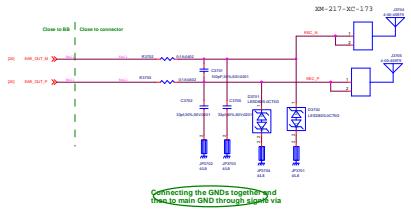
BATTERY CONNECTOR



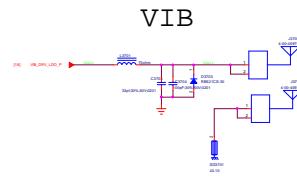
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SPK

Receiver



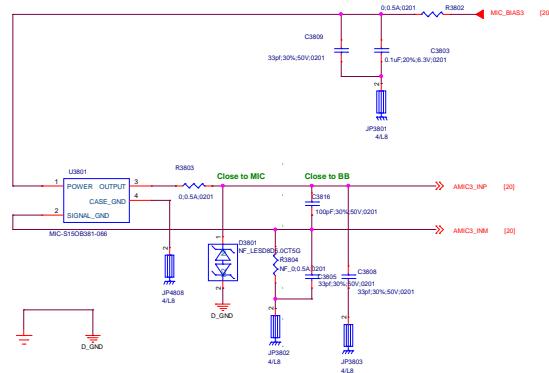
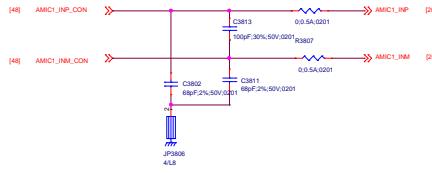
VIB



Page Name = 37_SPK_VibAndVibFilter
Document No.: Design Name = SPK_VibAndVibFilter
LIVINGTECH
Your Best Choice Electronics Co., Ltd.

Handset 2nd Microphone (Rear - DMNR), AMIC

MAIN MIC

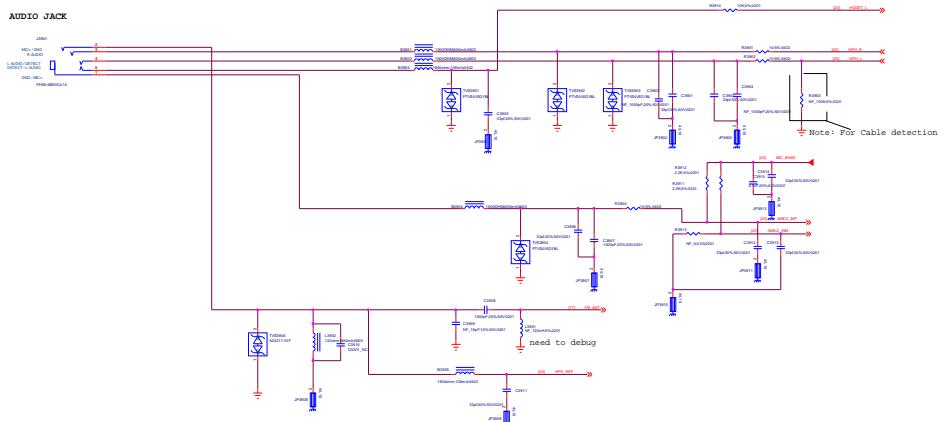


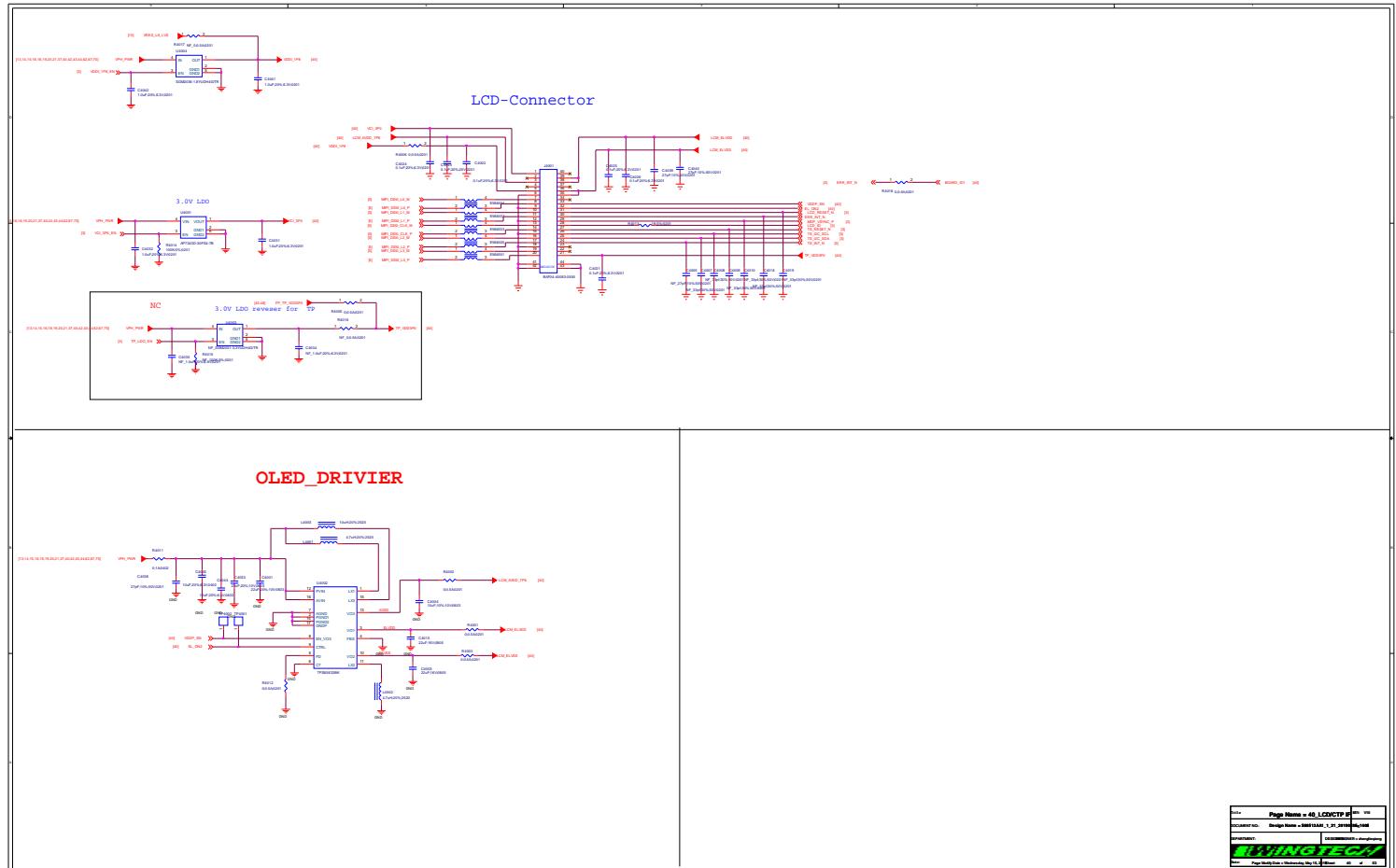
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Note 63-1: 1 uF for ACC mode

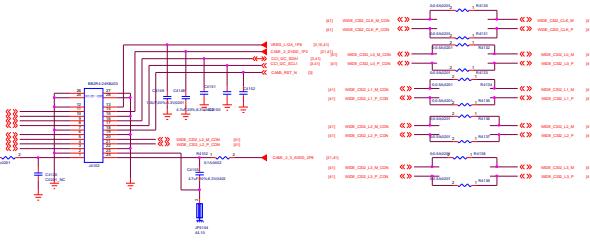
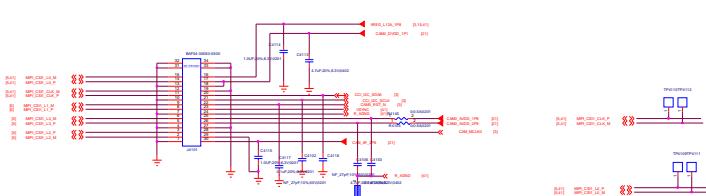
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DEPARTMENT:	DESIGNER = ZhengJing	
Date:	Page Modify Date = Wednesday, May 15, 2019 04:38	of 53

Note: Ferrite beads and their corresponding bypass capacitors on
on CDC_HPH_L_P, CDC_HPH_L_M and CDC_HPH_REF
are needed to reduce noise generated by audio/PM concurrency

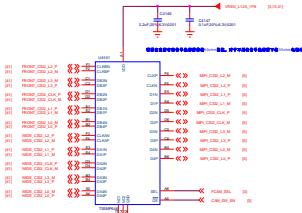
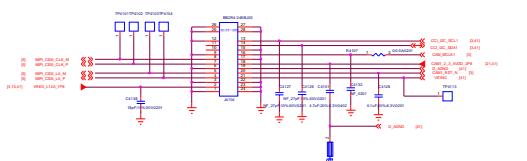




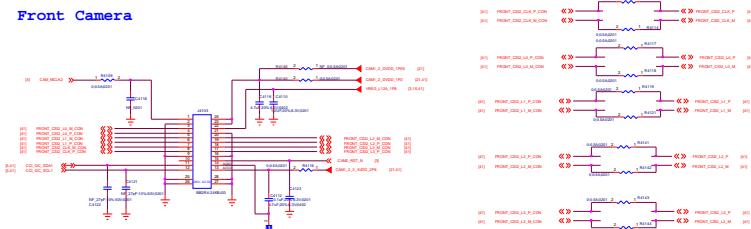
Main Camera



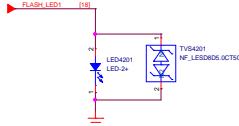
Depth Camera



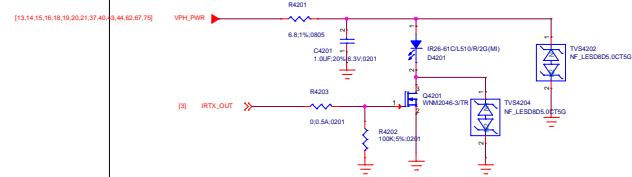
Front Camera



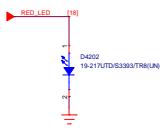
FLASH_DRIVER



IR_LED

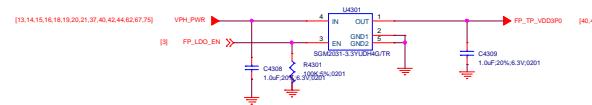


LED_DRIVER



Title:	Page Name = 42_Flash/RGB	V10
DOCUMENT NO.:	Design Name = S88912AA1_1_21_2019051509000 C	
DEPARTMENT:	DESIGNEER	zhangqiang
Net:	Page Modify Date = Wednesday, May 15 2019	42 of 53

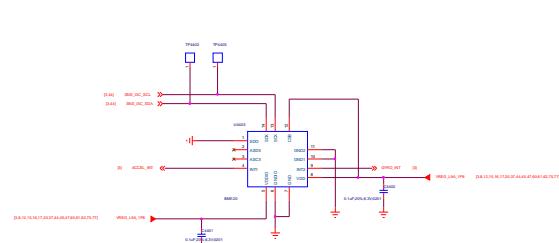
FP LDO 3.0V



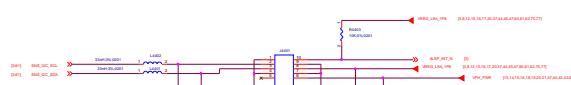
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DOCUMENT NO.:	Design Name = 888812AA1_1_21_20190515	Sheet No. C
DEPARTMENT:	DESIGNER = zhangjiaqiang	REVIEWER =
Date:	Page Modify Date = Wednesday, May 15 2019	43 of 53

ACCELEROMETER

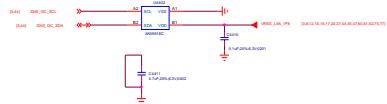
A+G SENSOR

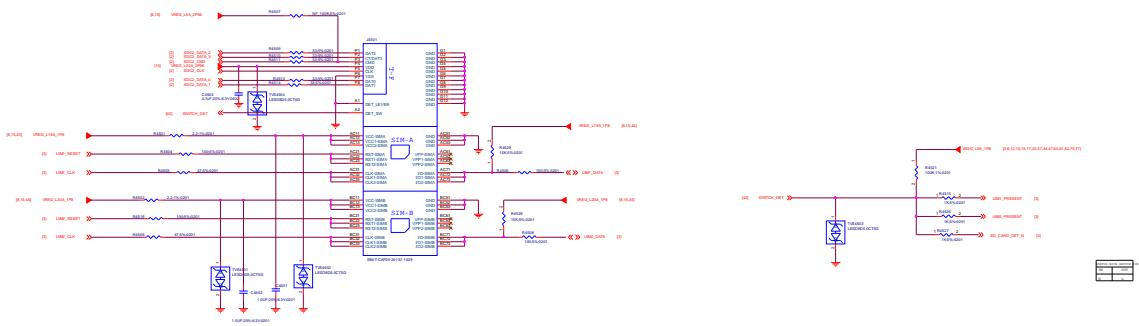


ALP-SENSOR

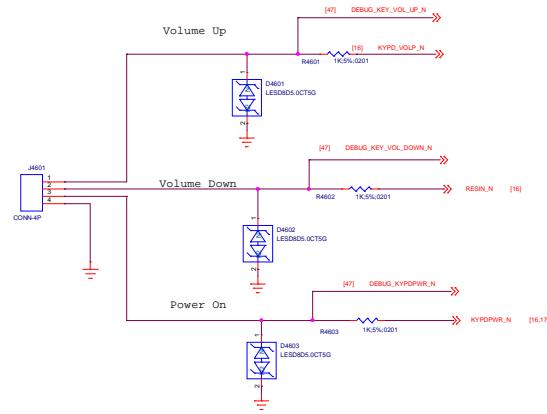


Magnetic Sensor





Keys



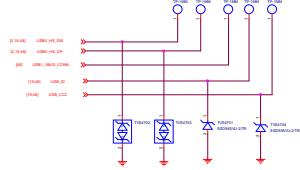
Schematic design notice of "75_PERI_KEYPAD" page.

Note 75-1: DO NOT put pull-up resistor on PWRKEY

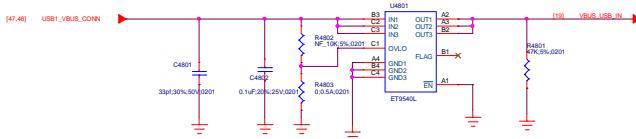
Note 75-2: Volume Up : HOME Key / GND
Volume Down : KPROW0/KPCOL0

Page Name = 46_Sidekey	REV: V10
DOCUMENT NO.: Design Name = 588812AA1_1_21_20190524EN05C	
DEPARTMENT:	DESIGNER = zhangjiaqiang
DATE: Page Modify Date = Wednesday, May 29 2019	401 of 53

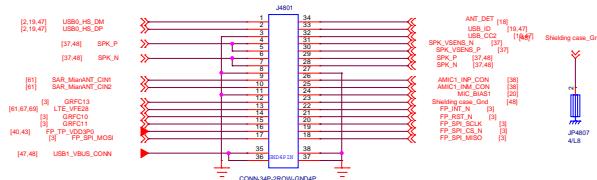
USB



OVP



Sub Board Connector



Page Name = 48_Sub PCB IF	REV: V10
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DEPARTMENT:	DESIGNER = zhangleiwei CHECKER = zhangleiwei
Date:	Page Modify Date = Wednesday, May 22 2019 40:41:53

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Engineering Tech

Page Number is 27, Total page 111
Document: Project Name: C:\USERS\HAROLD\DESKTOP\PROJECT.DWG
Created: 2-23-2007, 10:06
Modified: 2-23-2007, 10:06
Author: LAST_EDITOR
Comments: None

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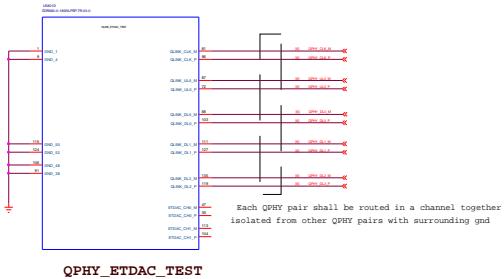
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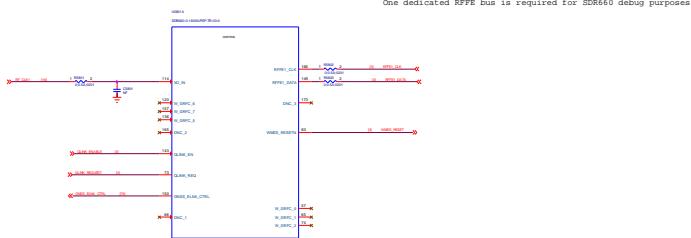
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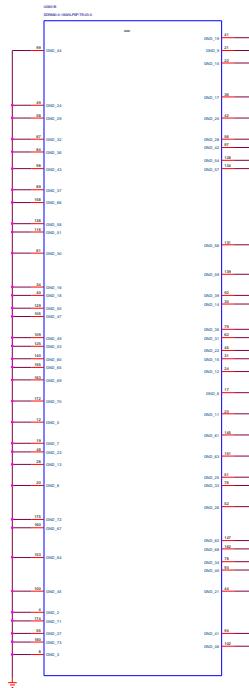
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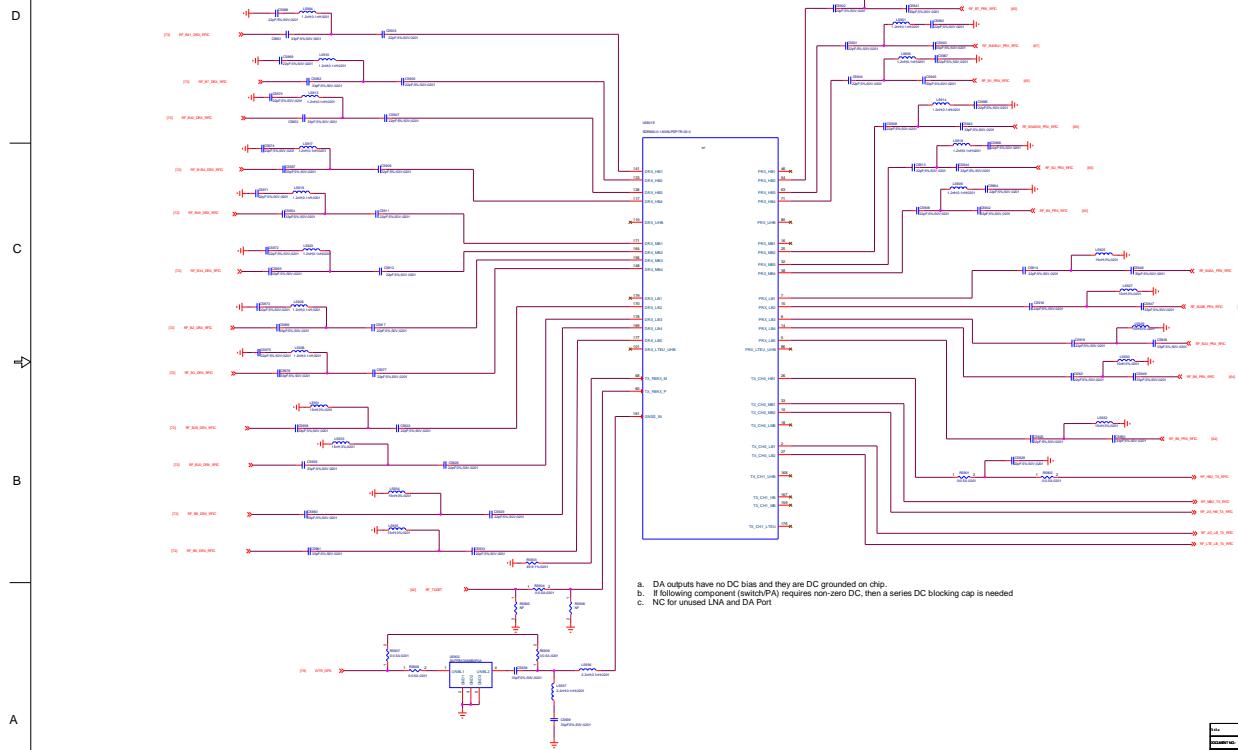
SDR660_MSS_GND



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LINE	DESCRIPTION	DATE	APPROVED
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SDR660 Tx,PRX,DRX,FBRX



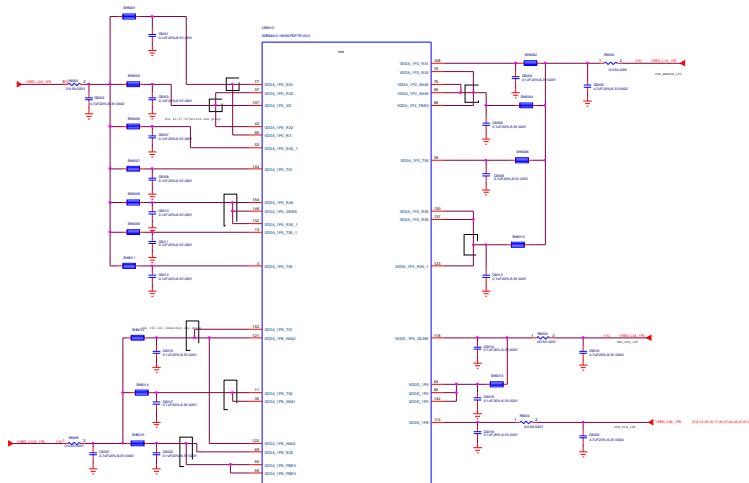
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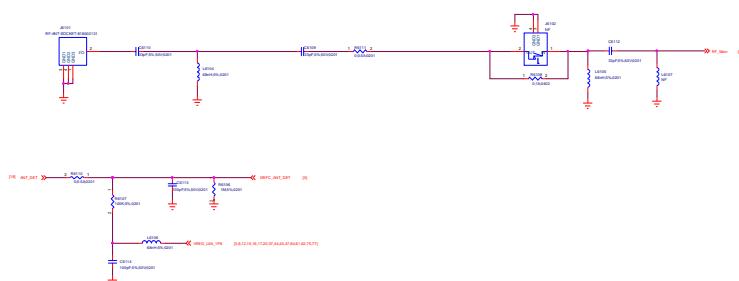
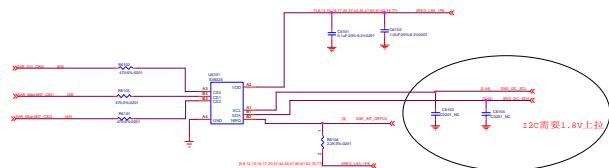
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SDR660 POWER

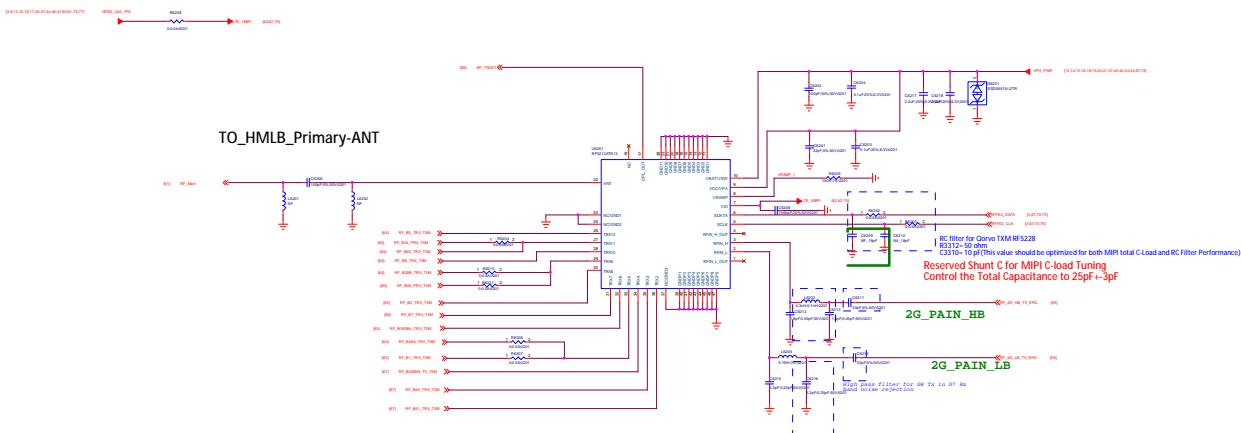


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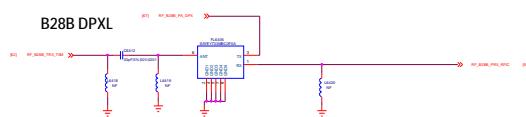
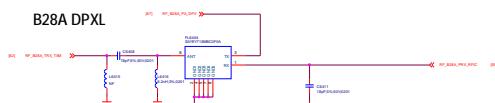
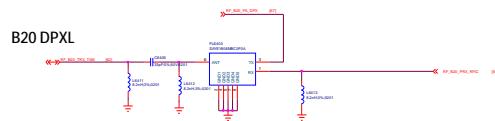
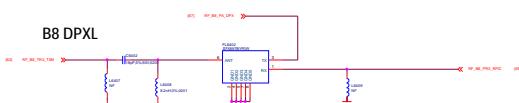
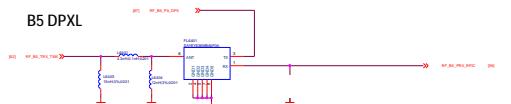




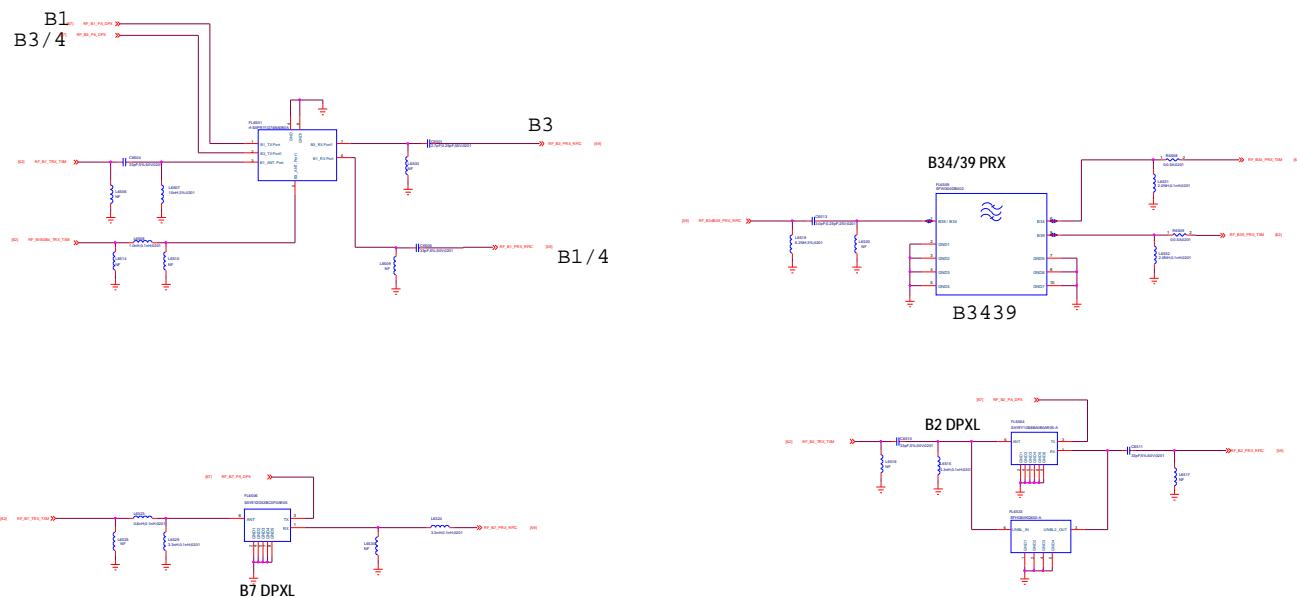
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Date:	Page Modify Date = Wednesday, May 15, 2013	Sheet:	61 of 83



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Division 1 Approved by LAST_EDITOR Last Edit Date: 2/23/2007_10:06								



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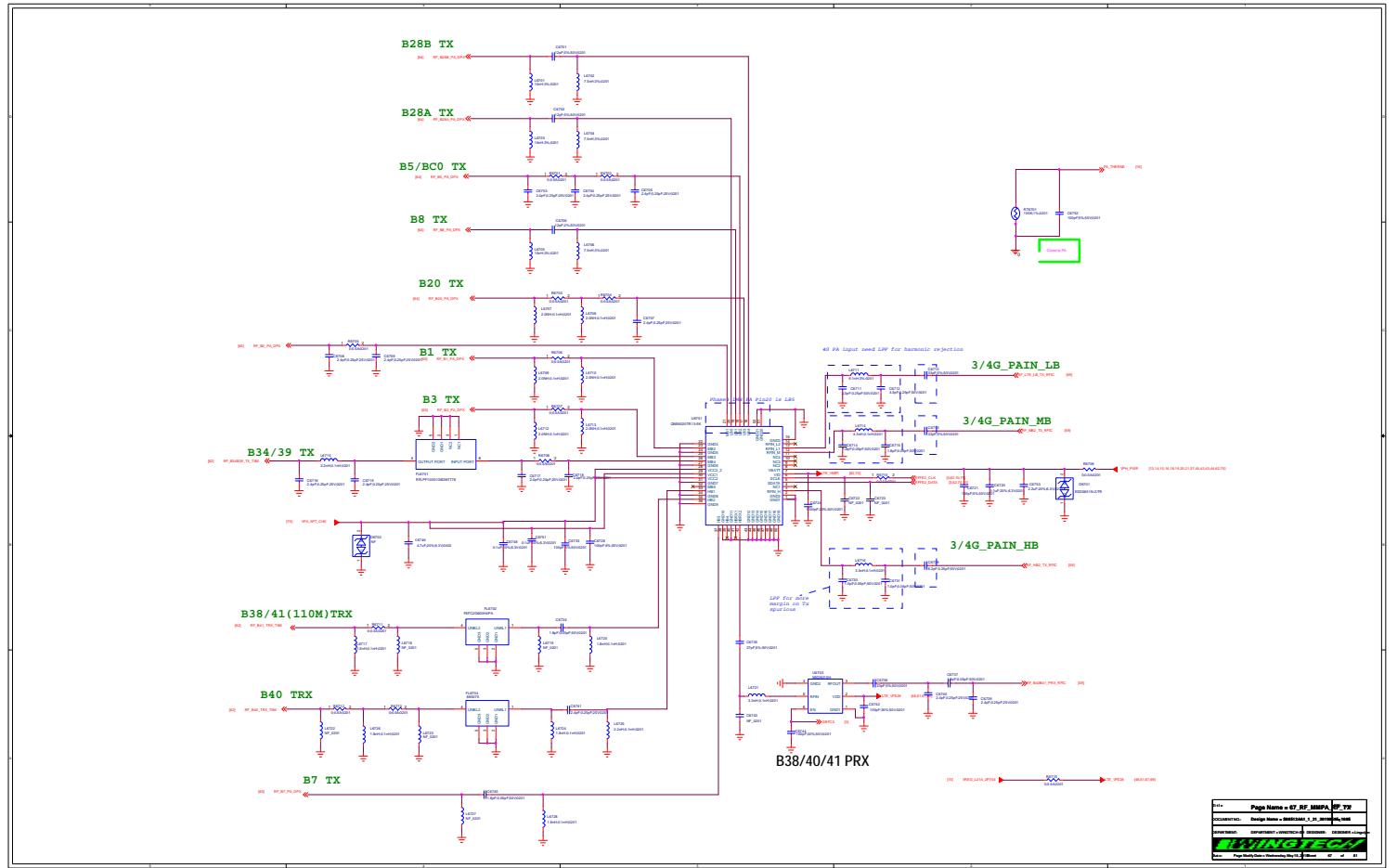
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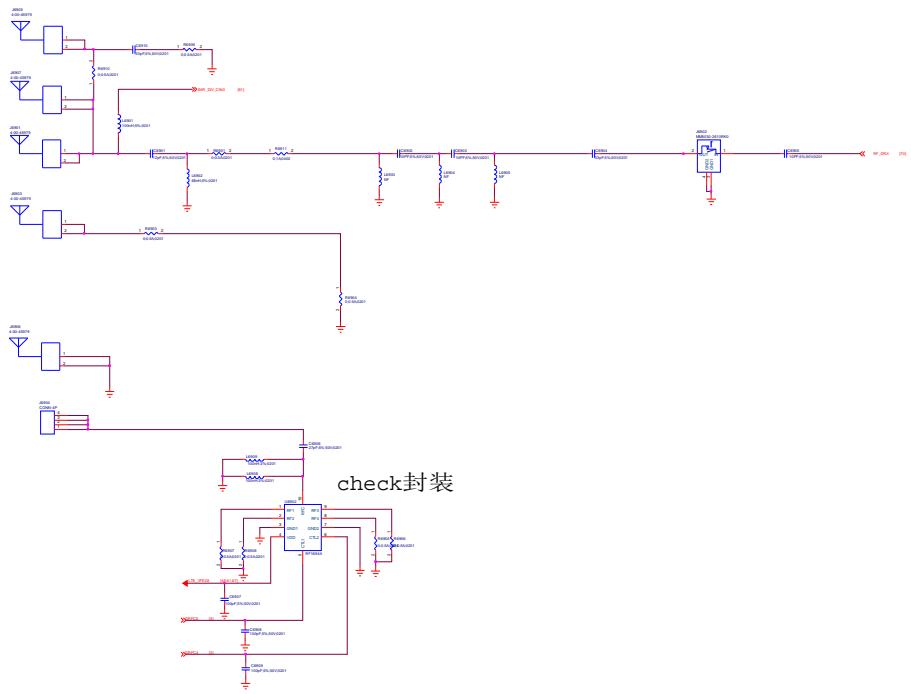
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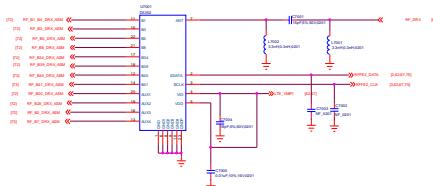


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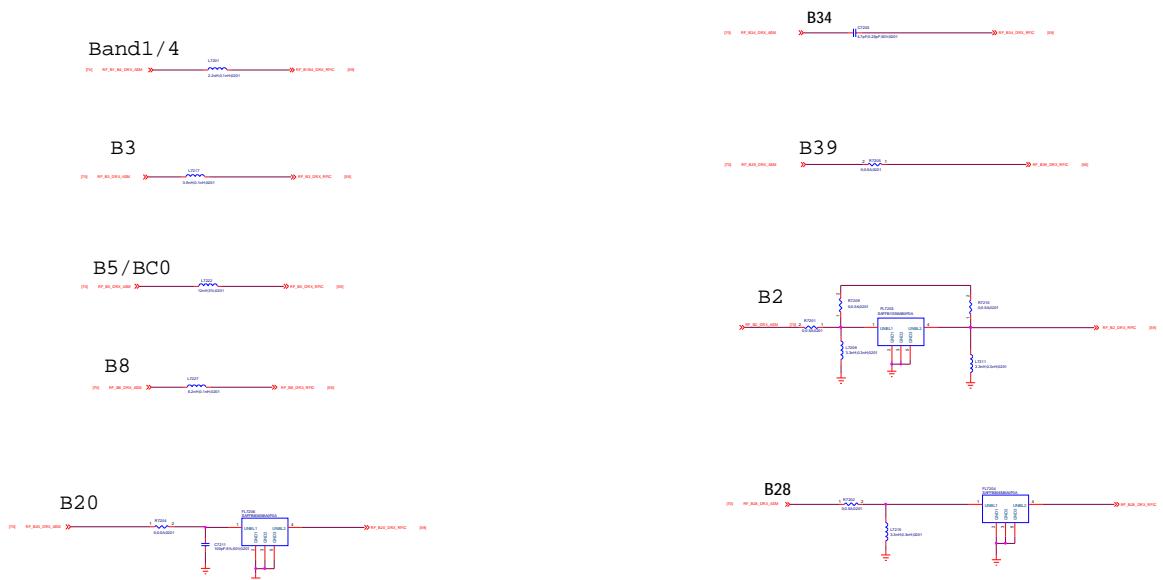
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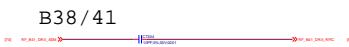
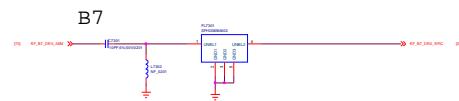
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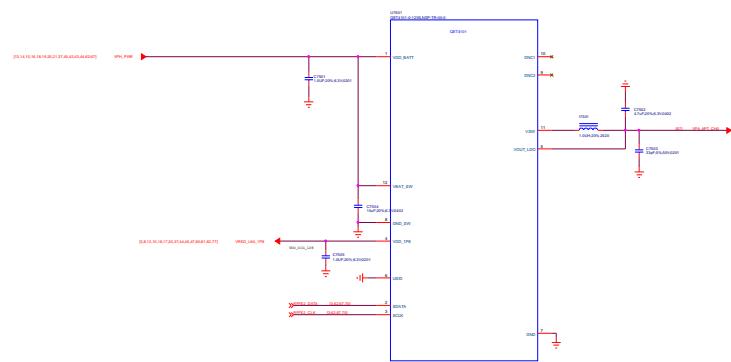
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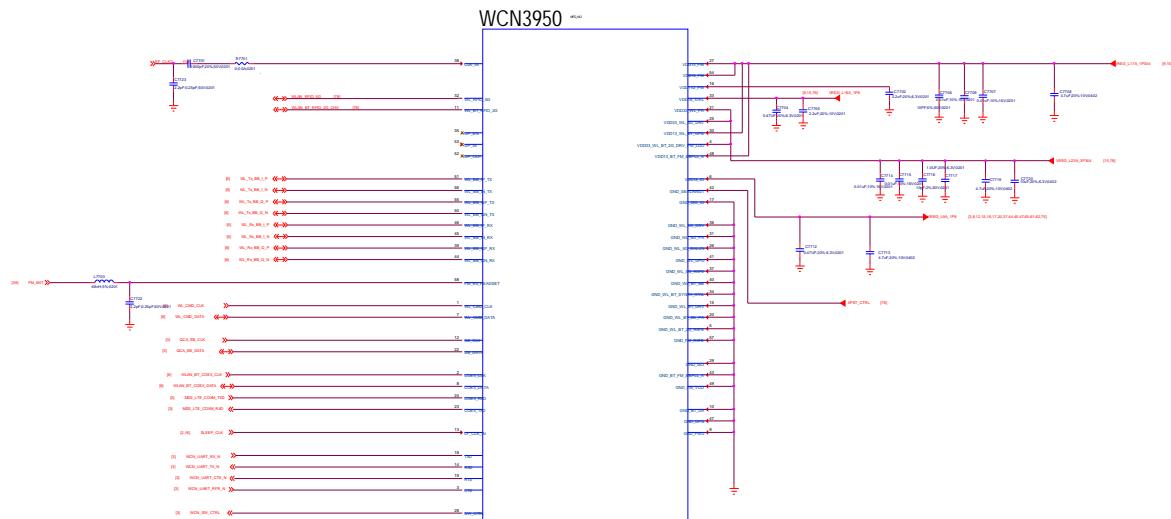
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WINGTECH		
Date:	Wednesday May 19, 2010	Sheet 75 of 81

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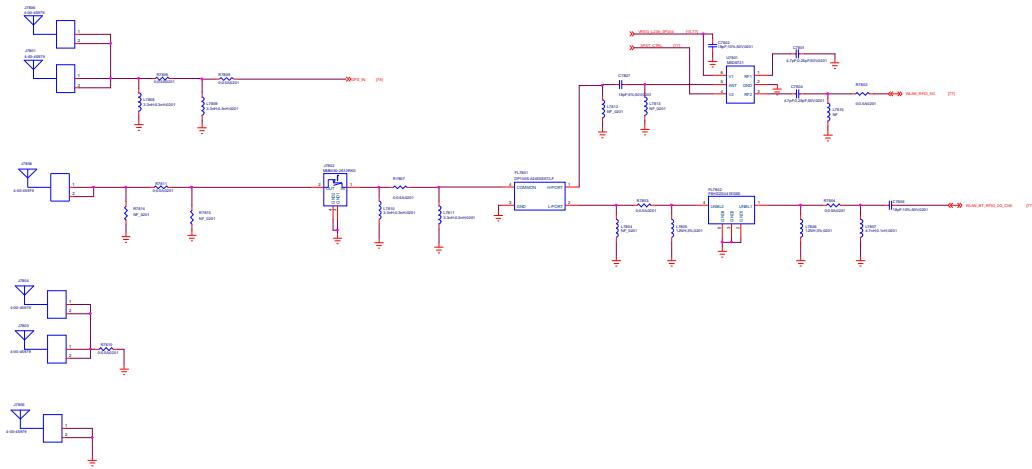
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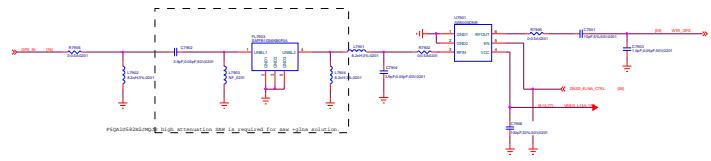


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DOCUMENTED:	SHREESHAN, J.P., JAYANTH, V.N.	Rev. 0
DEPARTMENT:	RFIC	DISCRETE Components
DATE:	Wednesday May 15, 2013	Sheet 1 of 1

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Created: 2-23-2007 10:06
Modified: 2-23-2007 10:06
Author: LAST_EDITOR
Comments: None
Engineering Tech
Page Style: Drawing - A4

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Document Status: Pending Review
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