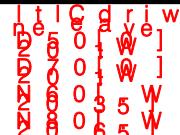


## TITLE

## CEDARVIEW + NM10( TigerPoint )

COVER SHEET	01	PCI SLOT+ 1x4 USB Conn	21
BLOCK DIAGRAM	02	Reserve	22
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CPU	
CHIPEST	NM10(TigerPoint-2W)
MEMORY	DDR3
SIZE	170mmx170mm

## VER 0.9

(1)修改自 BIPV10-IAP VER 0.9

## VER 0.95

(1)12V change to 24V

## VER 1.0

(1)CFAST Change to SATA

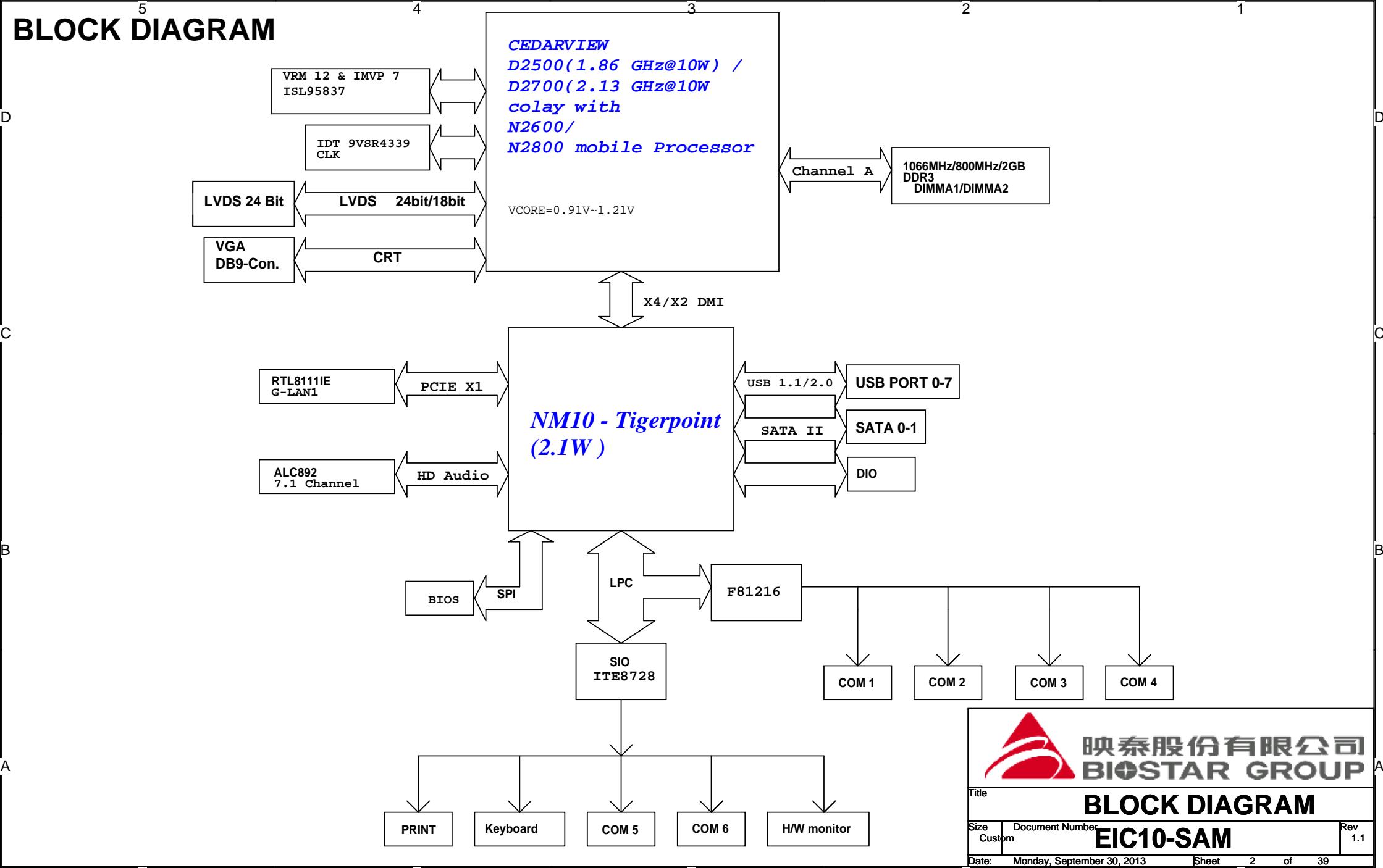
(2)ISL9440B Change to FP6326 (5V,3V)

## VER 1.1

(1)F81216DG Change to F81216AD  
HIN213A Change to ADM213

## VER 1.2

(1)change DC jack connector  
(2)reserve EAPD circuit  
(3)BQ15=FDV301N->2N7002



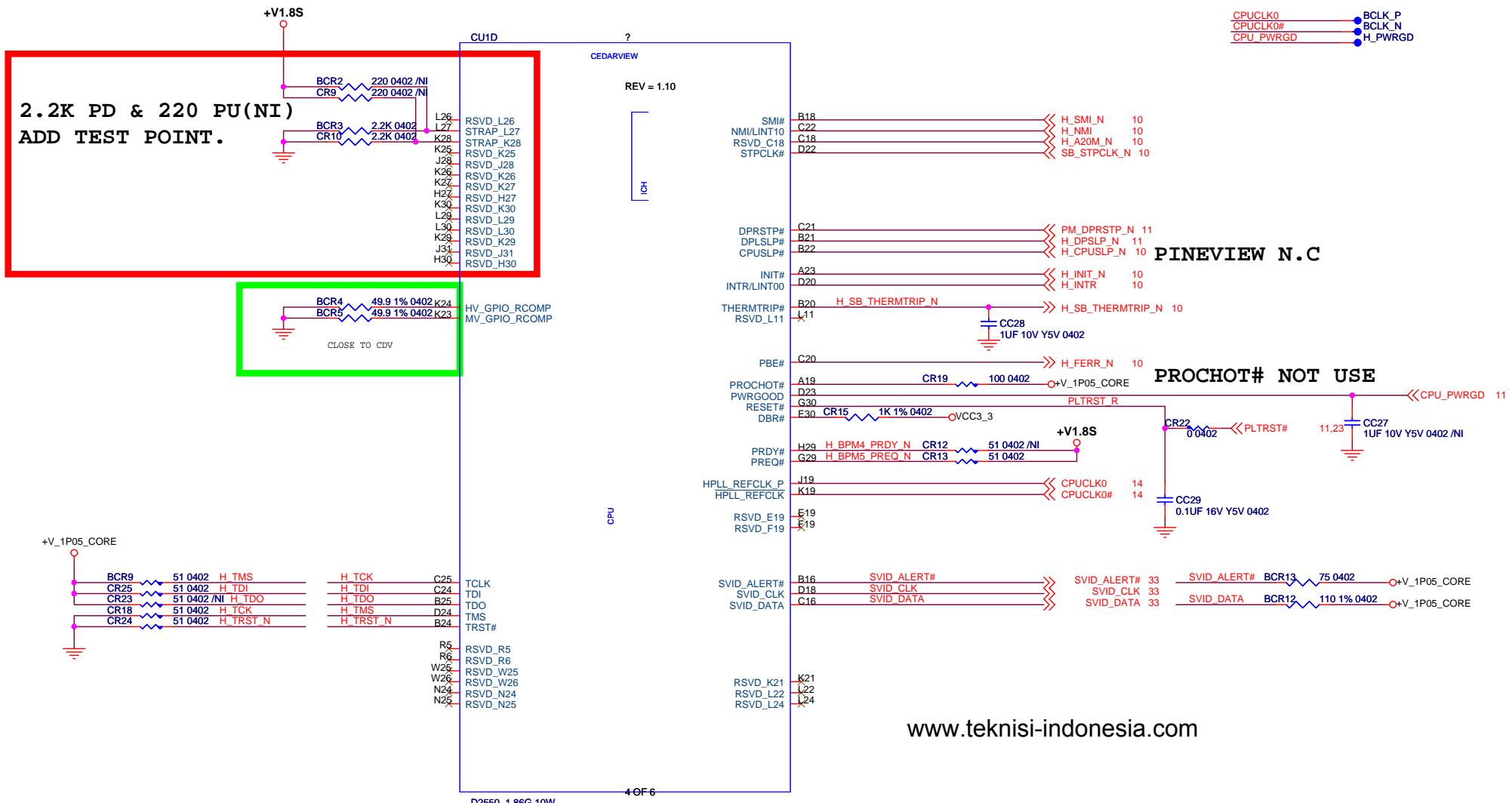
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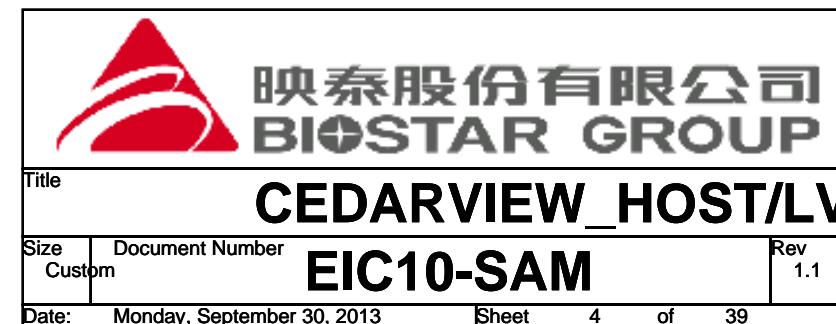
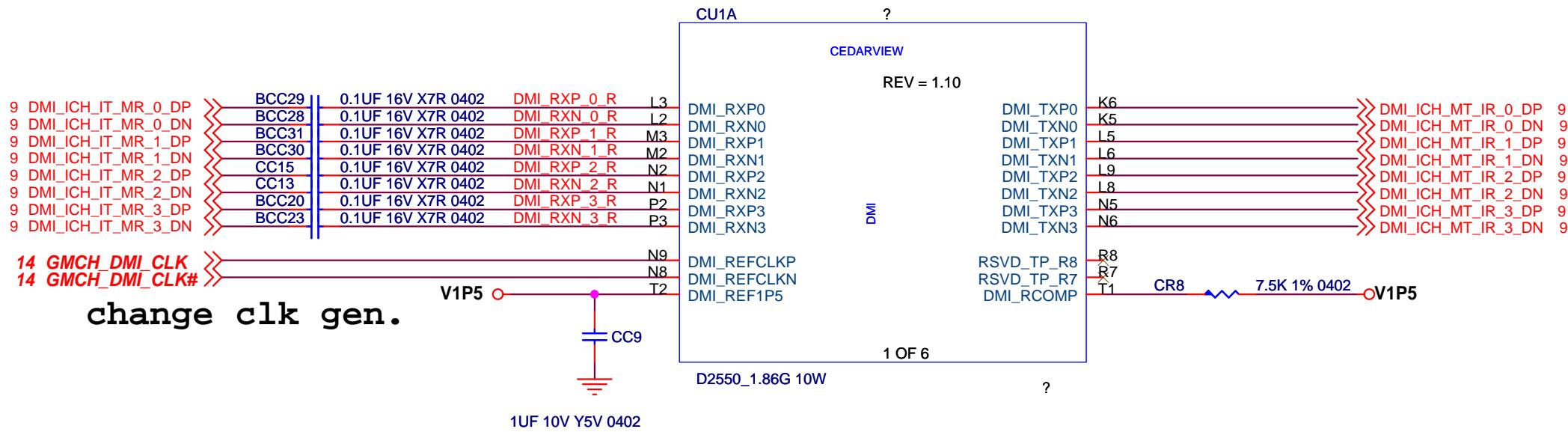
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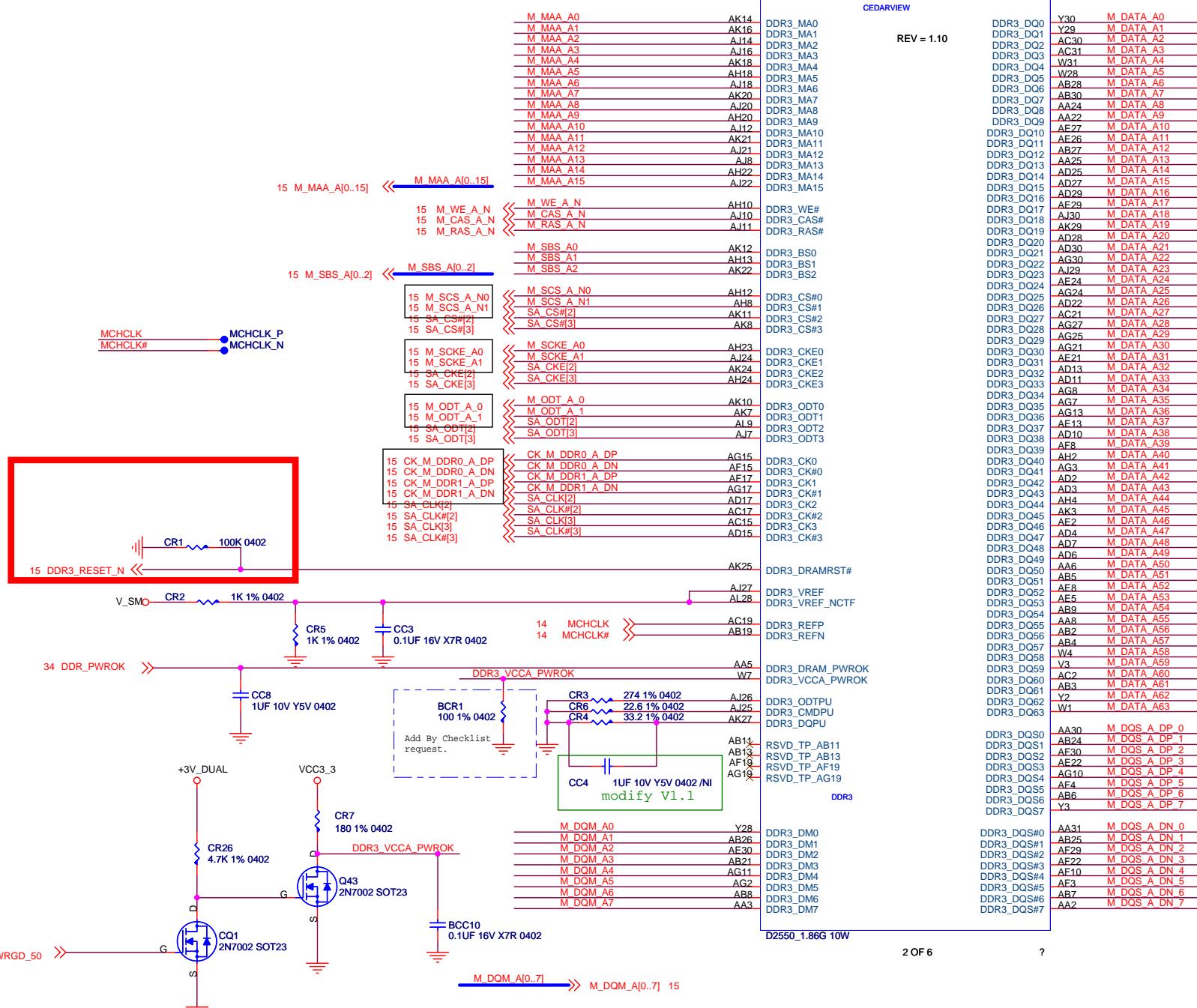
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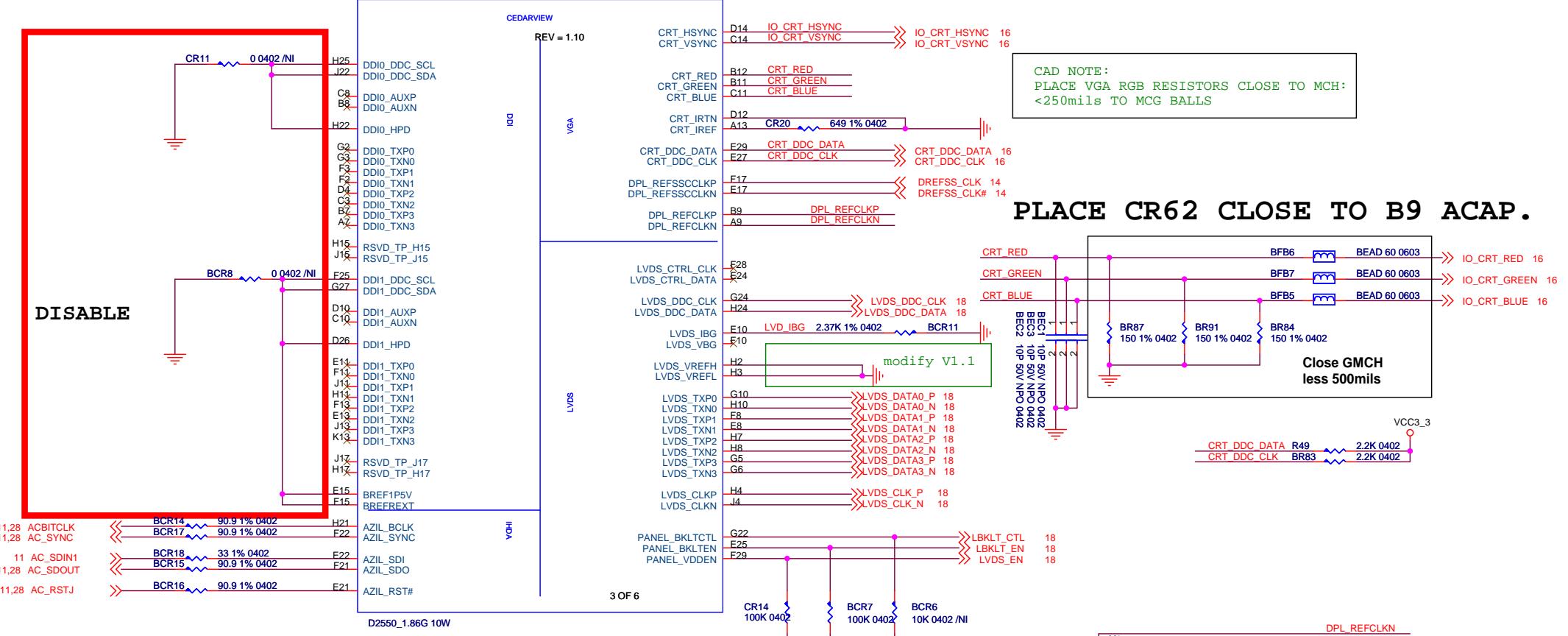




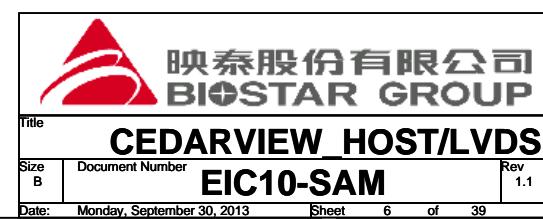


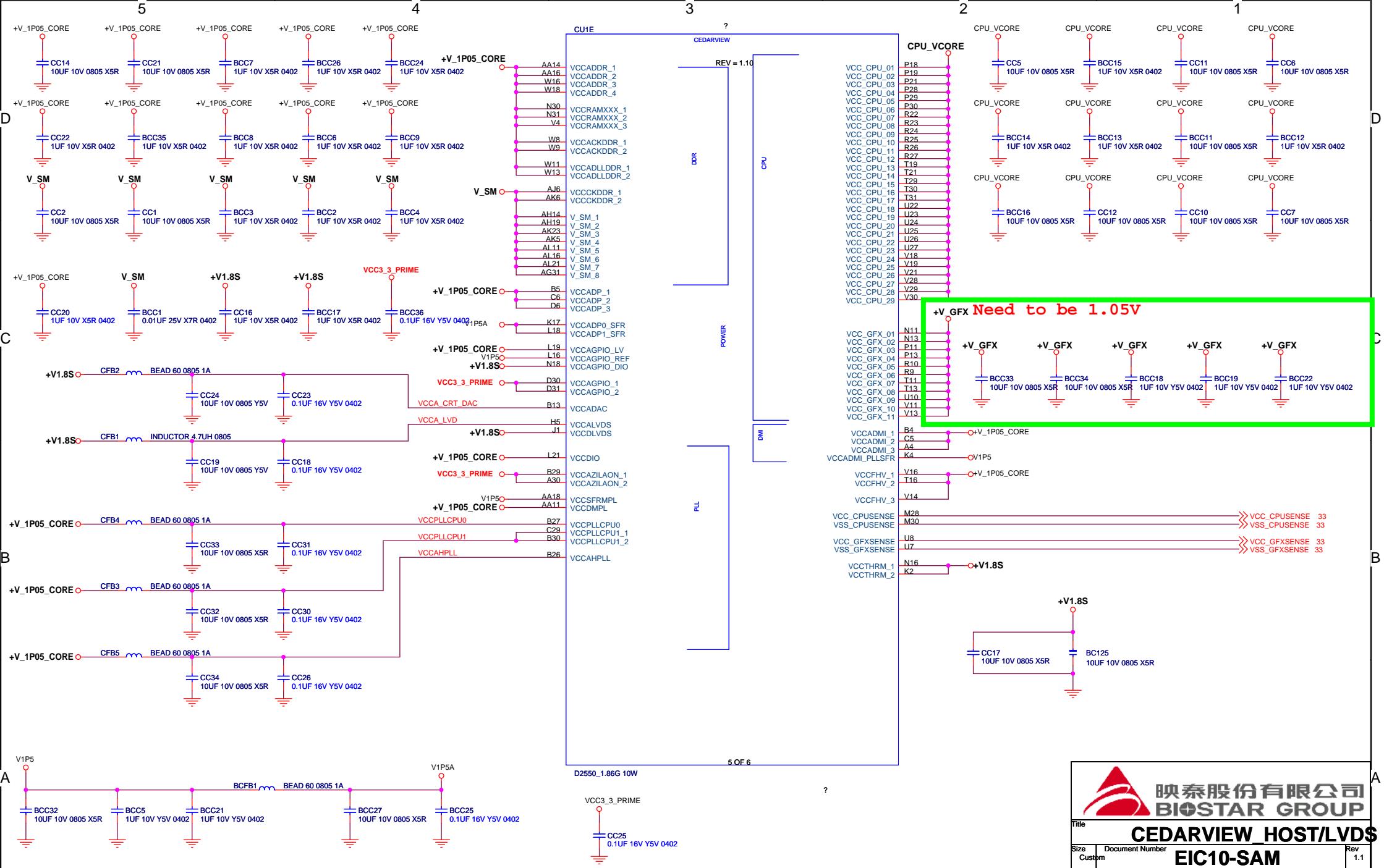
BIOSSTAR GROUP

## CEDARVIEW DDR3



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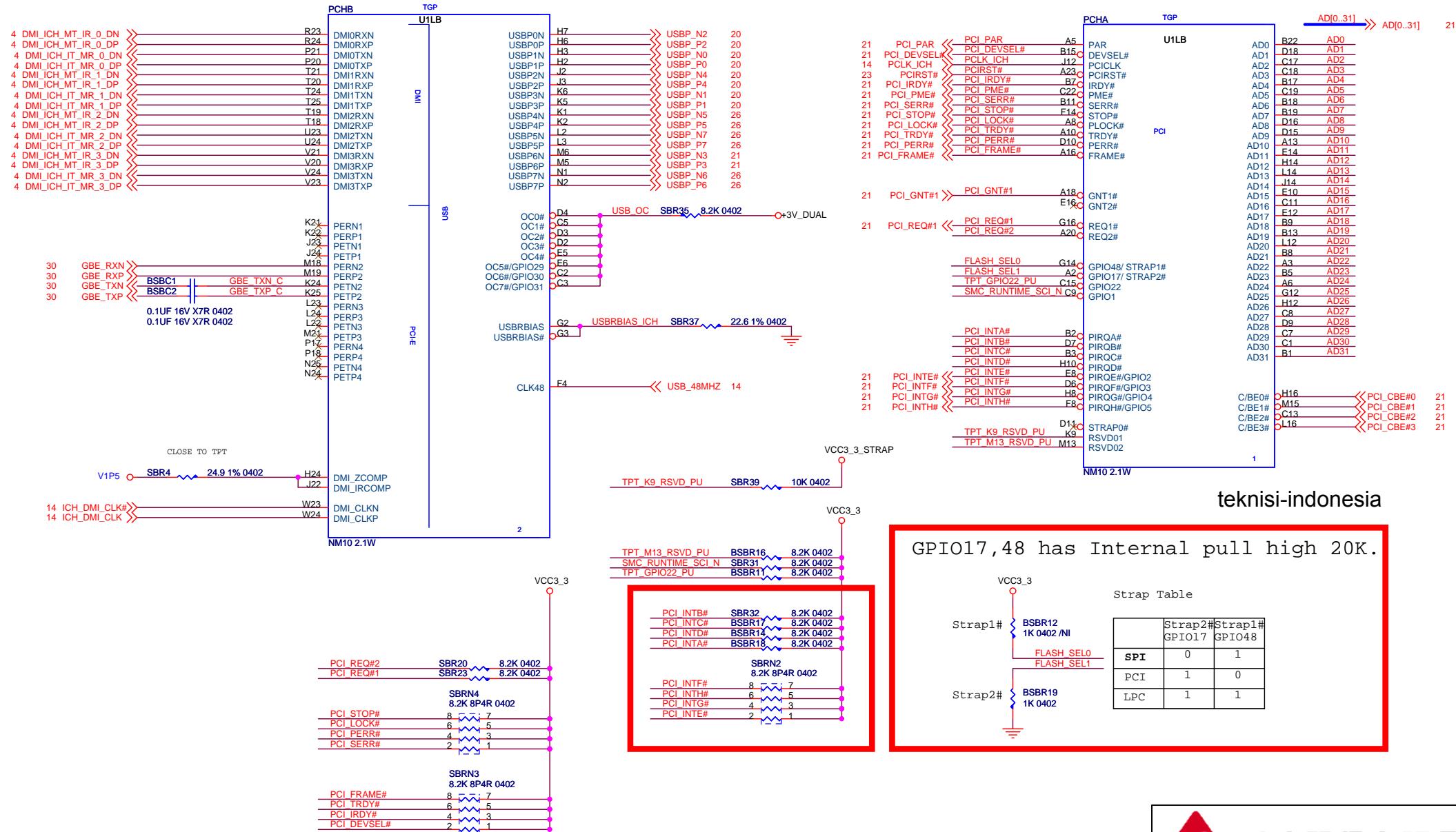
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GPIO17,48 has Internal pull high 20K.

	GPIO17	GPIO48
SPI	0	1
PCI	1	0
LPC	1	1



映泰股份有限公司

**TIGERPOINT\_DMI/PC**  
**EIC10-SAM**

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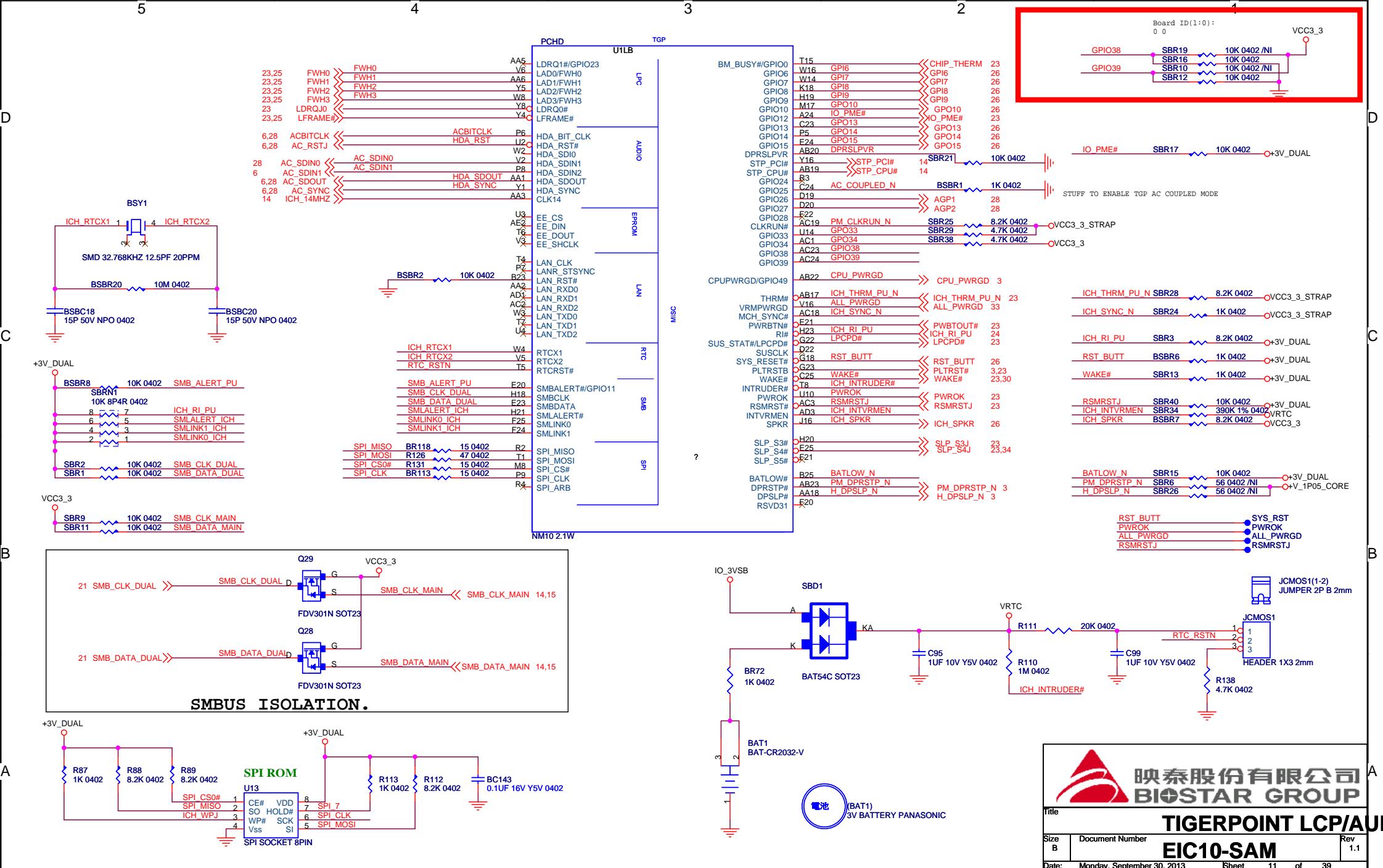
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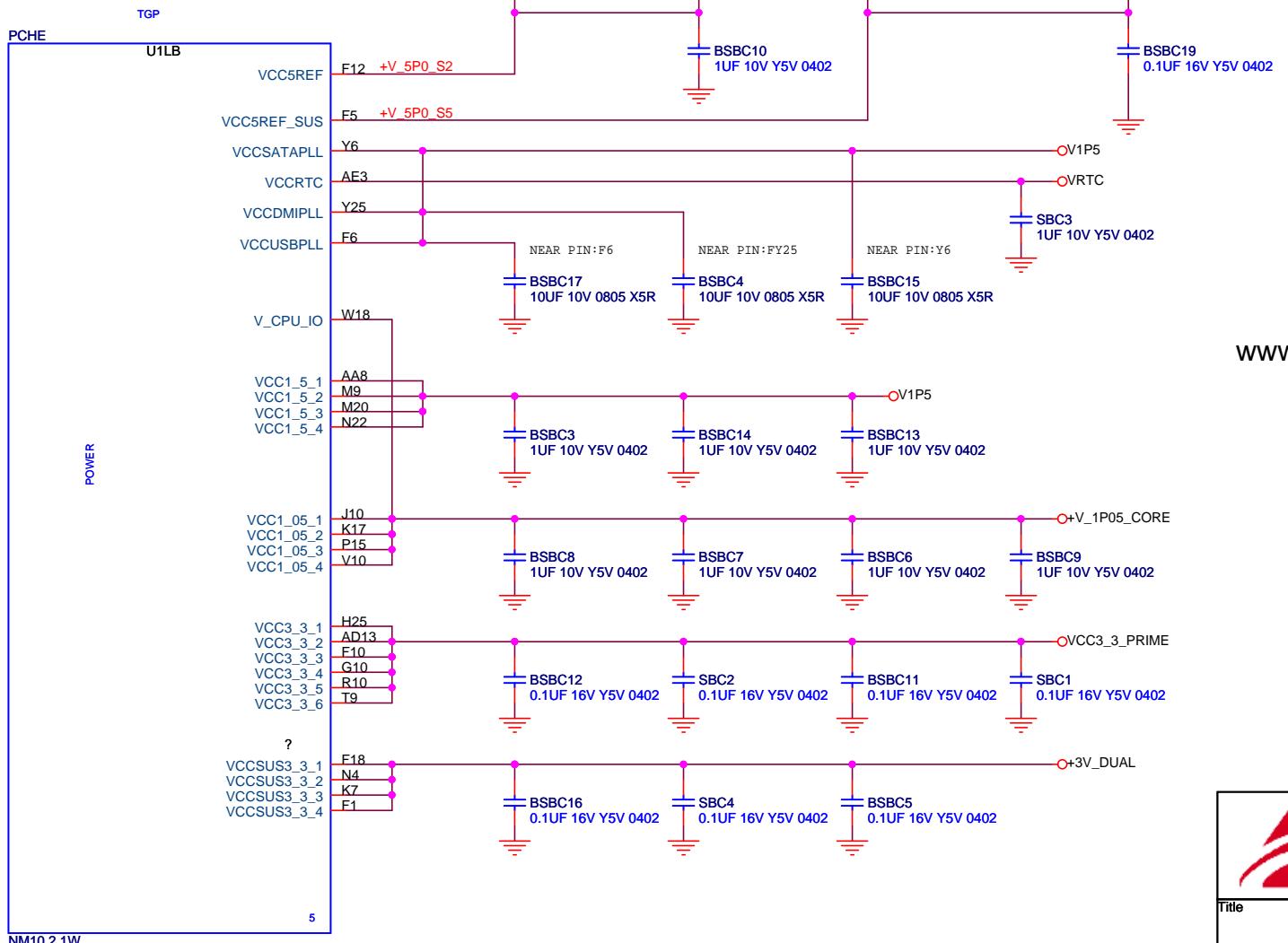
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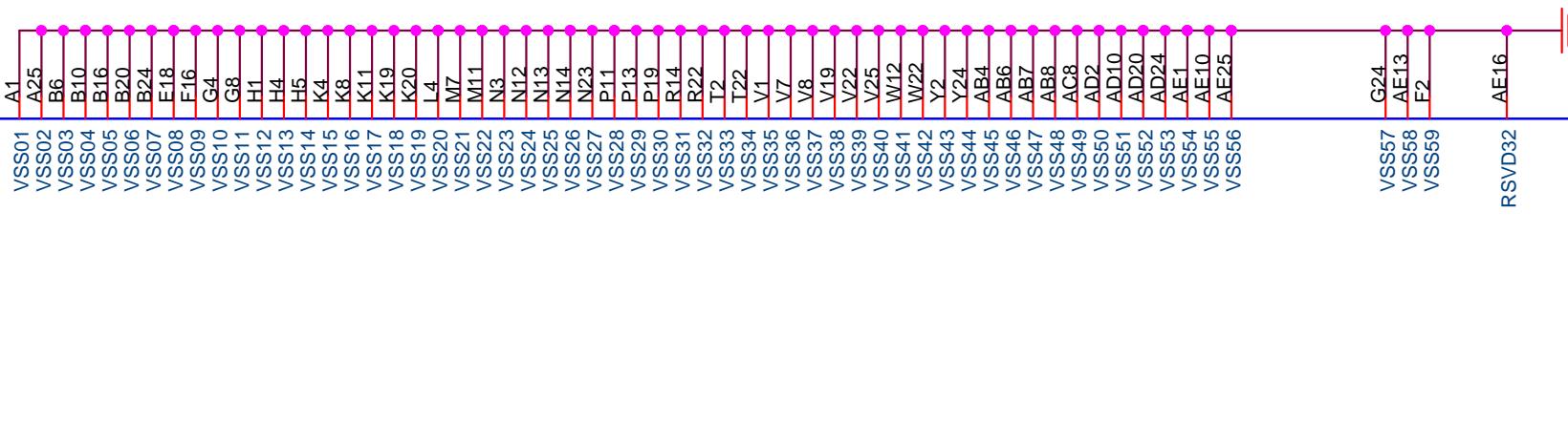

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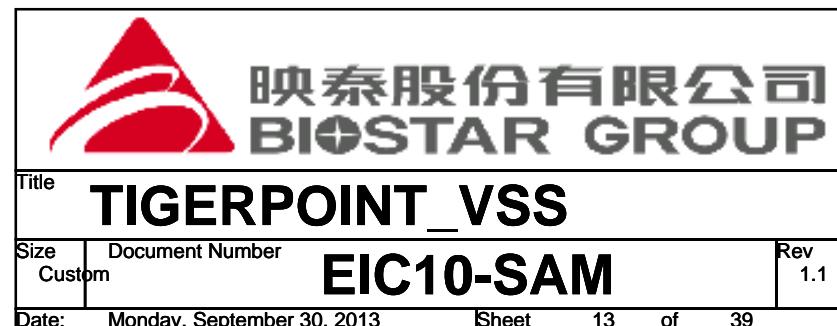
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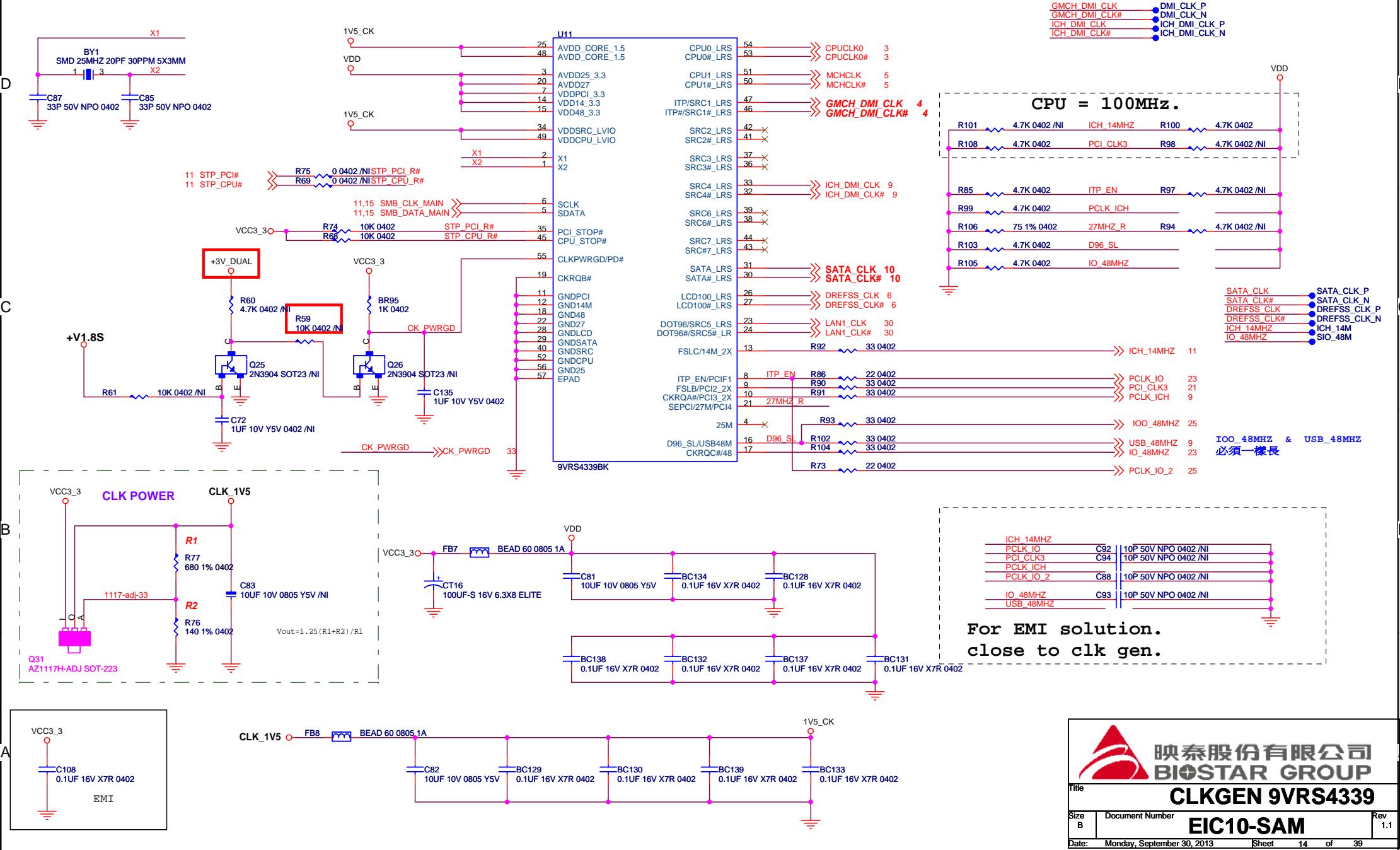
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NM10 2.1W  
<\$LOCATION>  
<MATERIAL>





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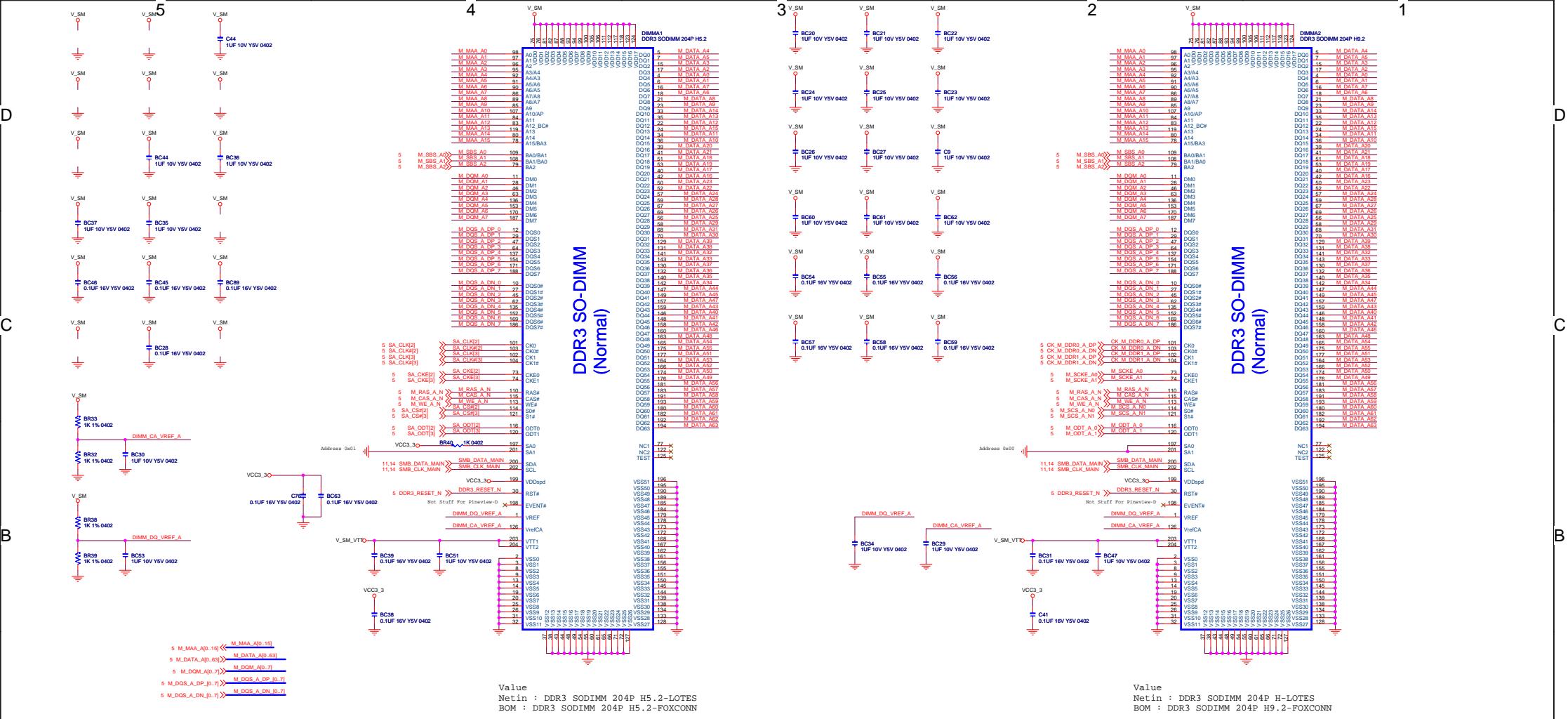
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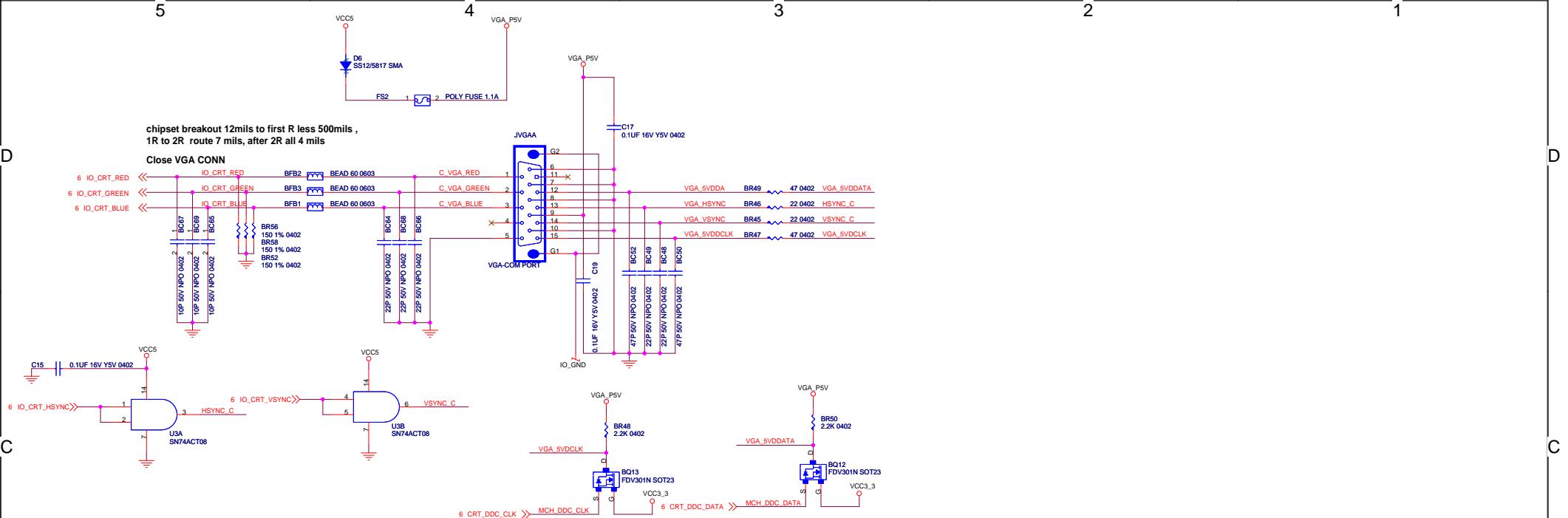
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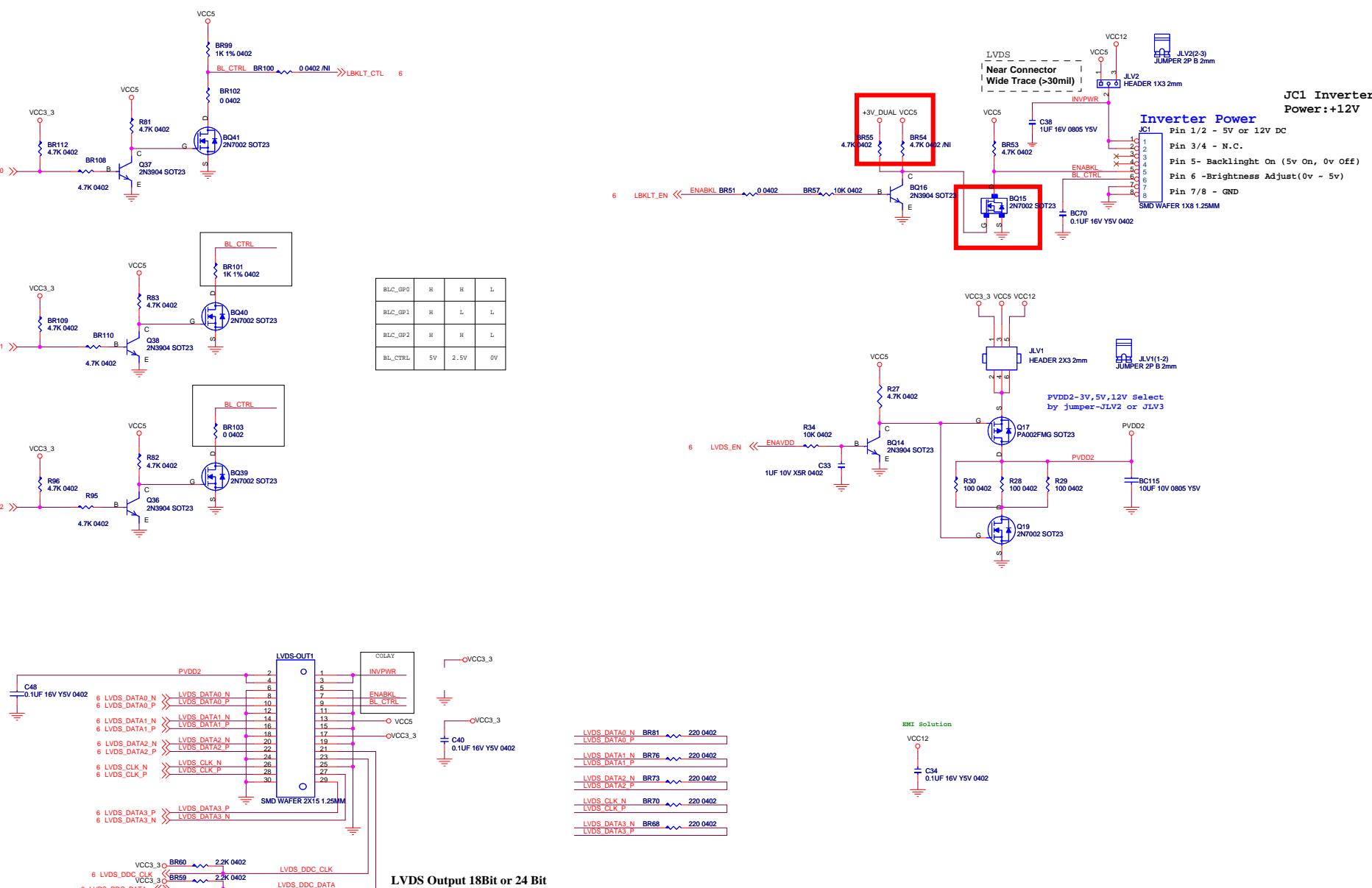


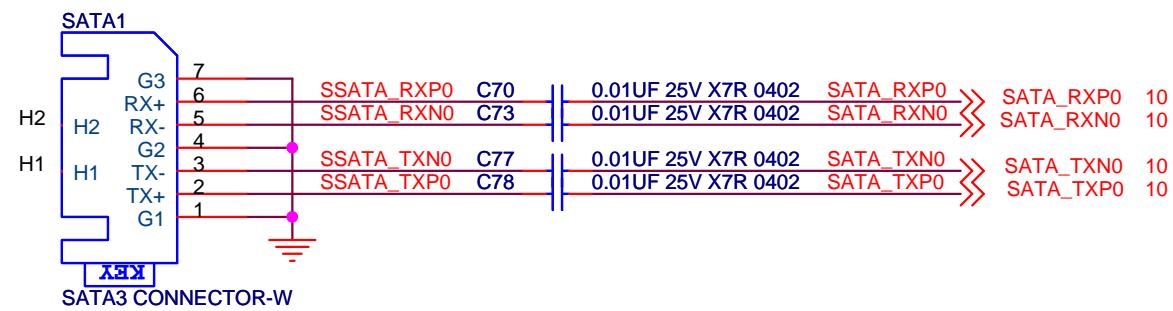
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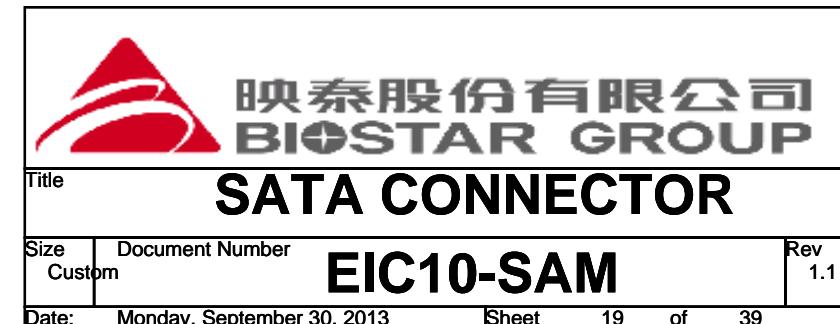
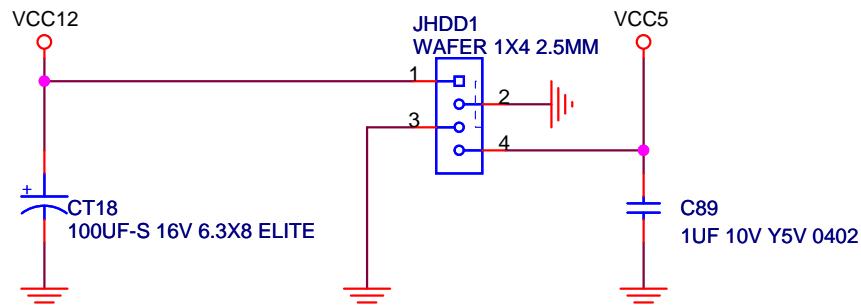
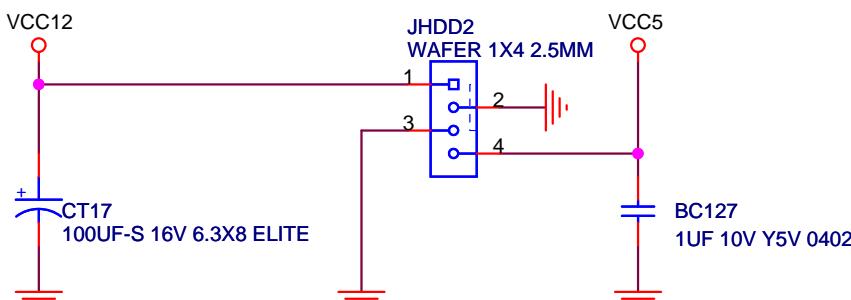
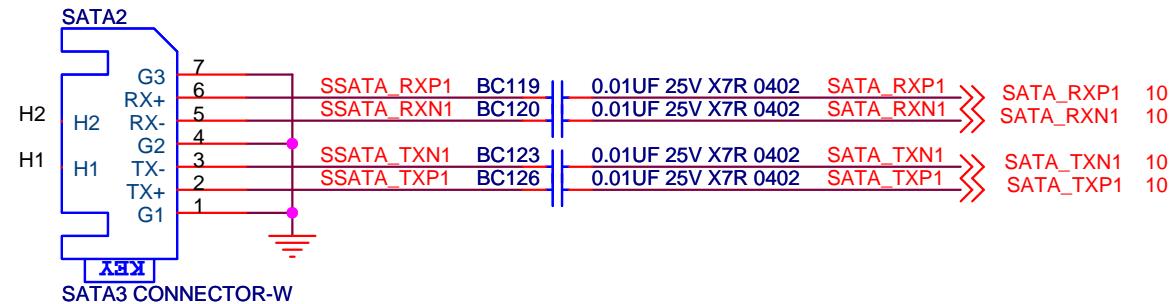
Title		<b>VGA CONNECTOR</b>		
Size C	Document Number			Rev 1.1
		<b>EIC10-SAM</b>		
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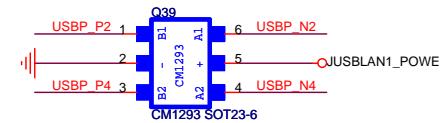
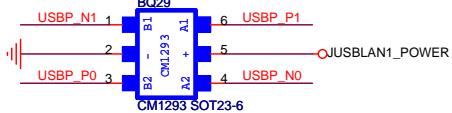
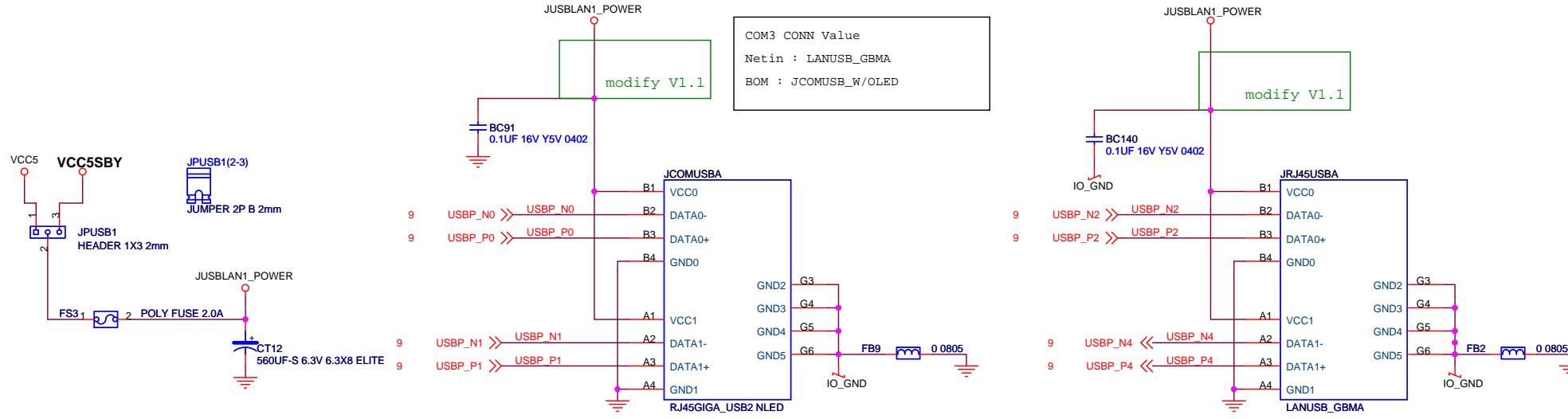






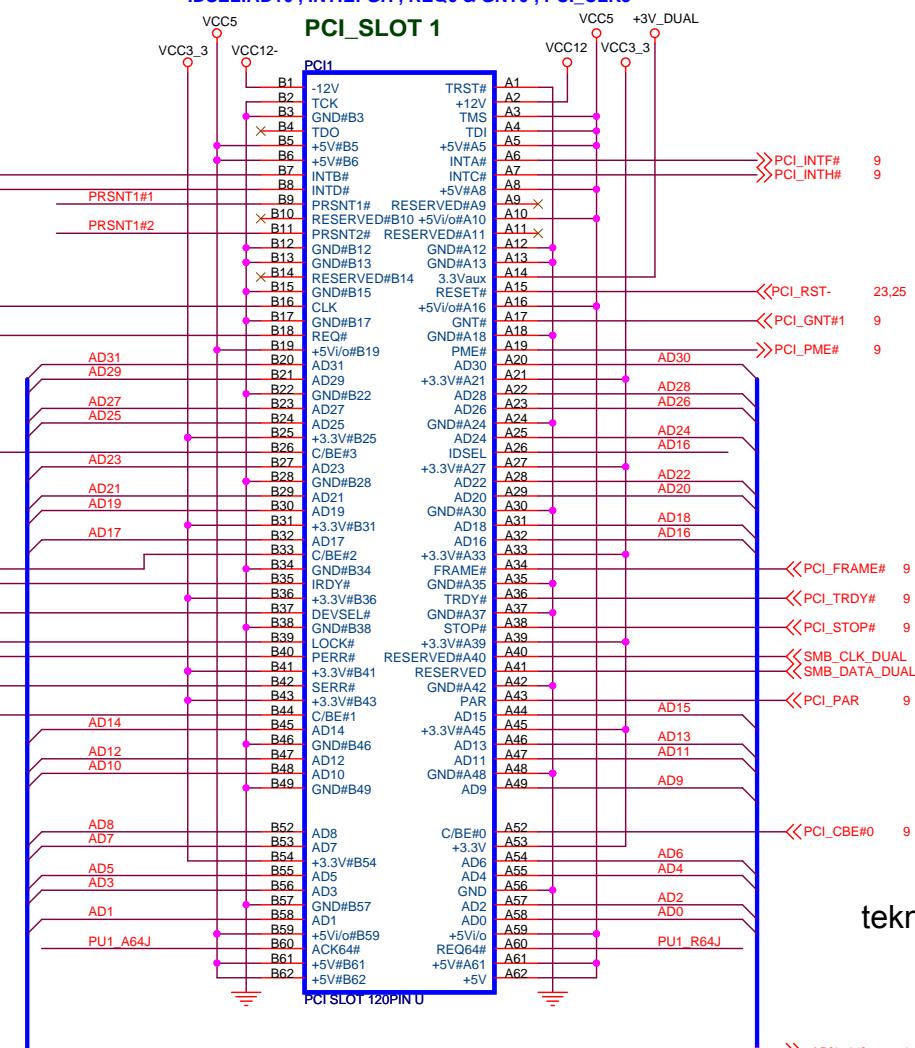
2011/08/09 Change from SATA CONNECTOR



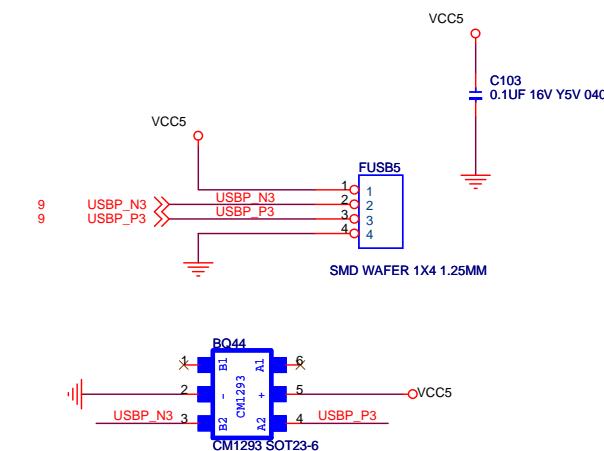
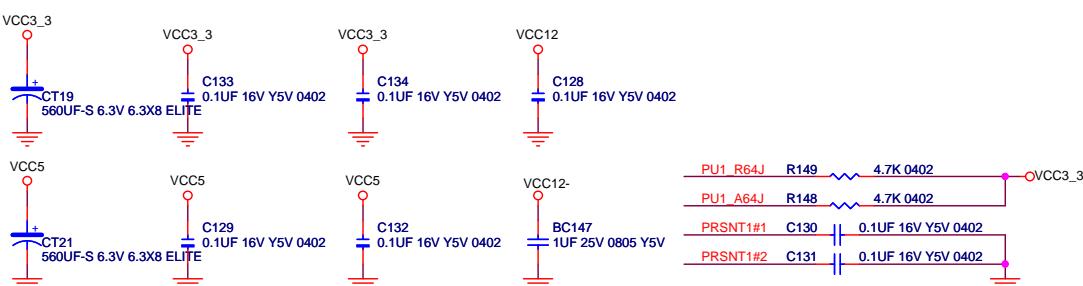


IDSEL:AD16 , INT:EFGH , REQ0 &amp; GNT0 , PCI\_CLK3

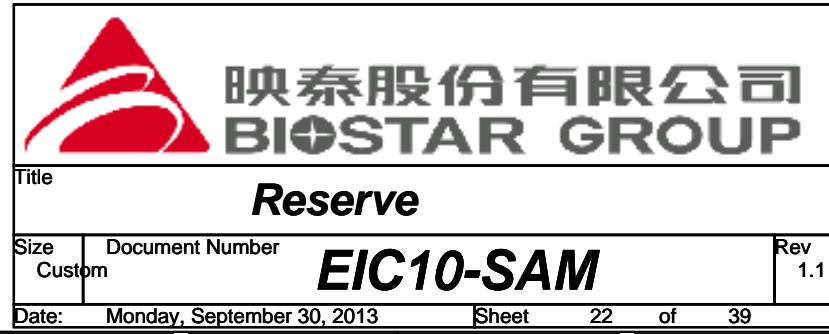
## PCI\_SLOT 1

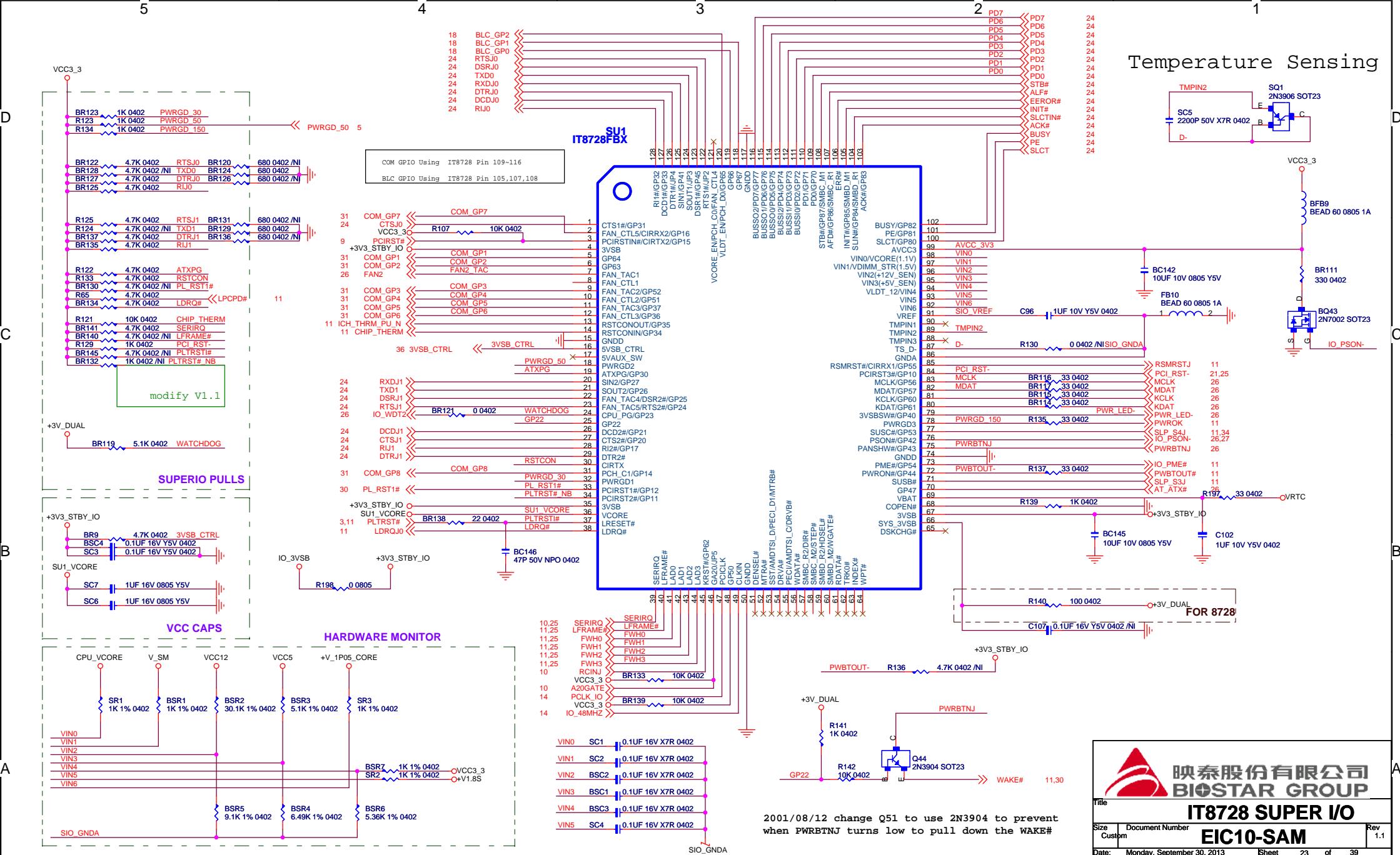


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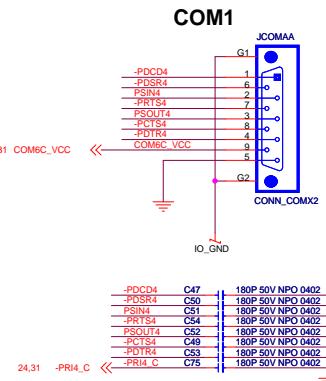
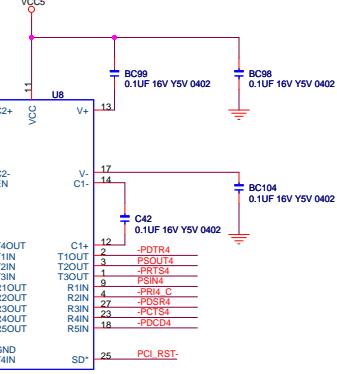
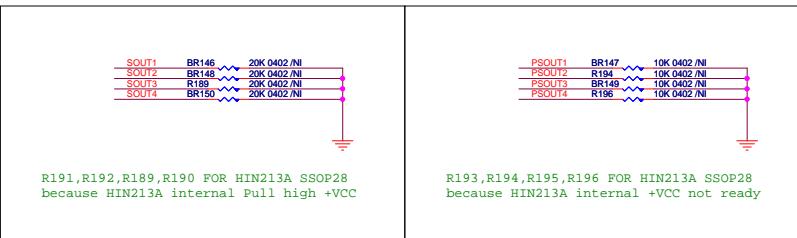
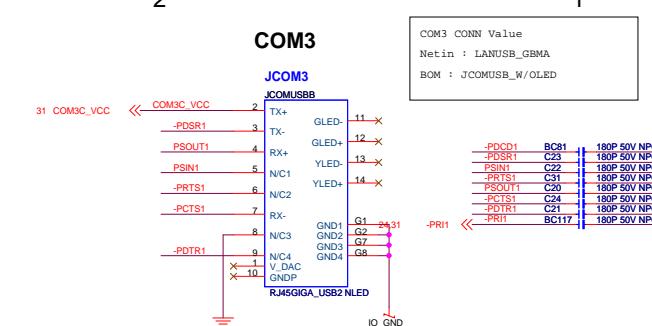
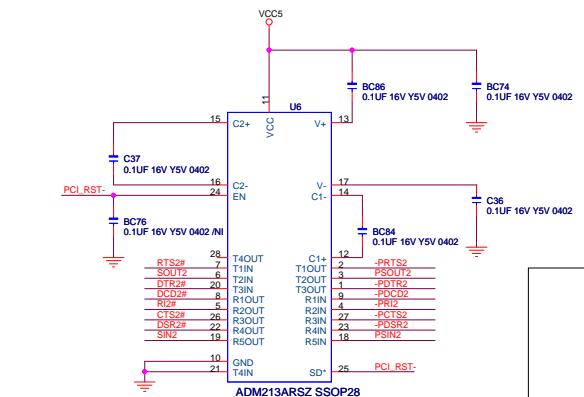
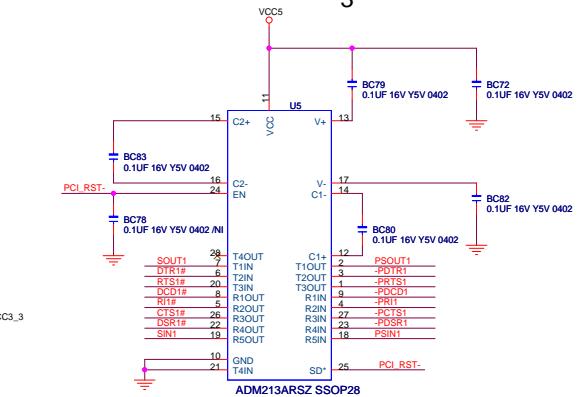
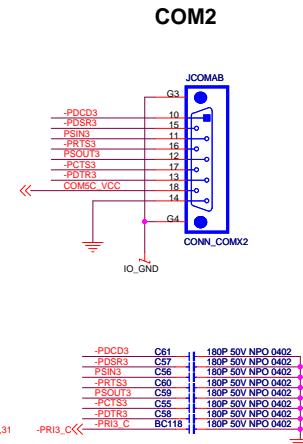
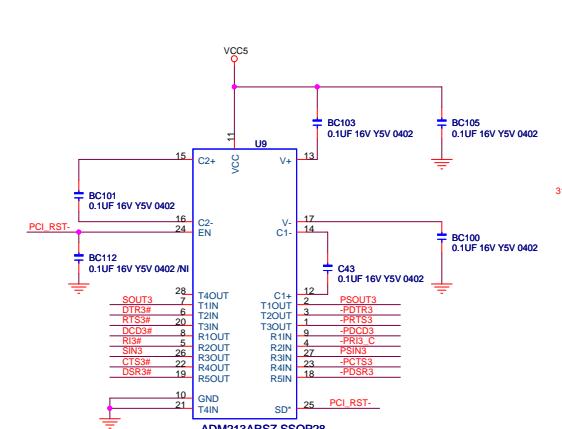
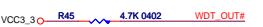
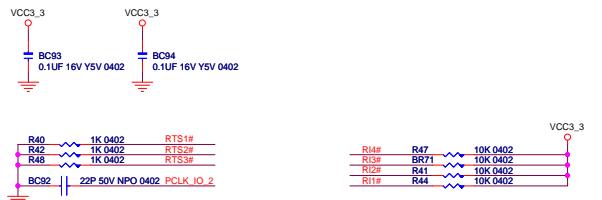
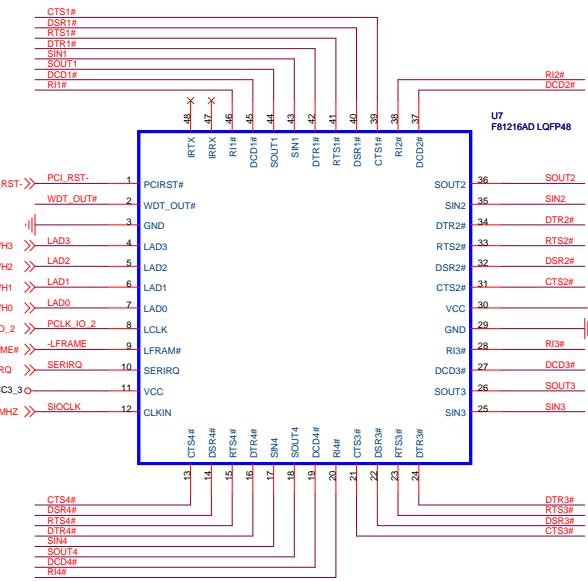


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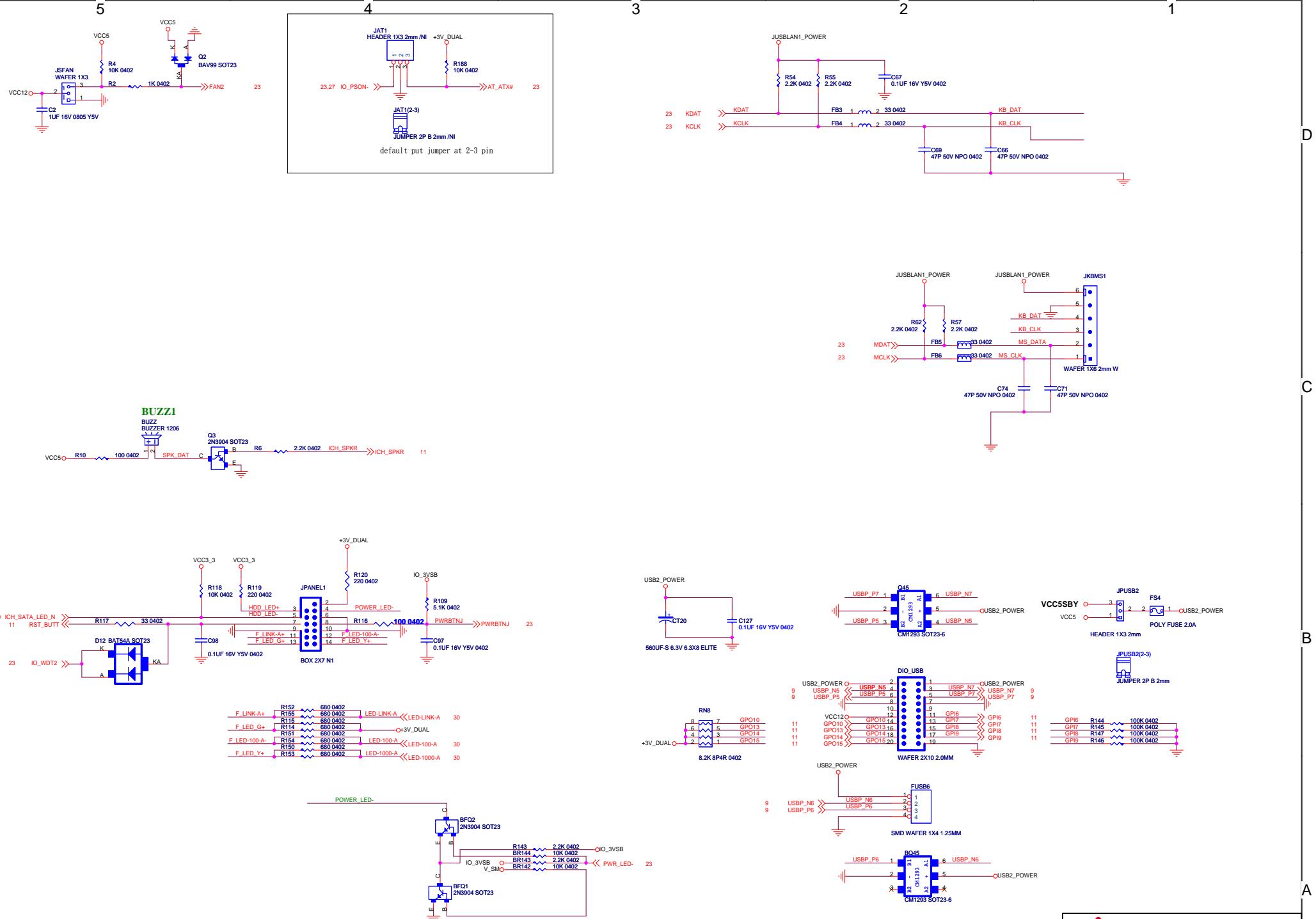


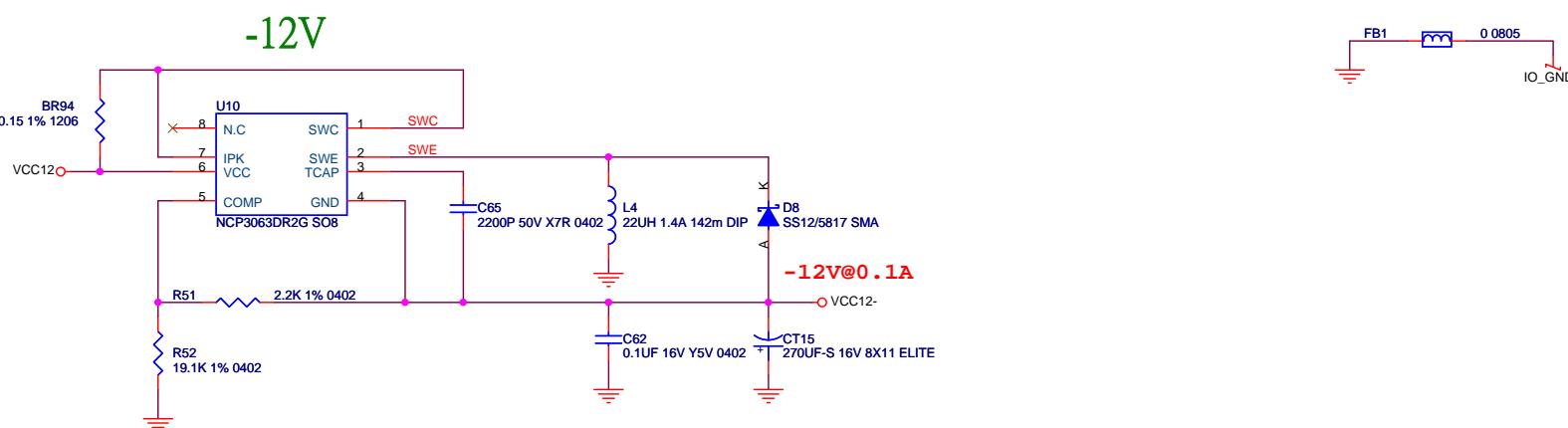
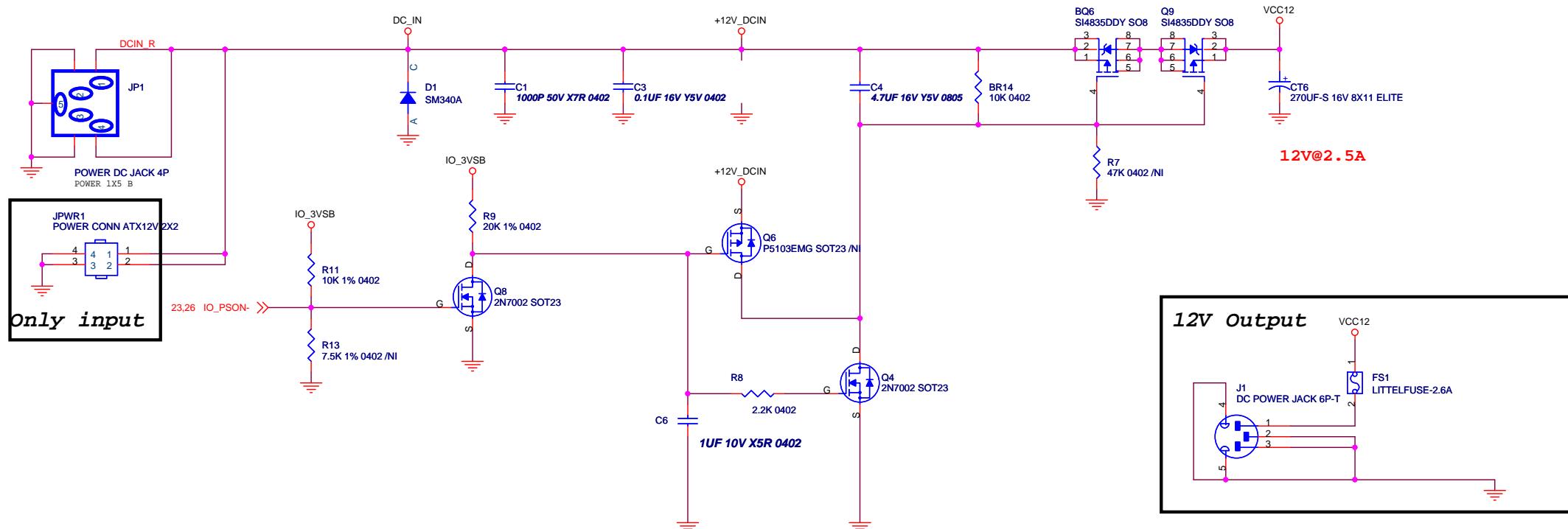


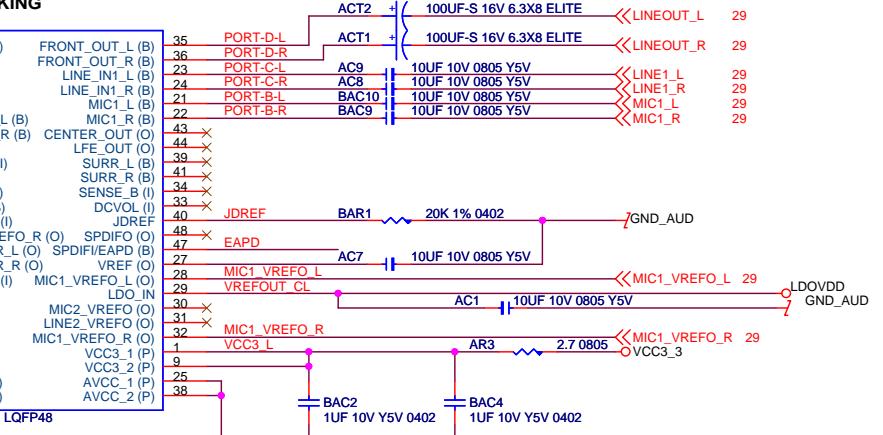
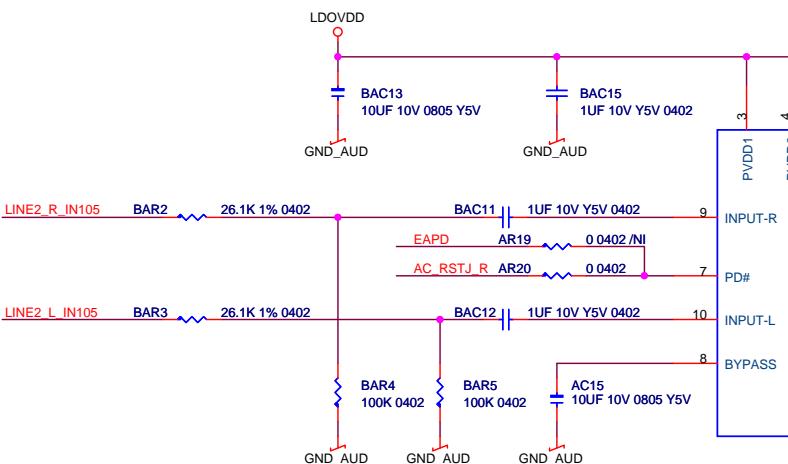
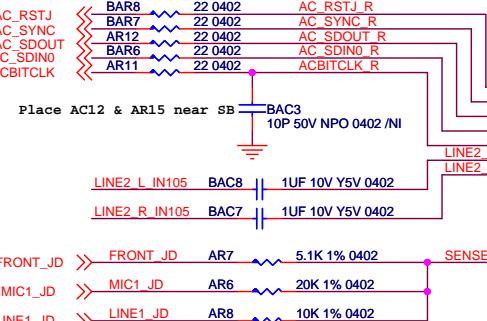
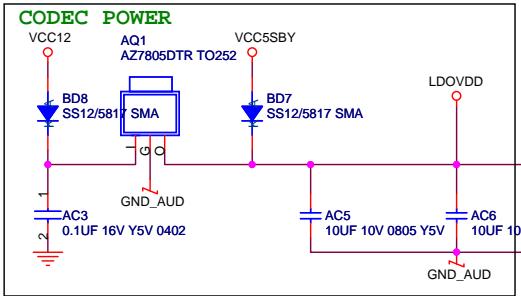
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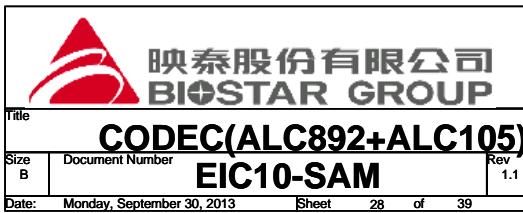
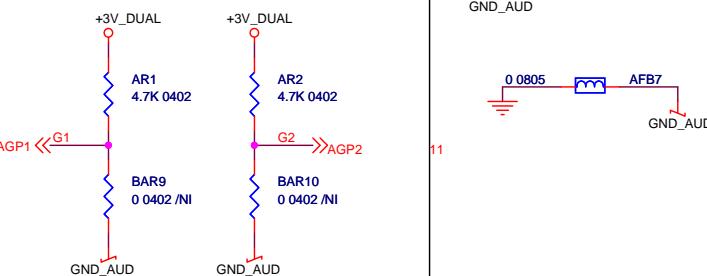


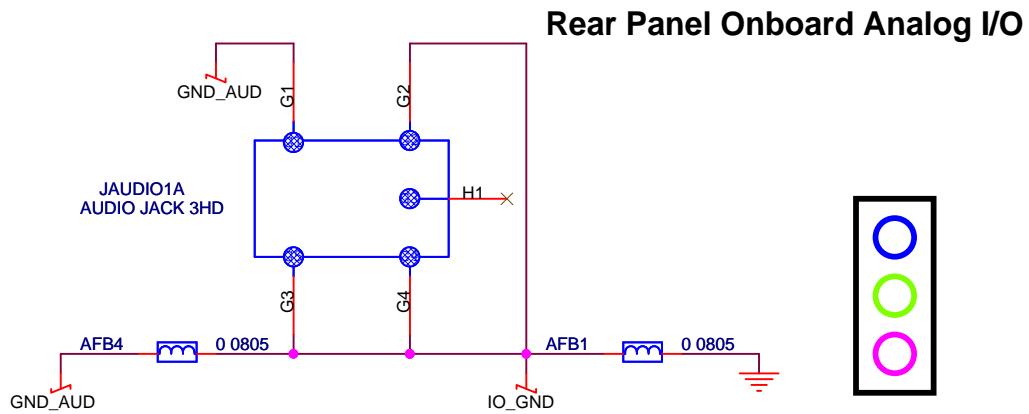
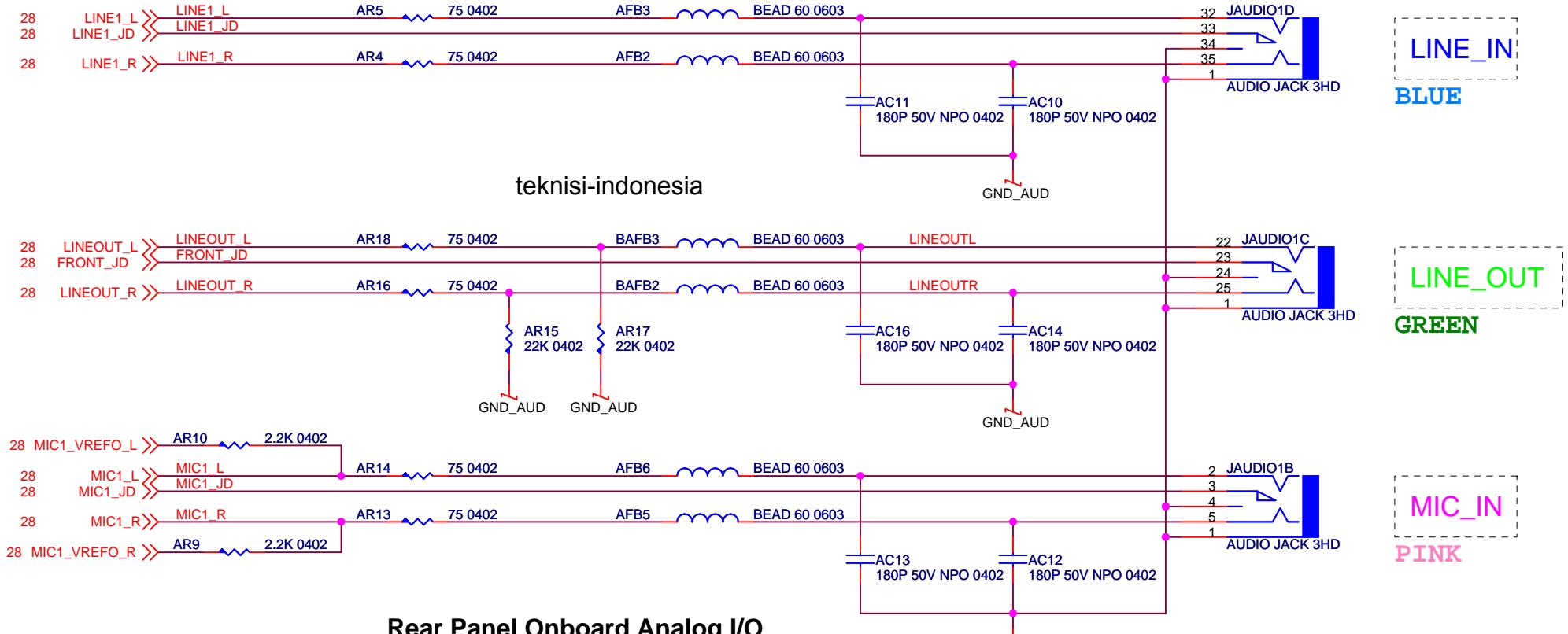


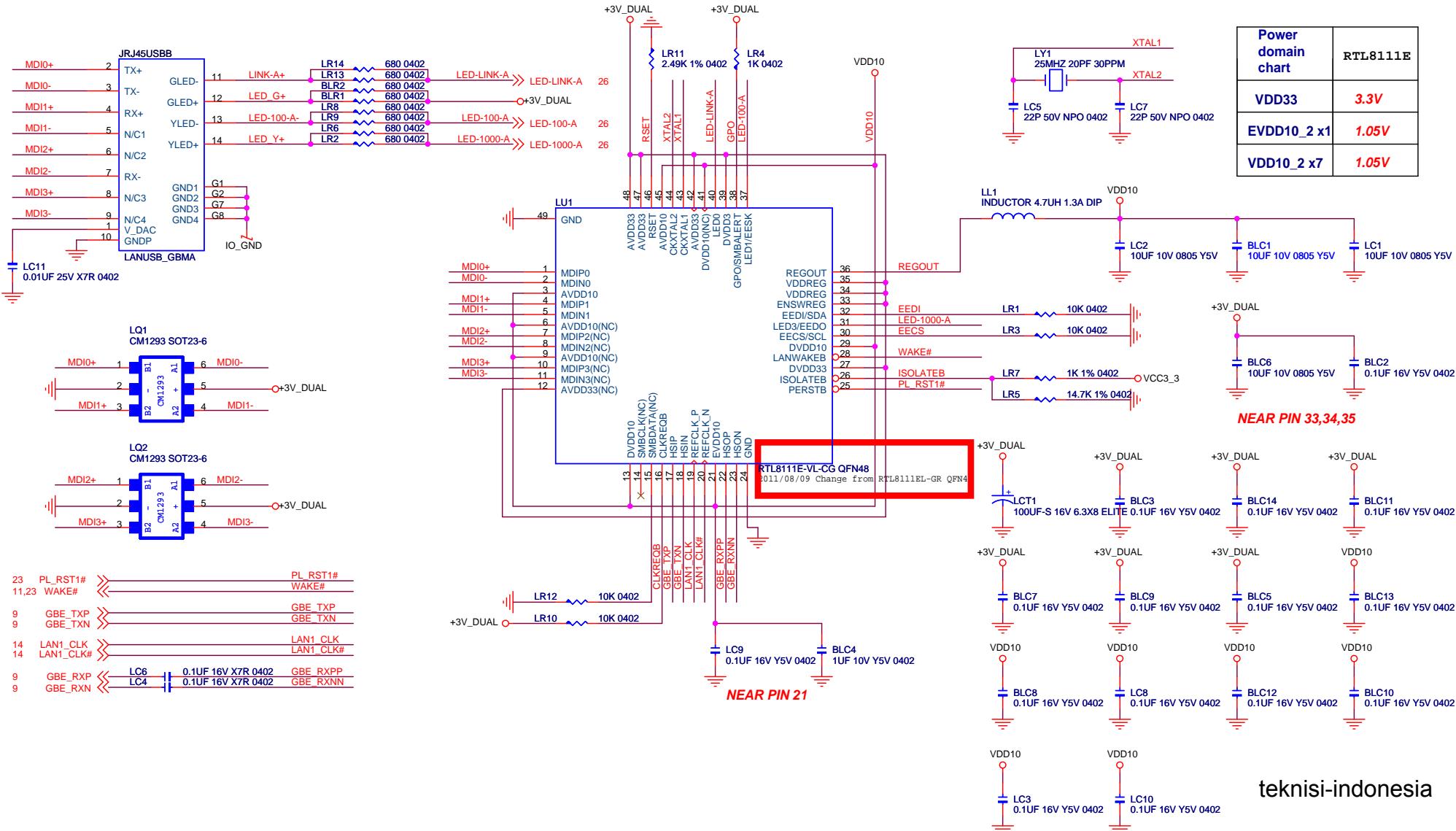


## Output Gain Table

G1	G2	Gain(single_end)	Gain(Differential)
0	0	5dB	11dB
1	0	8dB	14dB
0	1	13dB	19dB
1	1	19dB	25dB







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# REALTEK 8111E LAN

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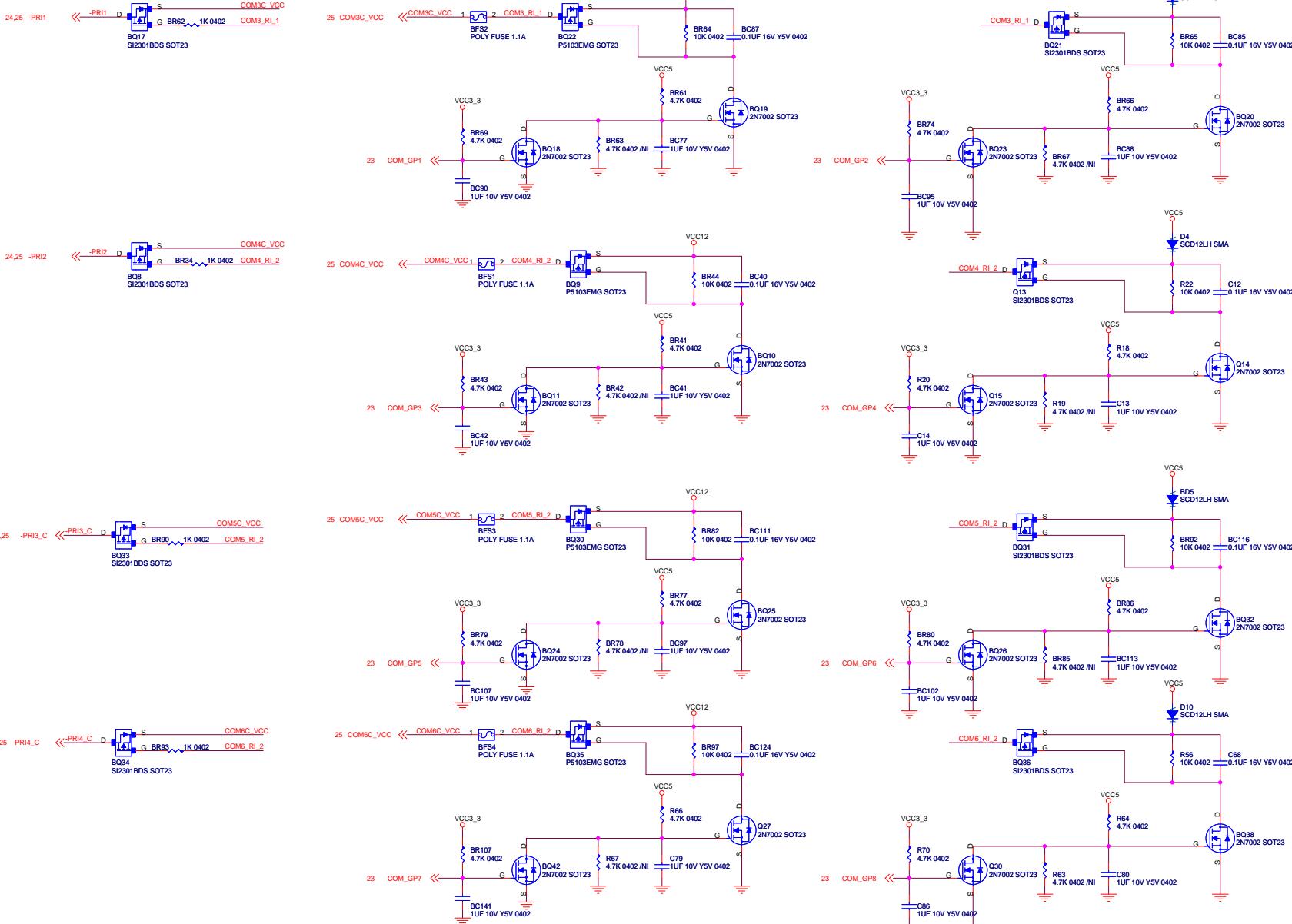
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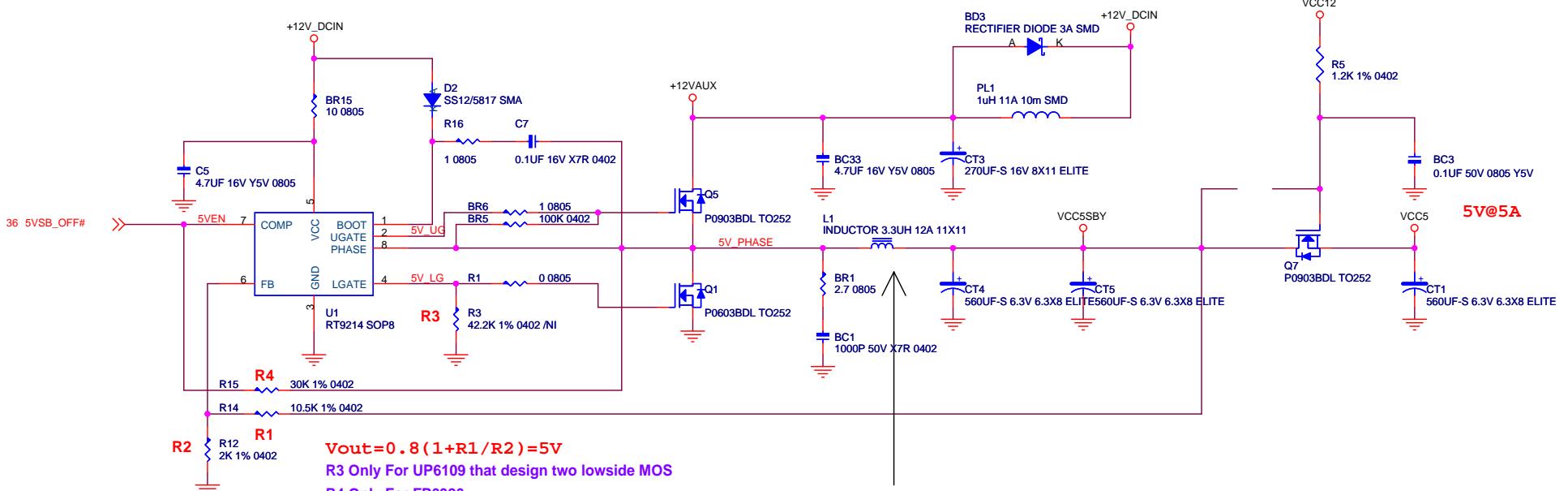
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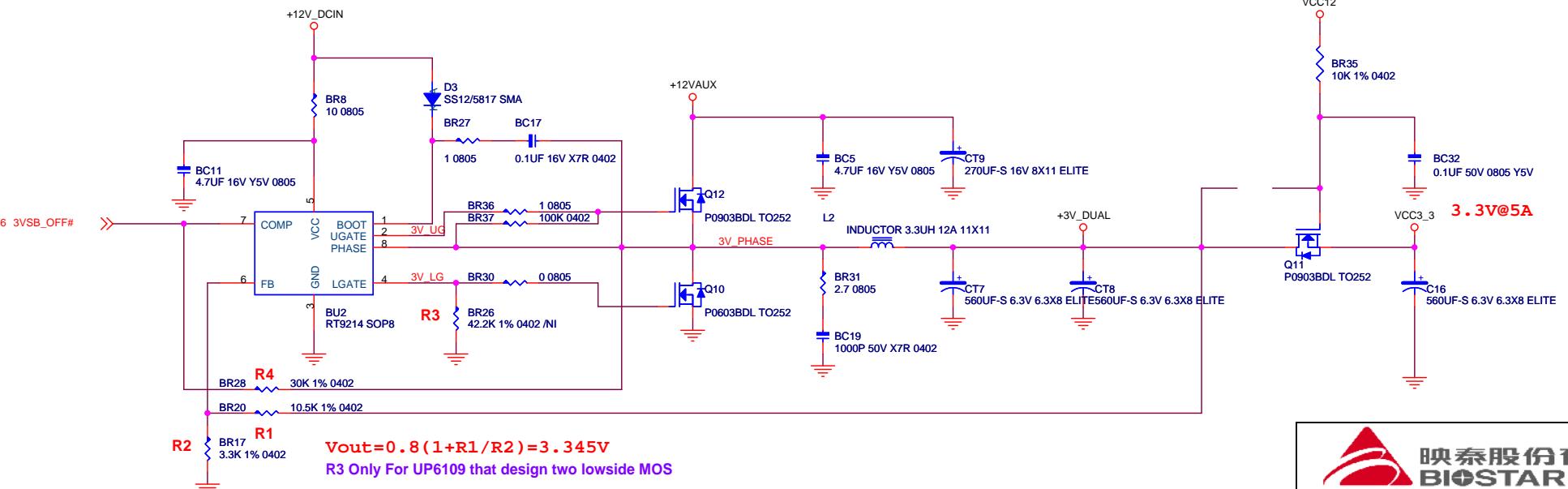
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	COM3		COM4		COM5		COM6	
	GP1	GP2	GP3	GP4	GP5	GP6	GP7	GP8
RI	High							
VCC12	Low	High	Low	High	Low	High	Low	High
VCC5	High	Low	High	Low	High	Low	High	Low





VALUE change @2011/08/08



Title		
Size B	Document Number	Rev 1.1
	<b>EIC10-SAM</b>	

Date: Monday, September 30, 2013

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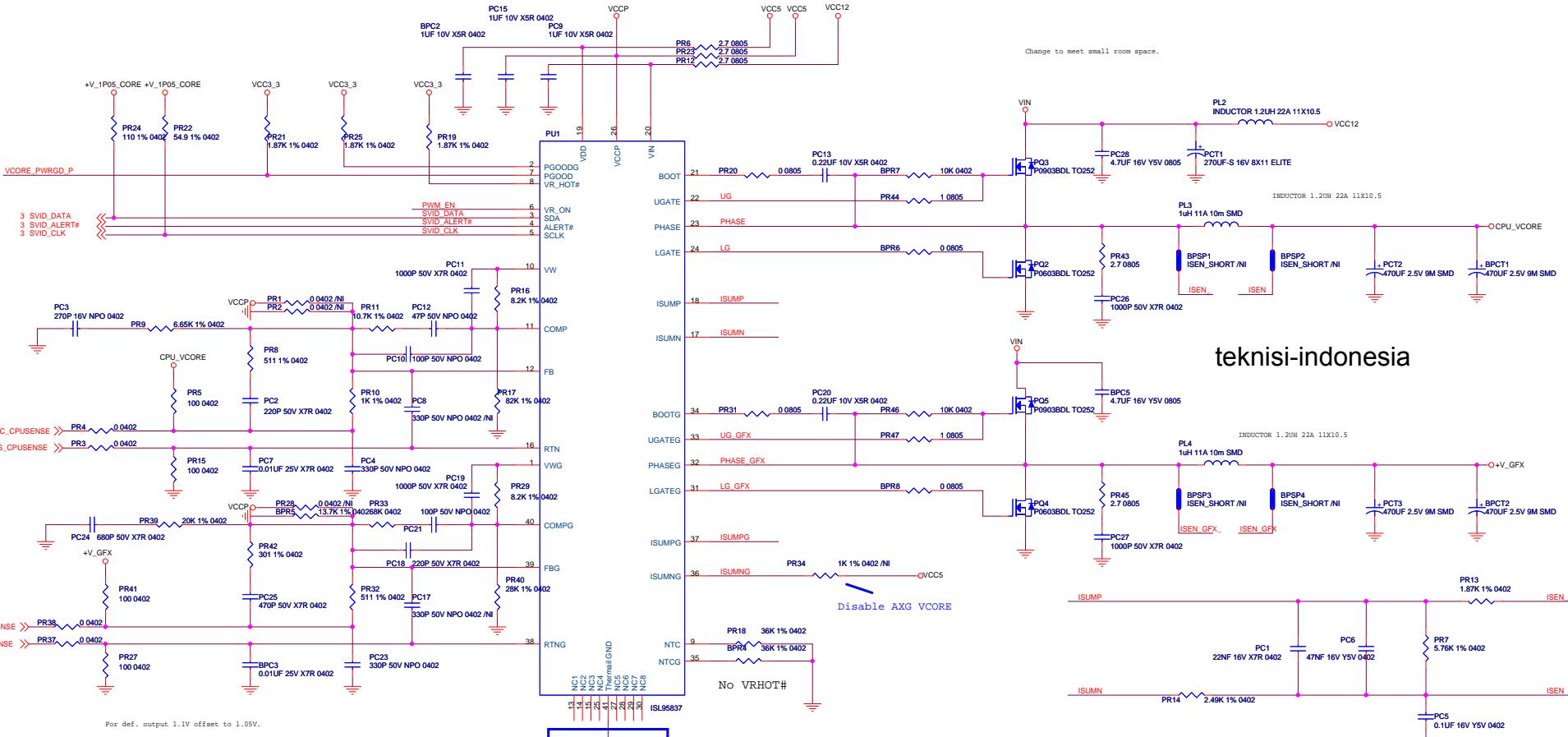
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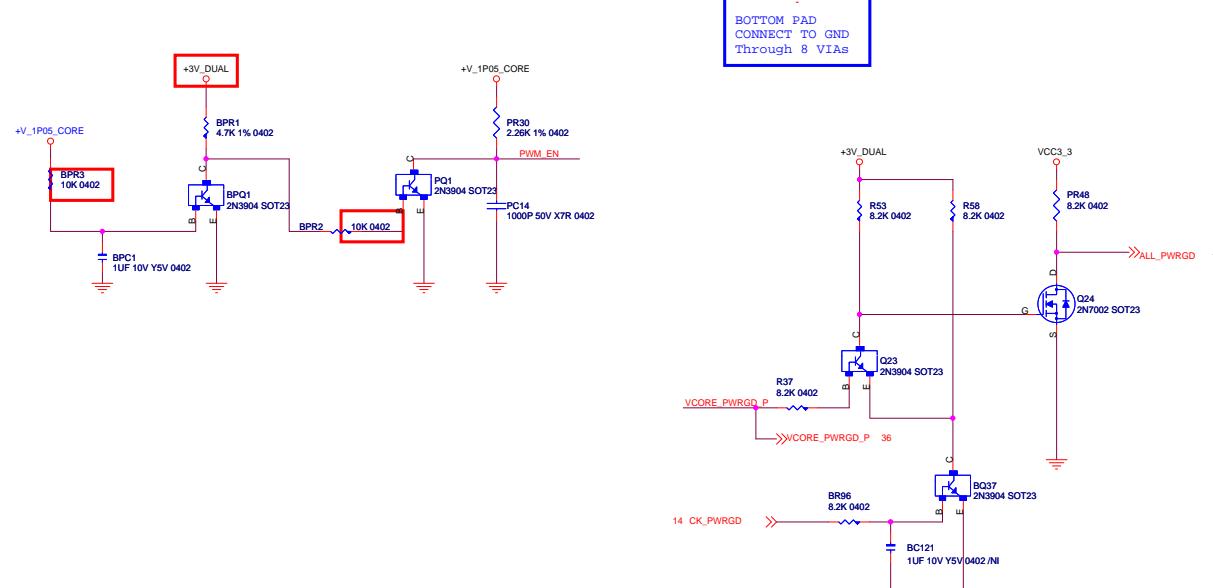
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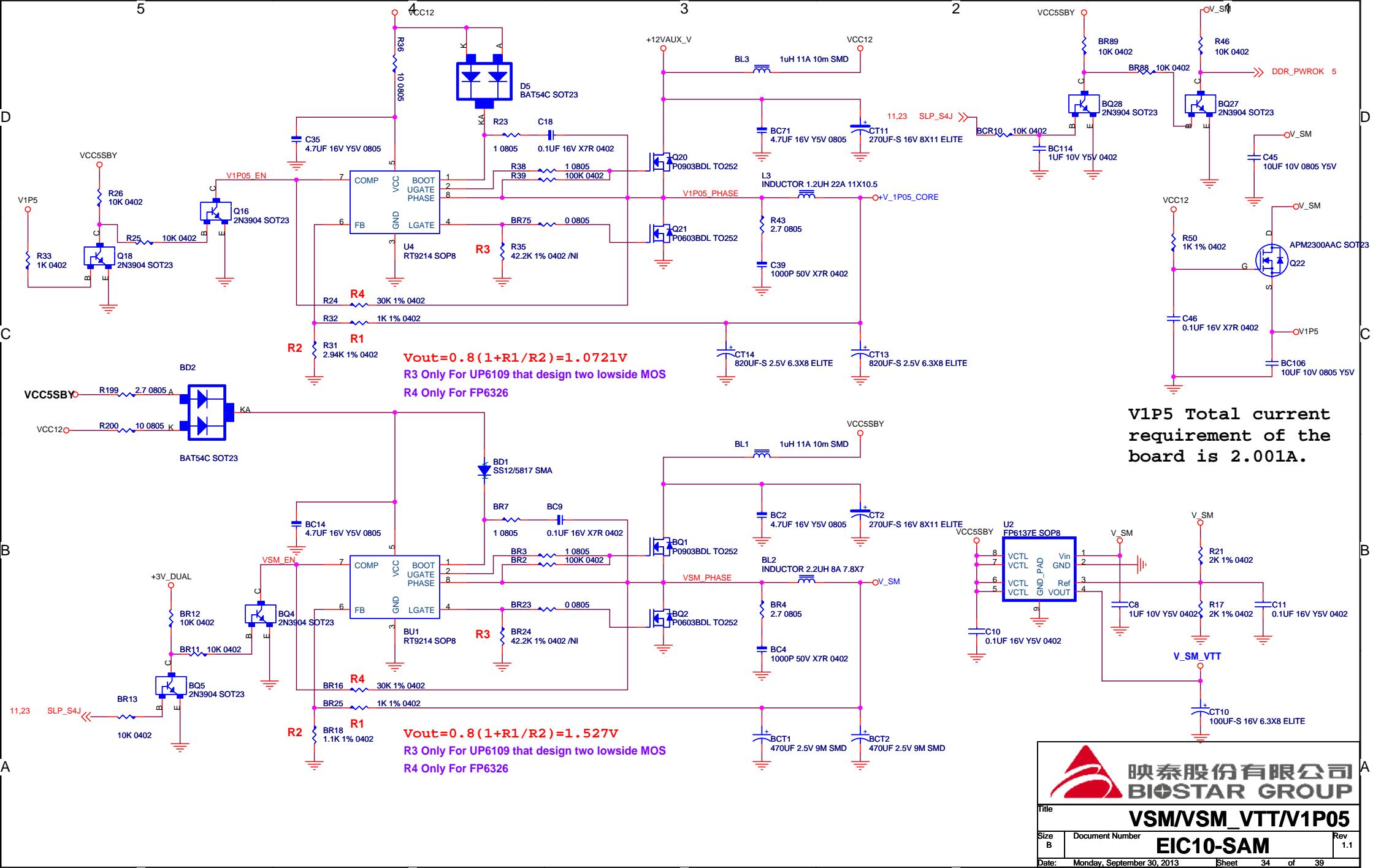
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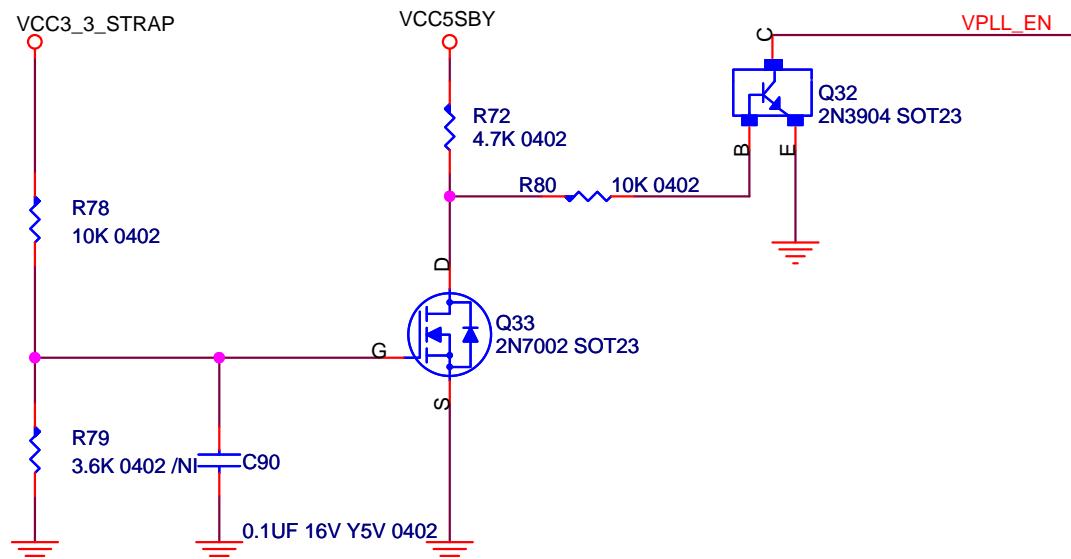
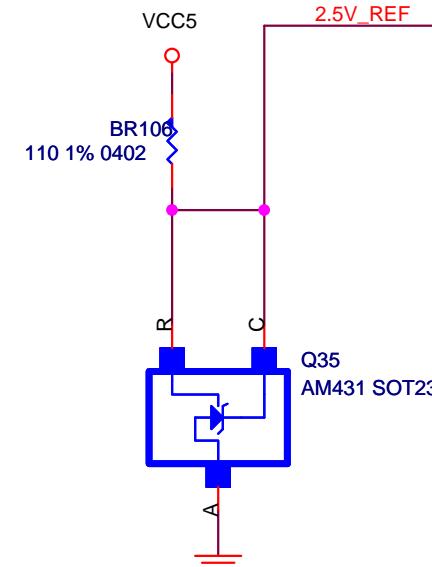
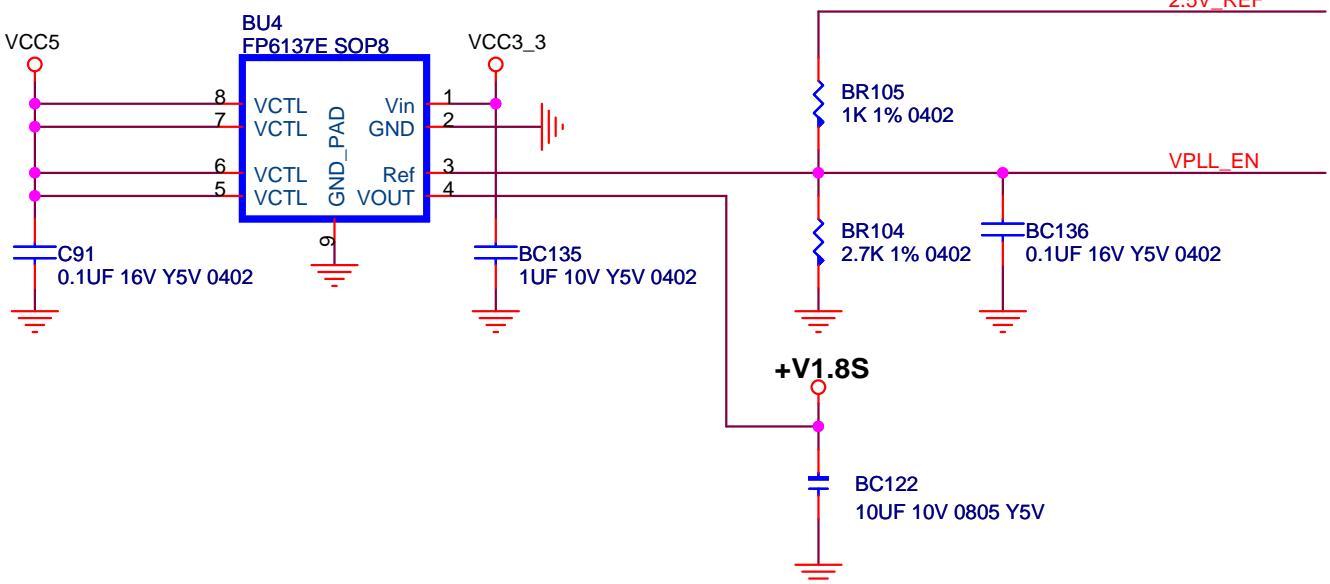
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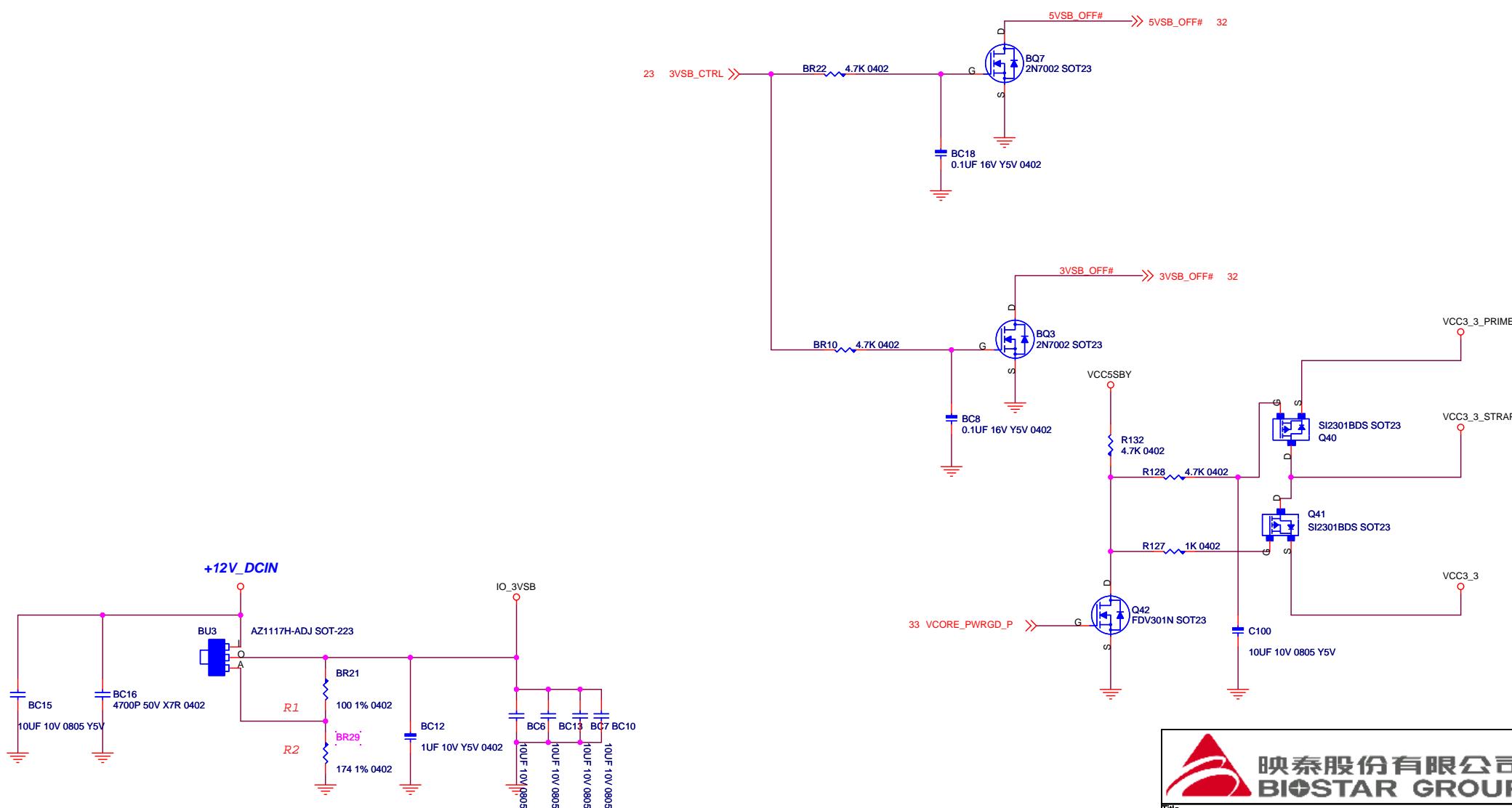


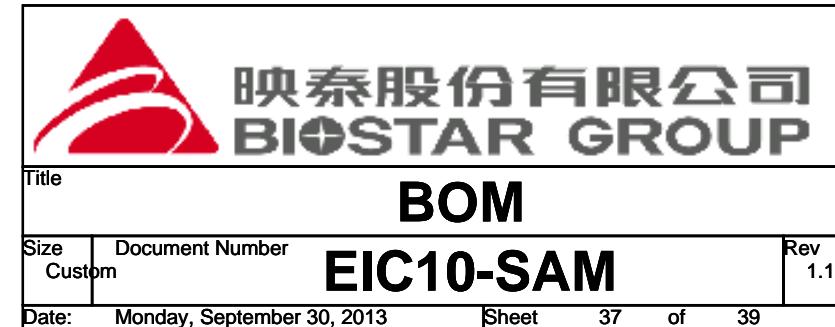
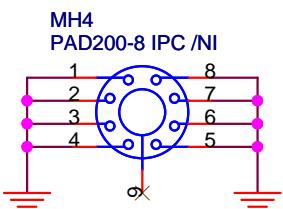
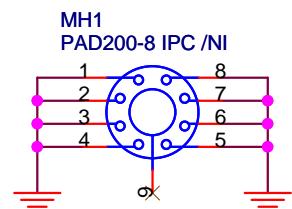
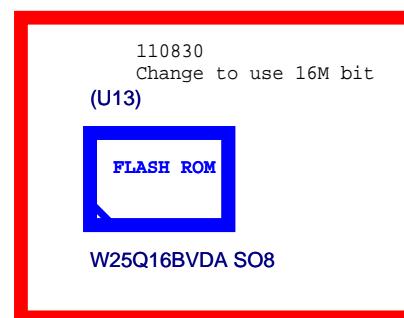
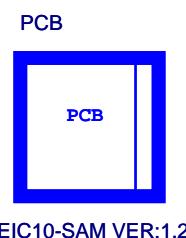
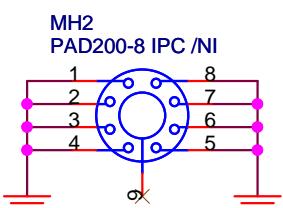
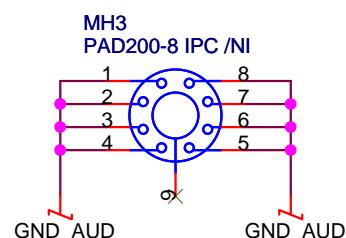
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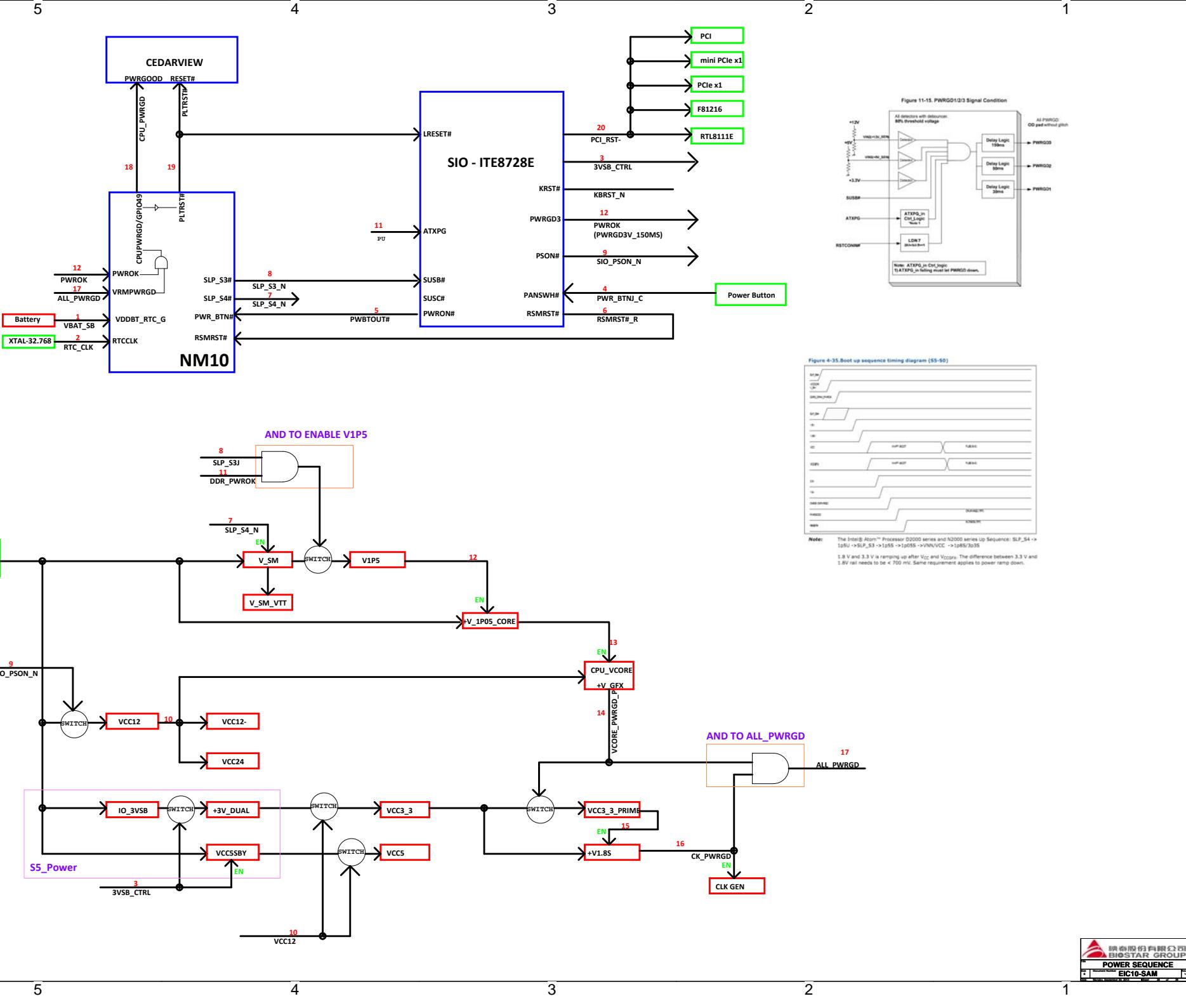
Size C Document Number EIC10-SAM Date: Monday, September 30, 2013 Sheet 33 of 39











v0.9 to V.095 Spec Update

Change SYSTEM PWR TO ISL9440B

Add SO-DDR3 Slot / DIMMA2

Real COM Port 5V/12V/RI select by GPIO

Audio Amplifier select by GPIO

LVDS Connector 40P change to 30P

One SATA Port Change to CFast header FOR Custormer

Internal COM Port change to 20P BOX Header FOR Custormer

Internal DIO and USB change to 20P BOX Header FOR Custormer

Add Single USB pin header

