

A B C D E

COMPAL CONFIDENTIAL

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MODEL NAME :

PCB# :DA40000RZ00

Revision :



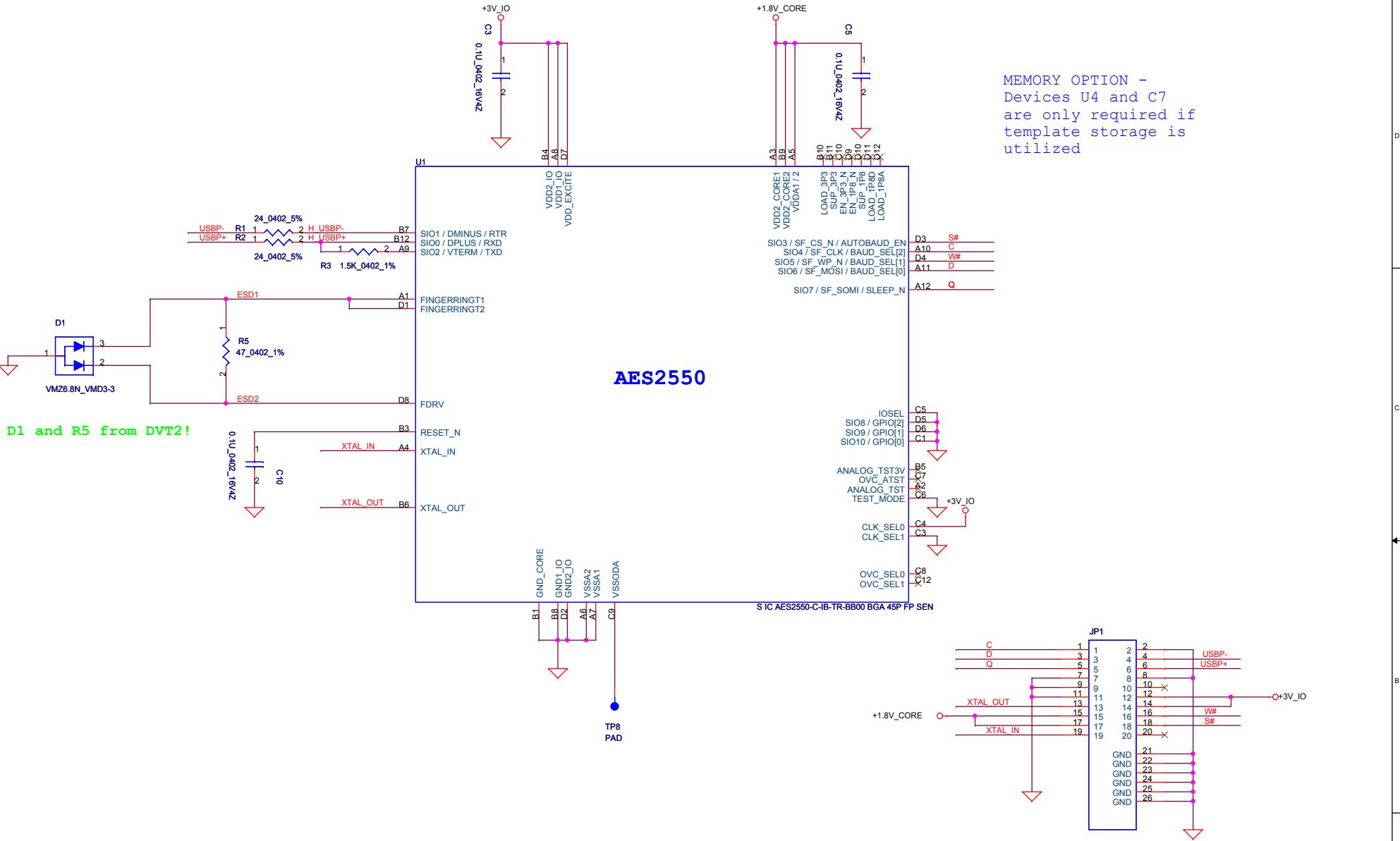
PCB 0D1 LS-6113P REV0 FPR/B

FPR Board Schematics

AuthenTec AES2550

REV 1.0 2010-06-02

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SYS FREQUENCY	6MHz	12MHz	RESERVED	RESERVED
SYSCLESEL[1:0]	00	01	10	11

LAYOUT NOTES:
 1. Critical nodes, listed by signal name and in order
 of importance: 1) DRIVE_RING1, 2) FDRV, 3) thick trace (20 mils) 4) +3V_OVC_VDDA, 5) +3V_FRP_VDD
 2. Place R3 close to AES1610 pads D6 and D7.
 3. For U3, See AuthenTec Layout Guidelines

FD1 Fiducial_c40m80 FD2 Fiducial_c40m80 FD4 Fiducial_c40m80 FD3 Fiducial_c40m80



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