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EIC10-SAM VER 1.2	
CPU	Cedarview
CHIPSET	NM10(TigerPoint-2W)
MEMORY	DDR3
SIZE	170mmx170mm

#### VER 0.9

(1)修改自 BIPV10-IAP VER 0.9

#### VER 0.95

(1)12V change to 24V

#### VER 1.0

(1)CFAST Change to SATA

(2)ISL9440B Change to FP6326 (5V,3V)

#### VER 1.1

(1)F81216DG Change to F81216AD

HIN213A Change to ADM213

#### VER 1.2

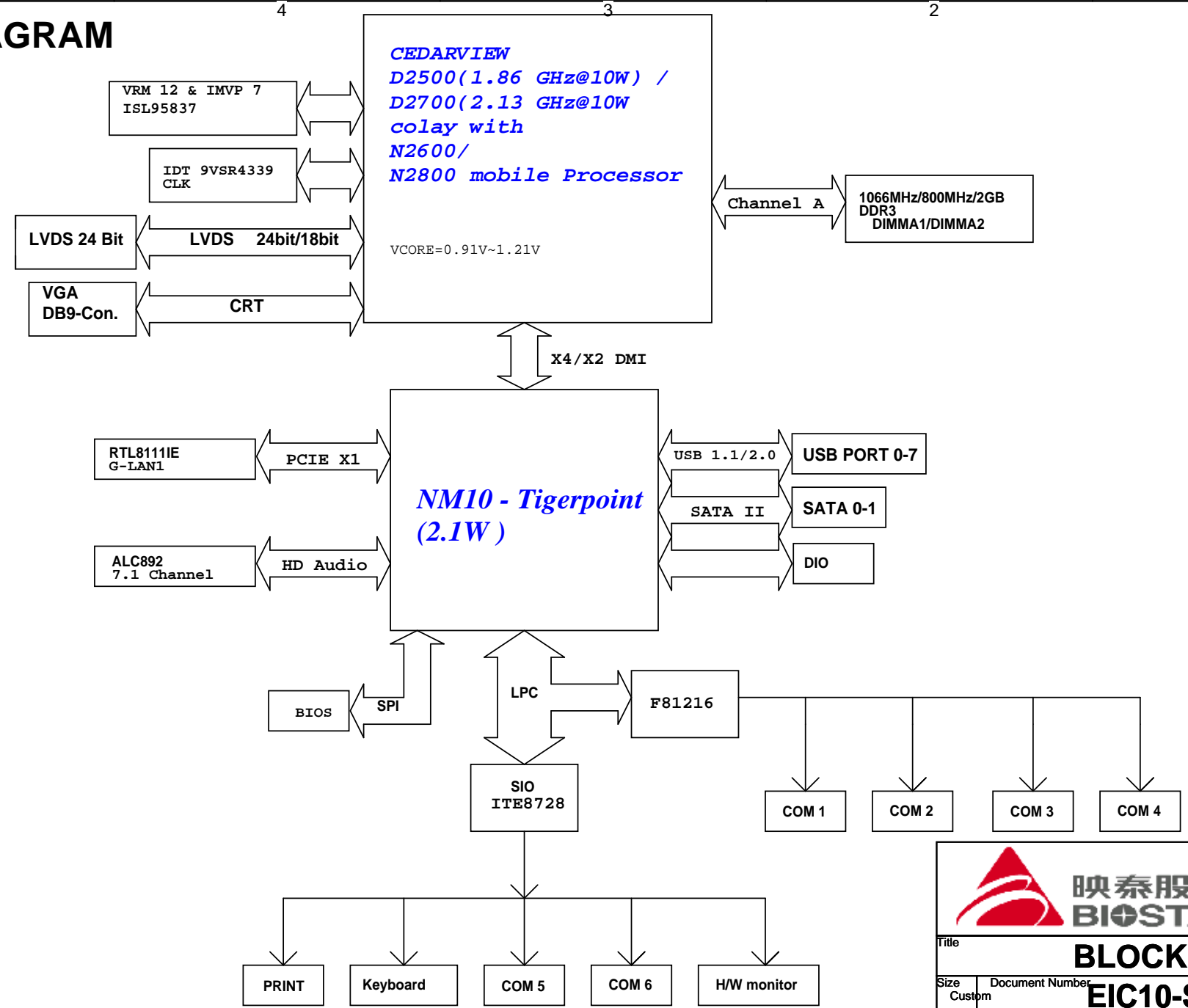
(1)change DC jack connector


(2)reserve EAPD circuit

(3)BQ15=FDV301N->2N7002

		
Title <b>Cover Sheet</b>		
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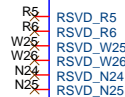
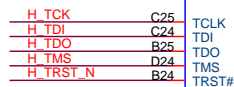
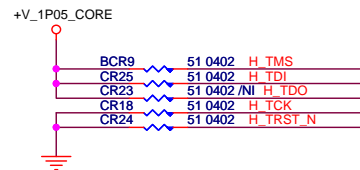
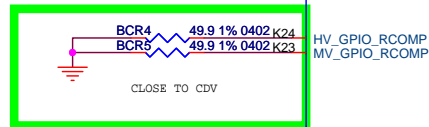
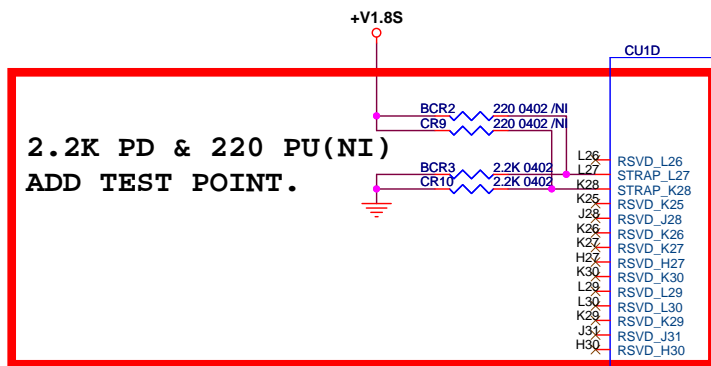
BLOCK DIAGRAM





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D2550\_1.86G 10W

?

CU1D

?

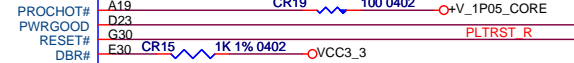
CEARVIEW

REV = 1.10

IC1

CPU

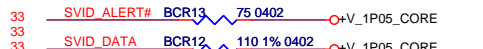
4 OF 6



PINEVIEW N.C

PROCHOT# NOT USE

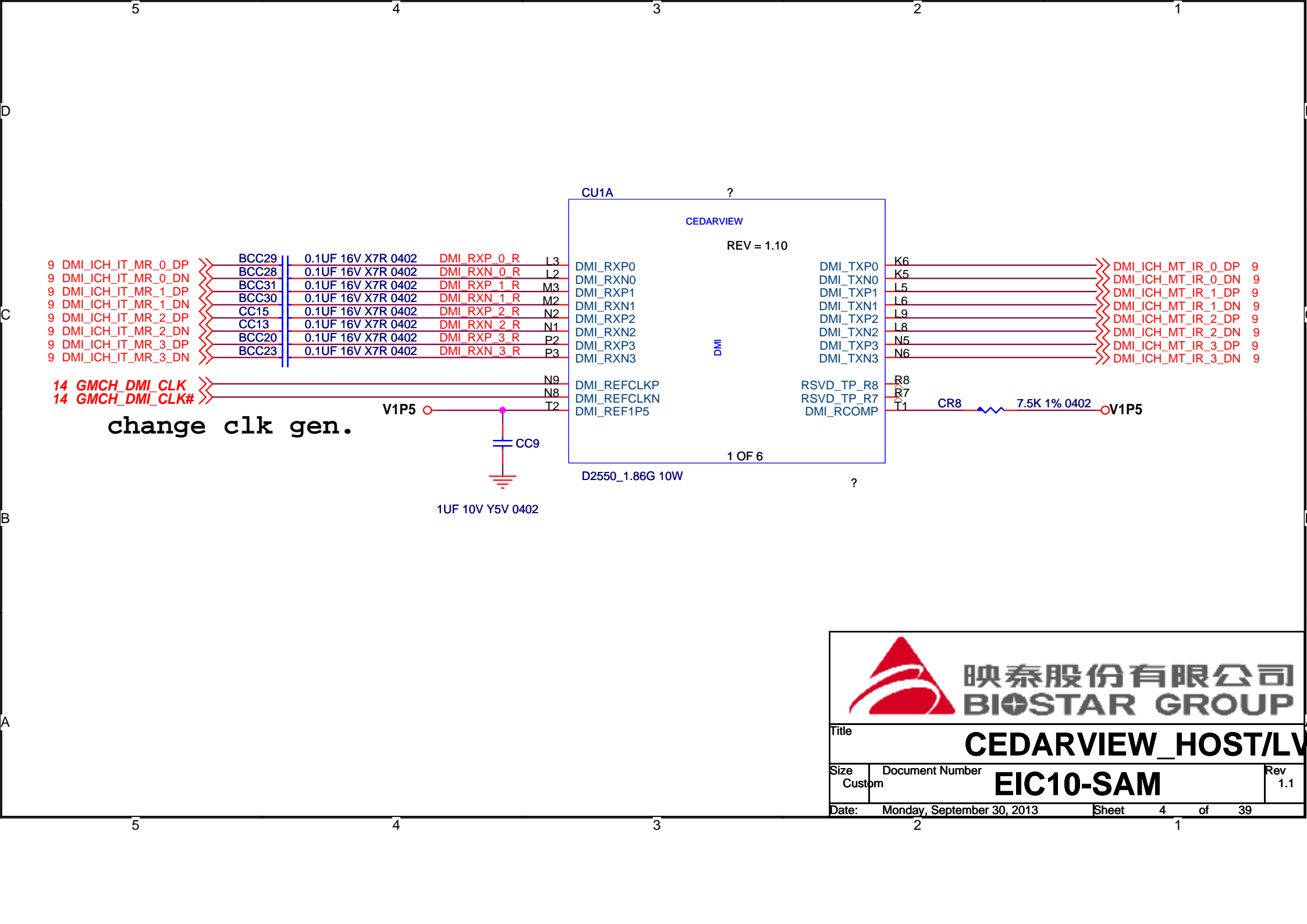
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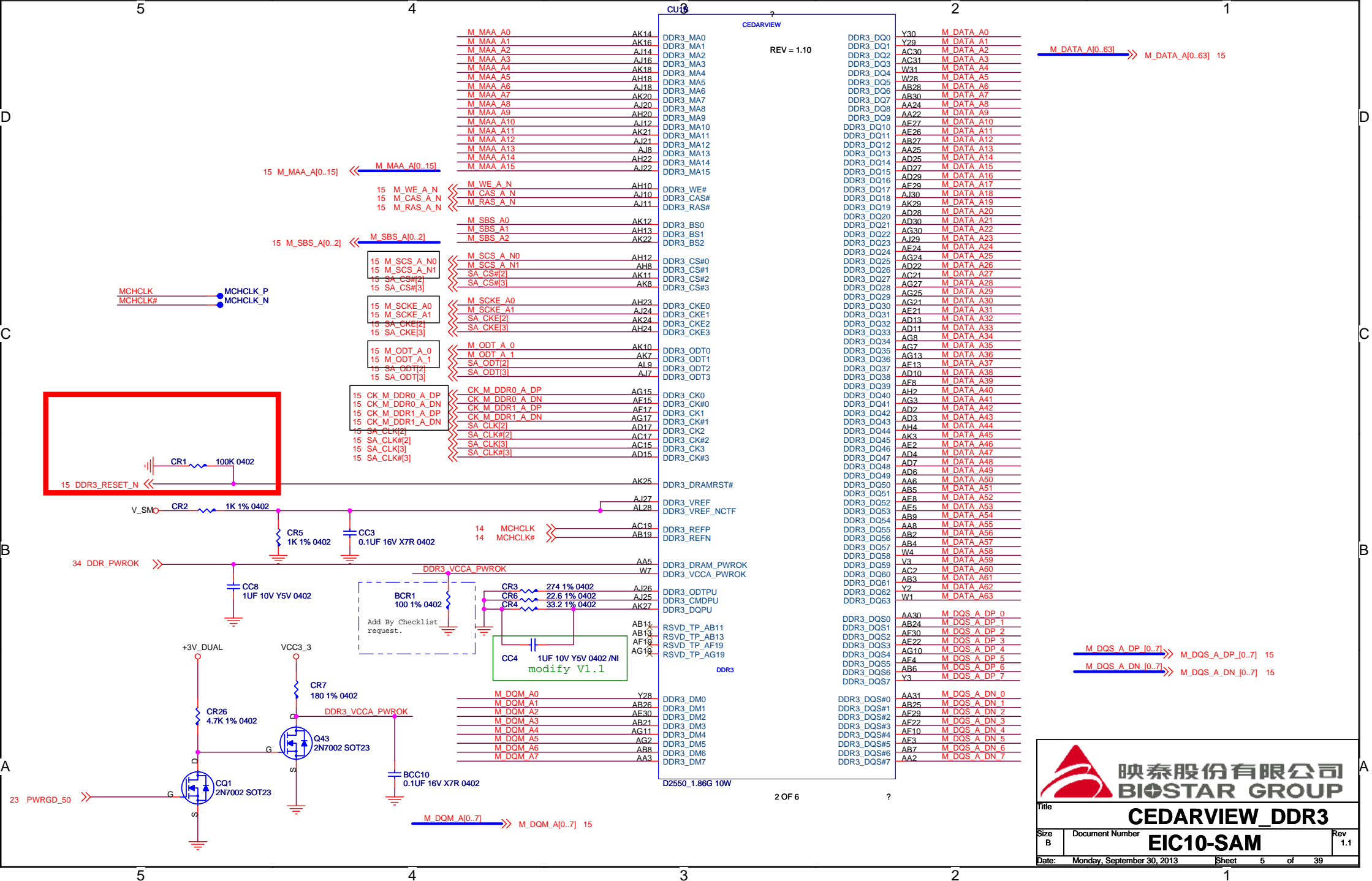


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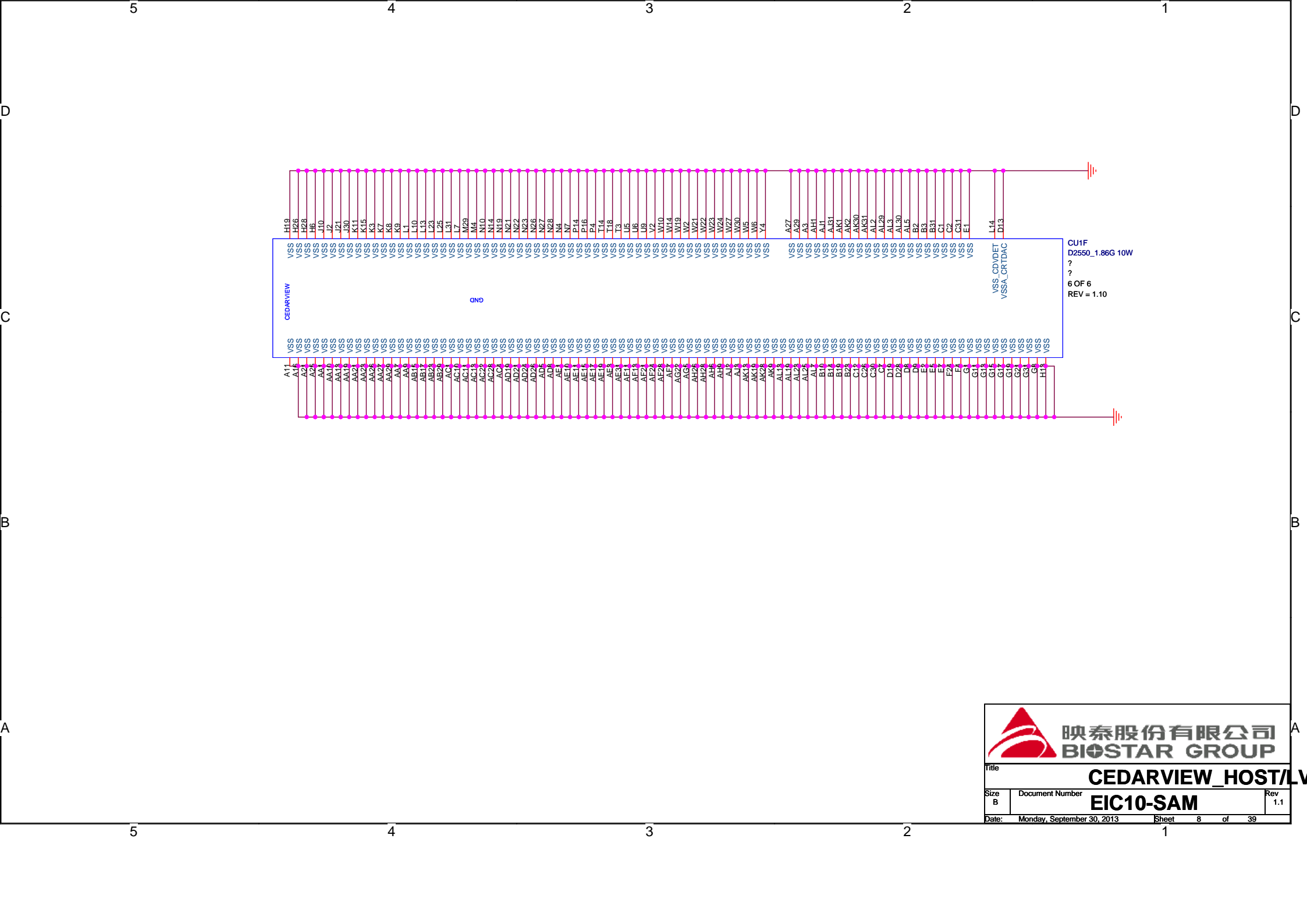
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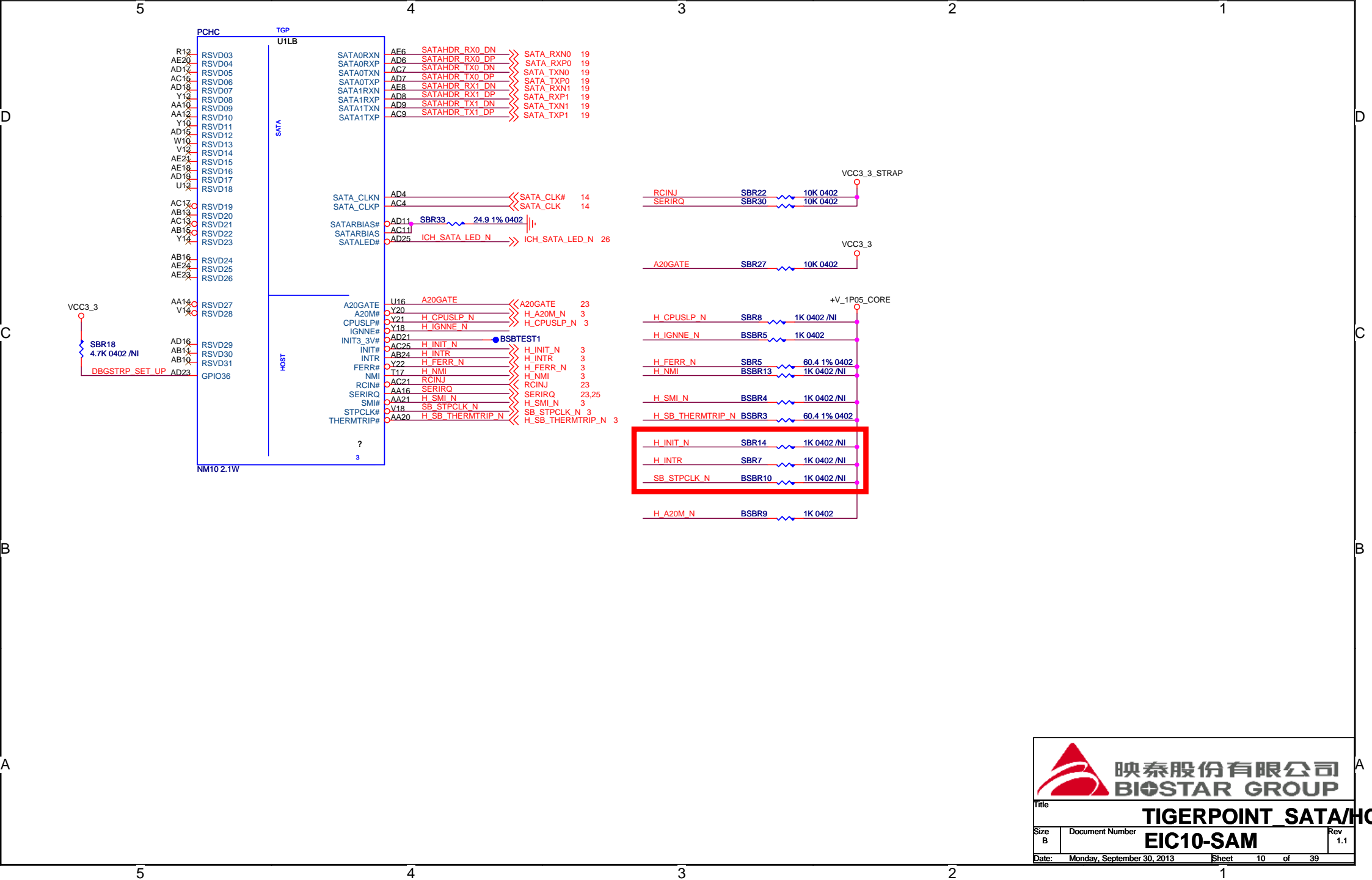




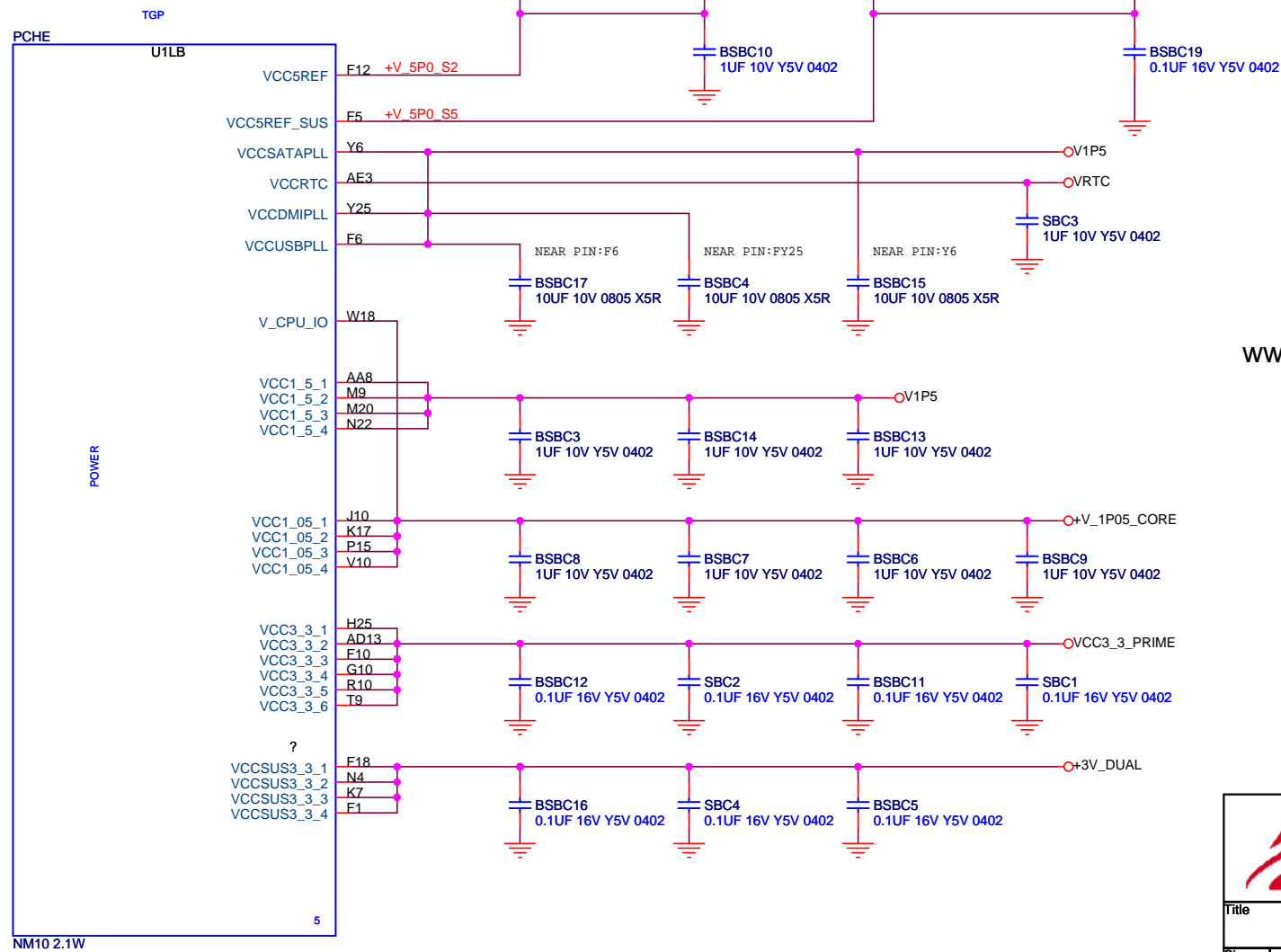











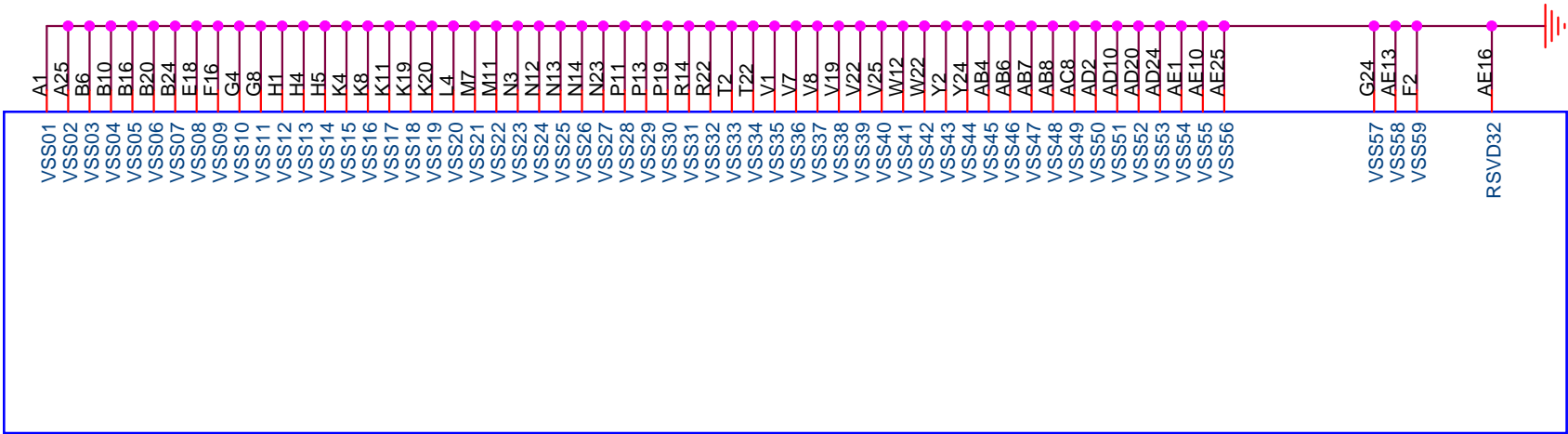


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<b>Title</b> <b>TIGERPOINT POWER</b>			
<b>Size</b> Custom	<b>Document Number</b> <b>EIC10-SAM</b>		<b>Rev</b> 1.1
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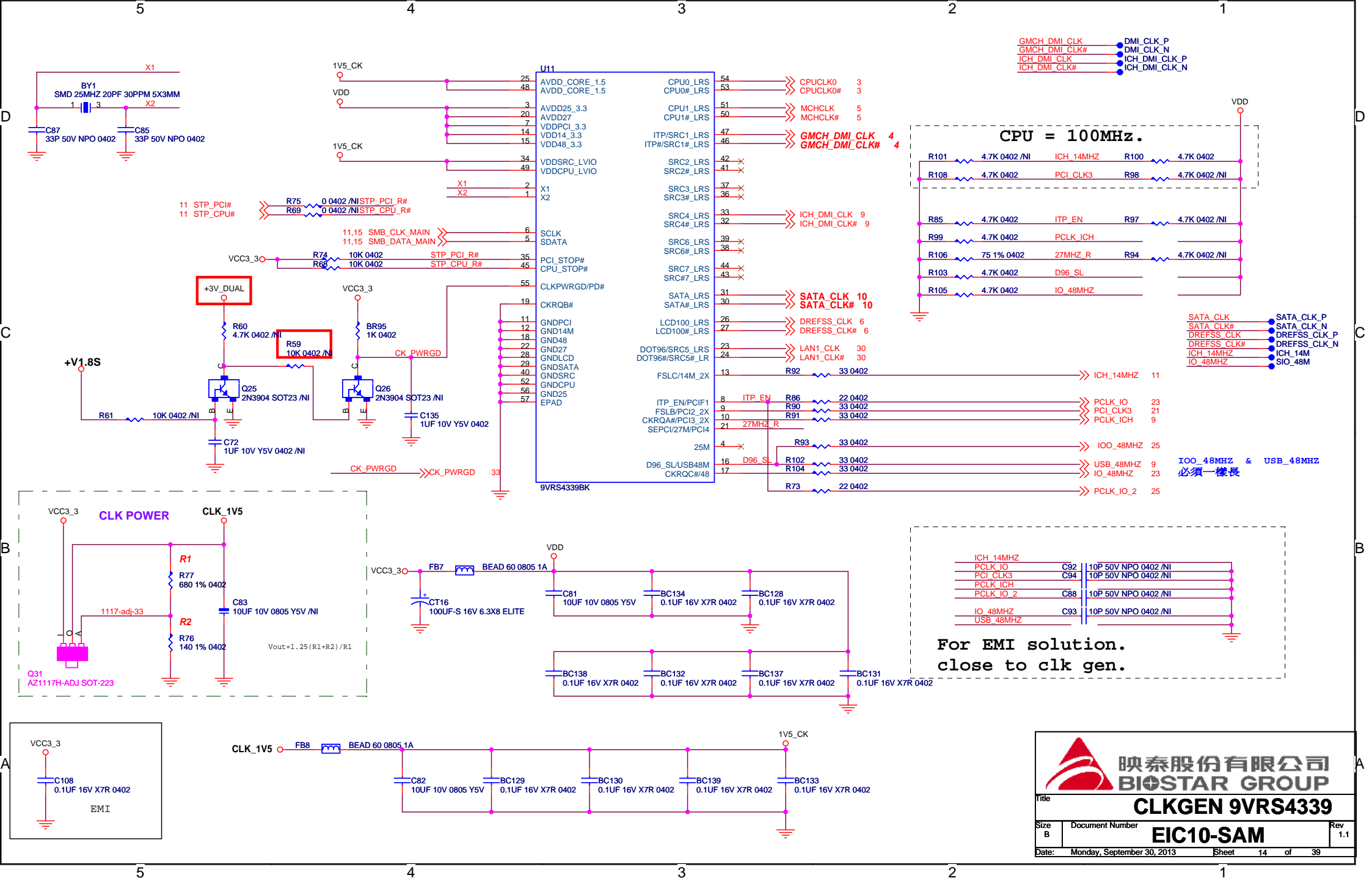
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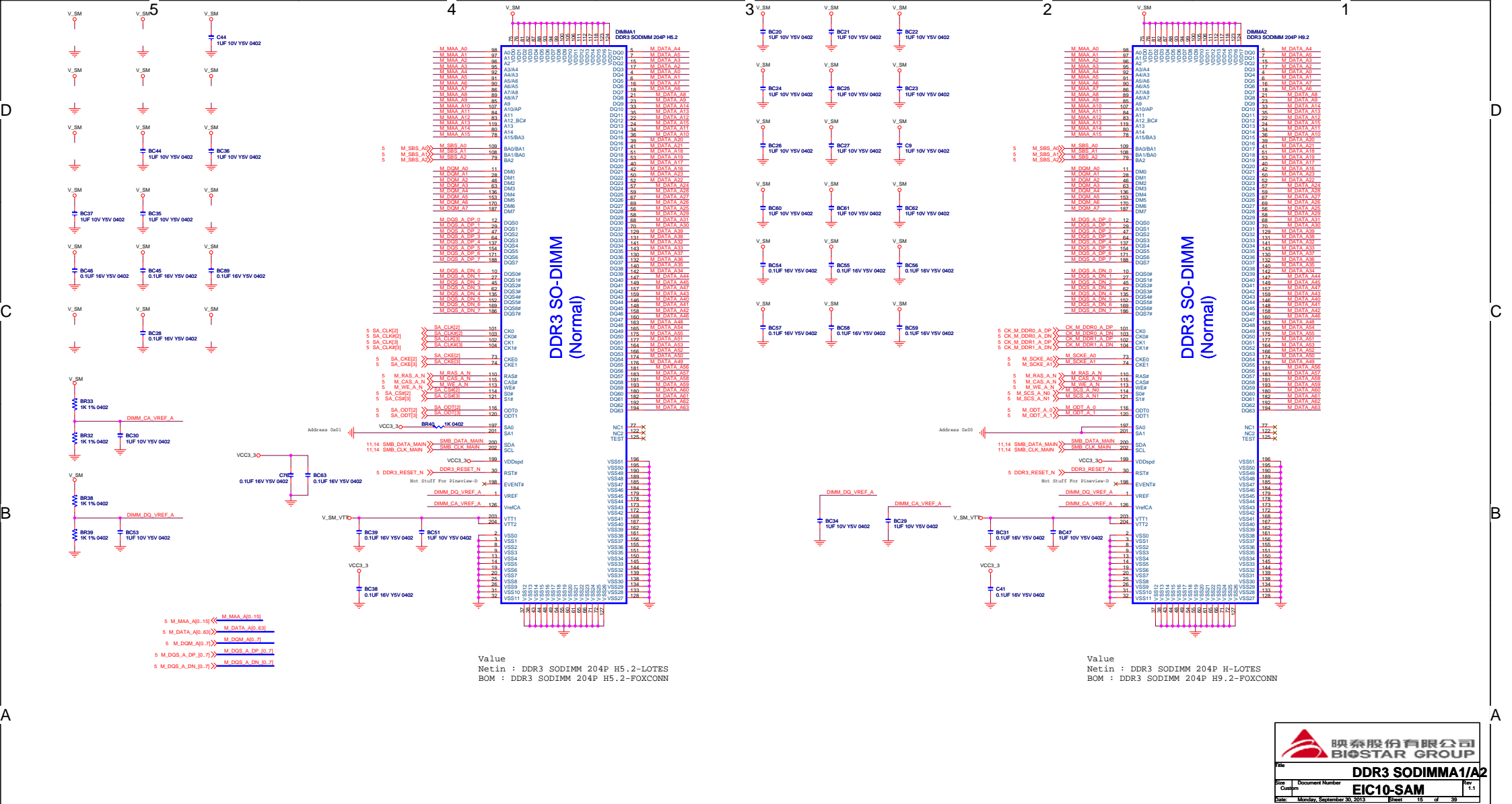
TGP



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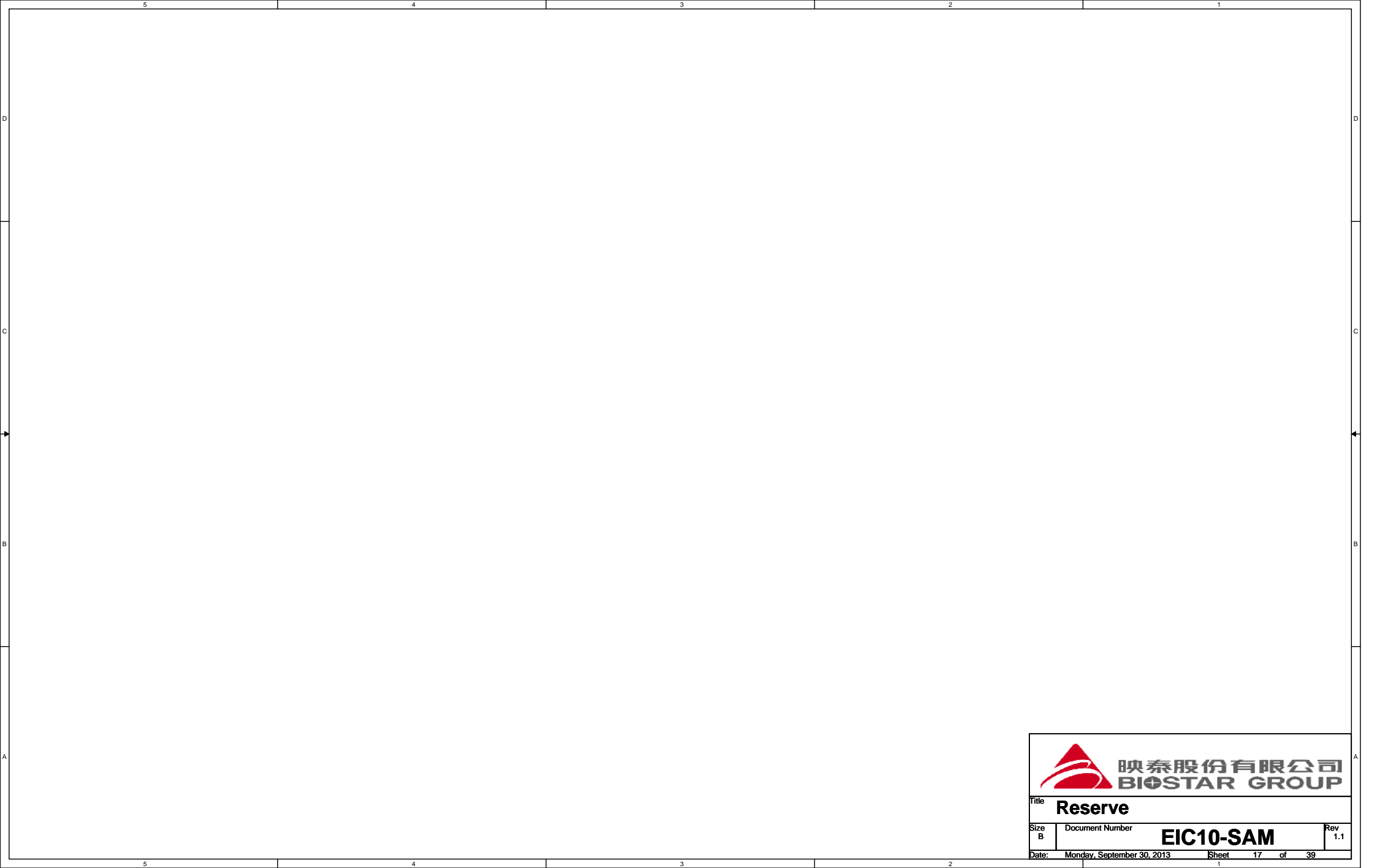
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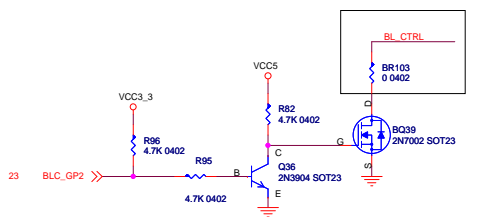
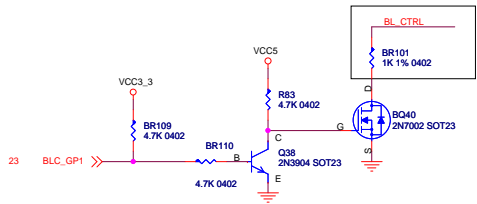
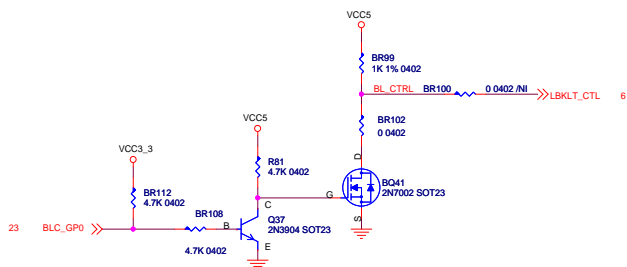




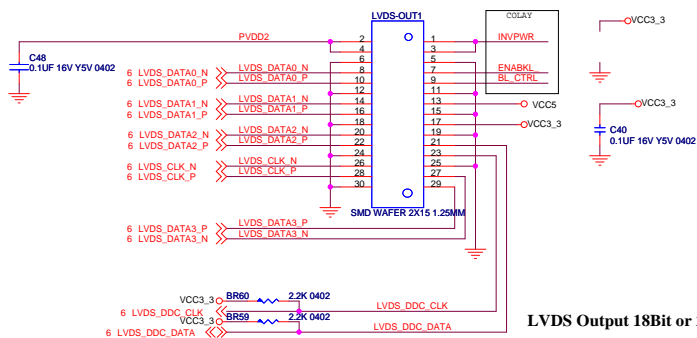
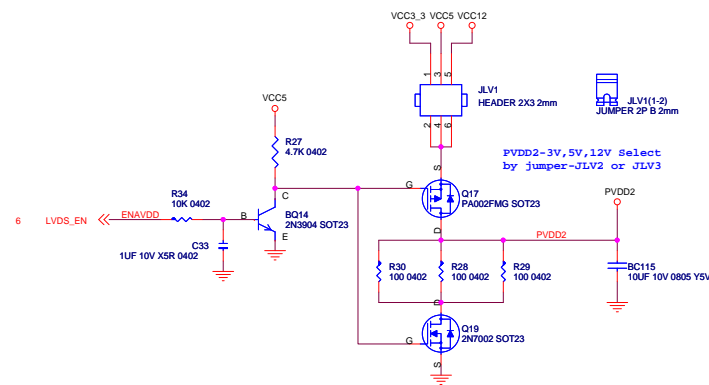
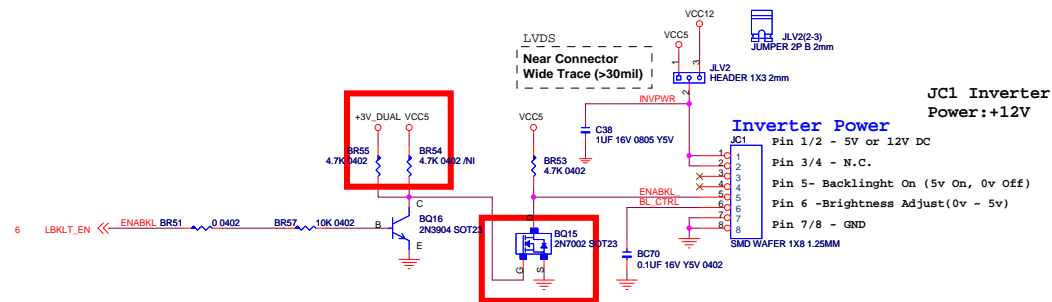




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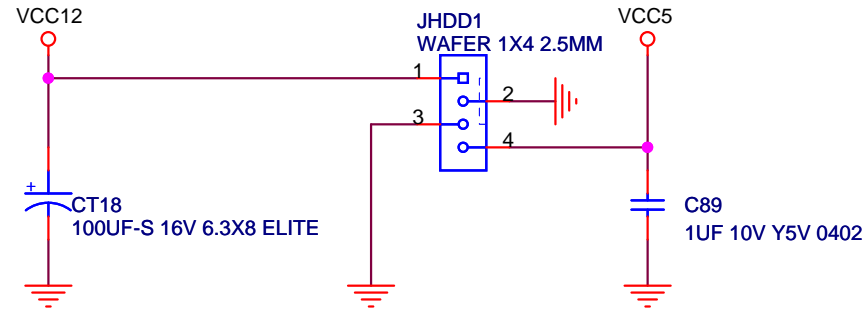
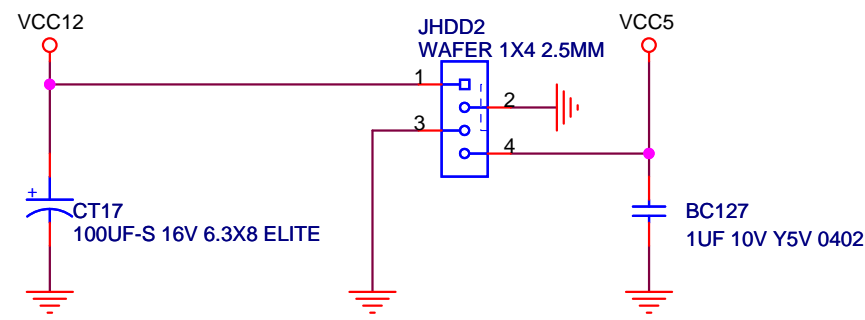
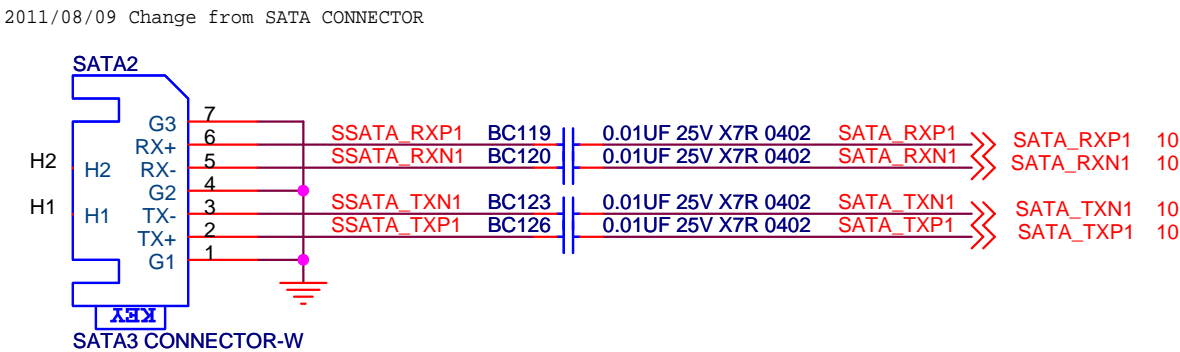
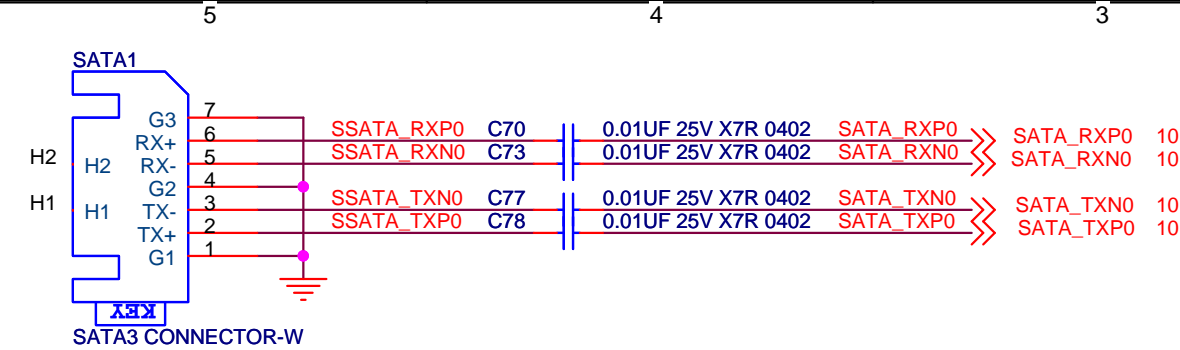



BLC_GP0	H	H	L
BLC_GP1	H	L	L
BLC_GP2	H	H	L
BL_CTRL	5V	2.5V	0V



LVDS_DATA0_N	BR81	220 0402
LVDS_DATA0_P	BR76	220 0402
LVDS_DATA1_N	BR73	220 0402
LVDS_DATA1_P	BR70	220 0402
LVDS_DATA2_N	BR68	220 0402
LVDS_DATA2_P	BR68	220 0402
LVDS_CLK_N	BR68	220 0402
LVDS_CLK_P	BR68	220 0402
LVDS_DATA3_N	BR68	220 0402
LVDS_DATA3_P	BR68	220 0402

LVDS Output 18Bit or 24 Bit



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Title  
**SATA CONNECTOR**


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REMOVED



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Title

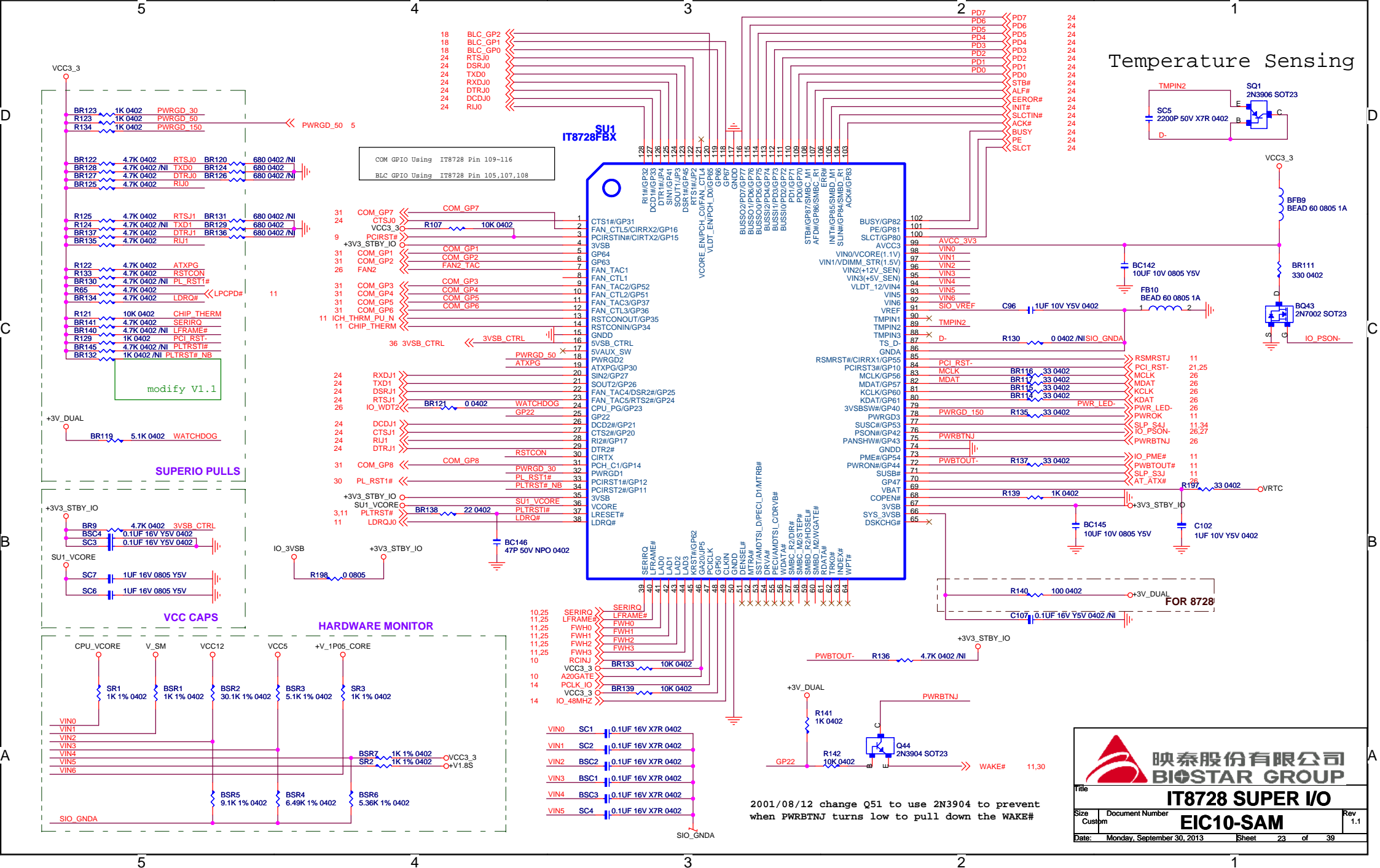
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Size  
Custom

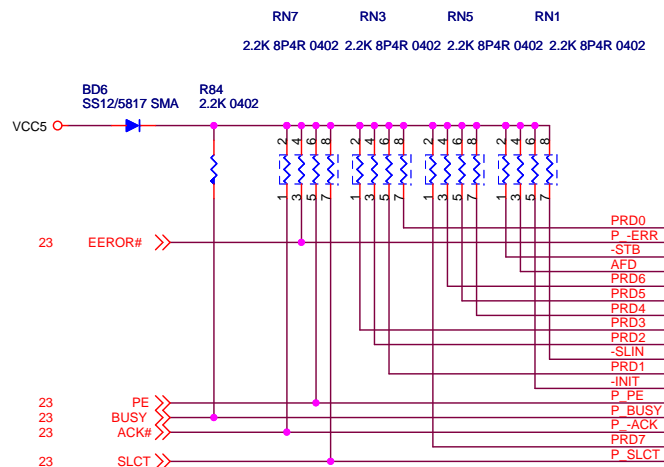
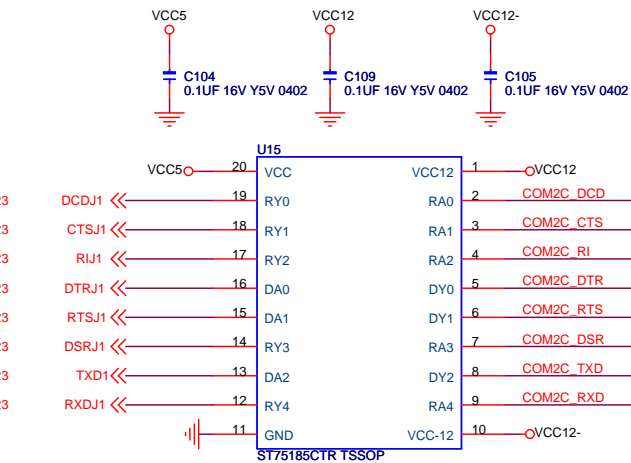
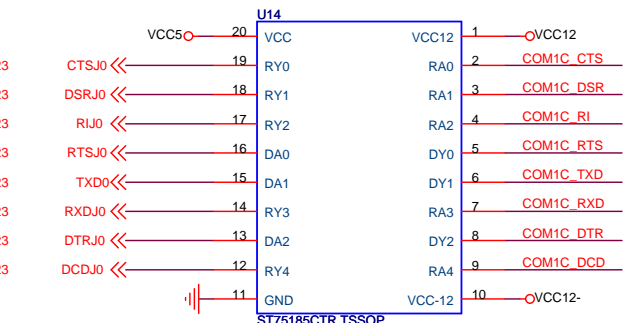
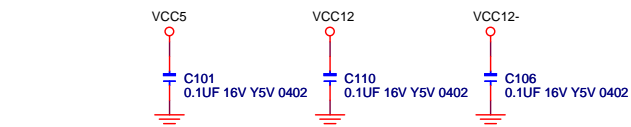
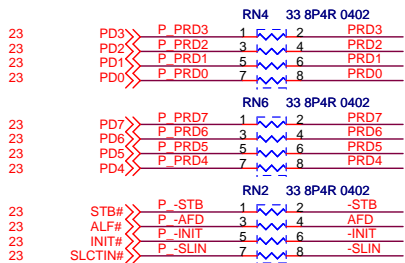
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EIC10-SAM

Rev  
1.1

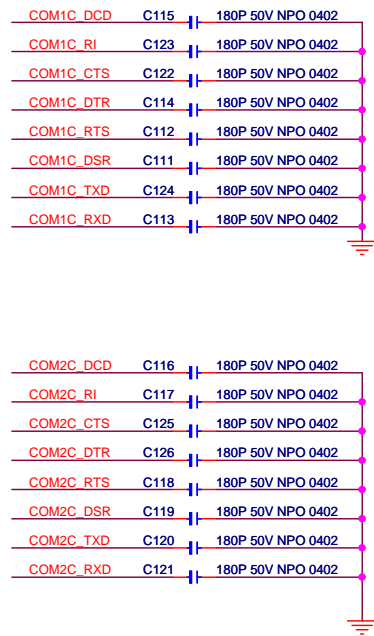
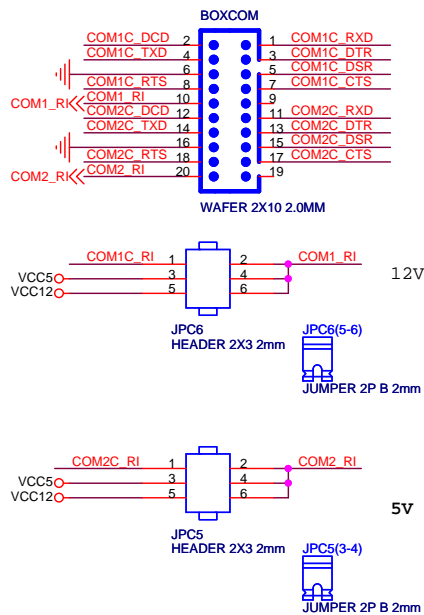
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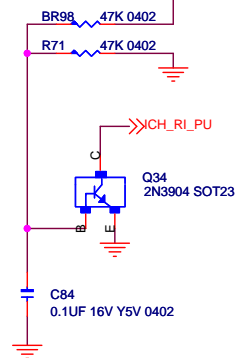
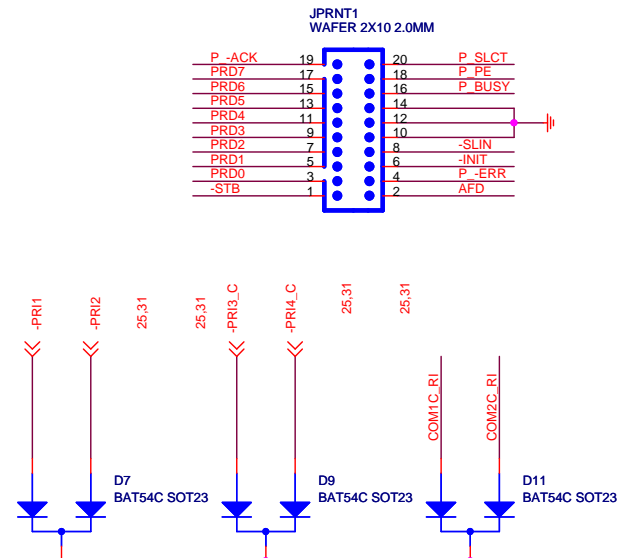
## Near I/O Chip



## COM6 COM5



## PARALLEL CONNECTOR



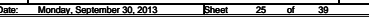
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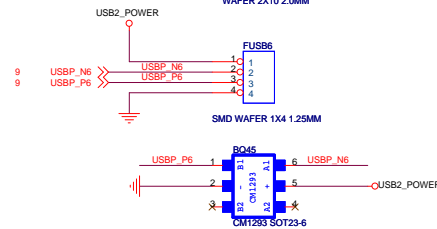
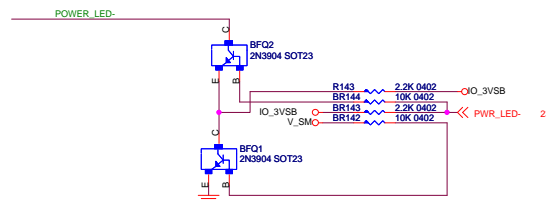
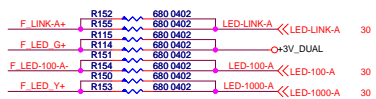
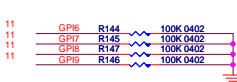
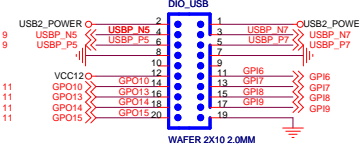
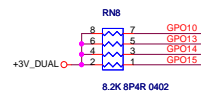
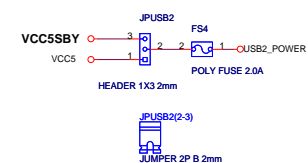
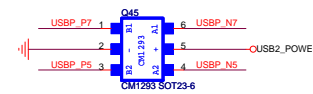
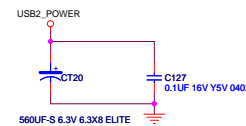
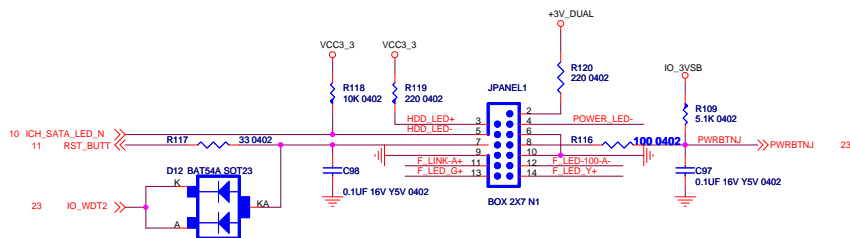
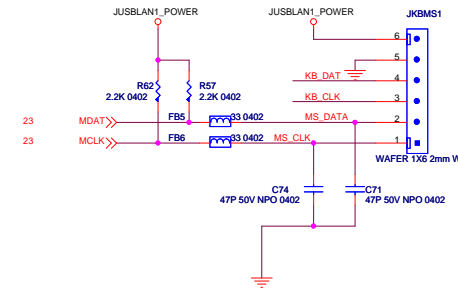
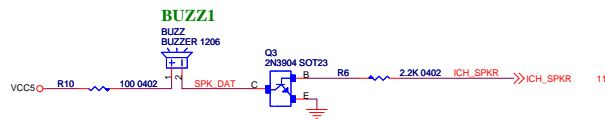
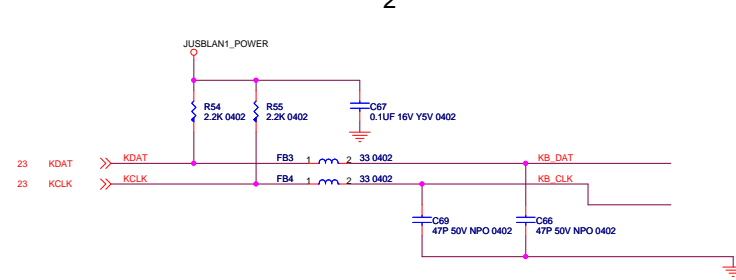
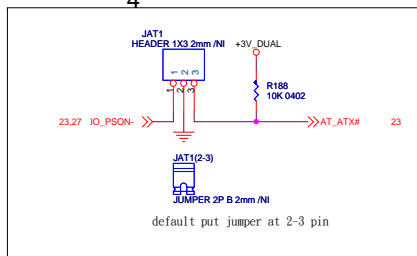
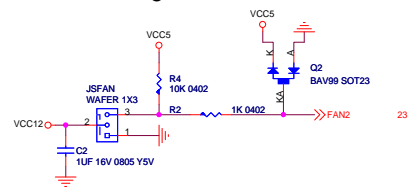
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**EIC10-SAM**

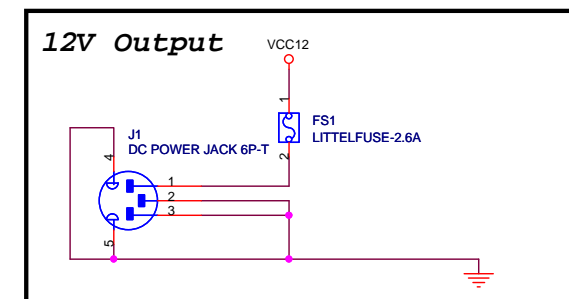
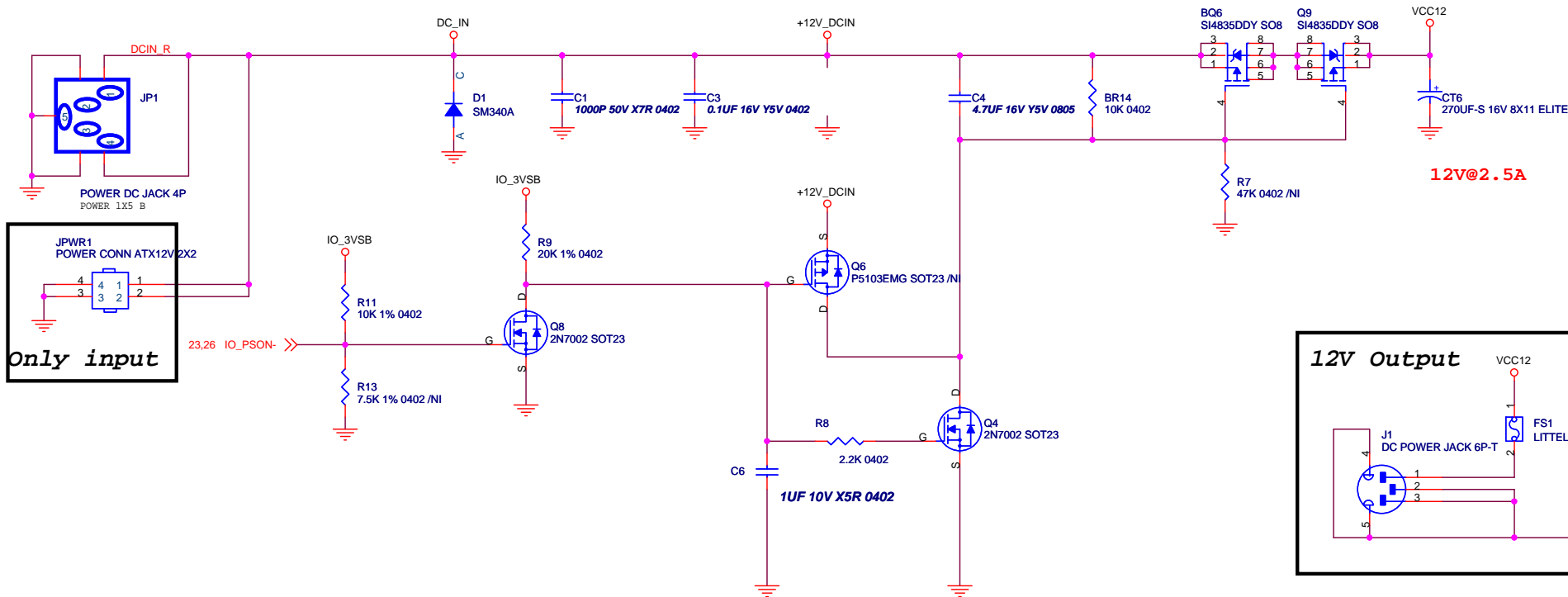
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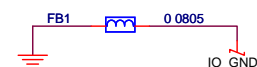
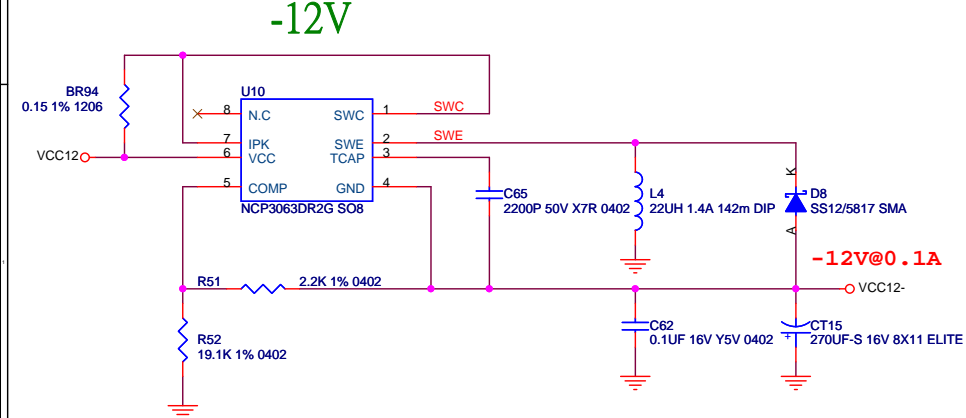


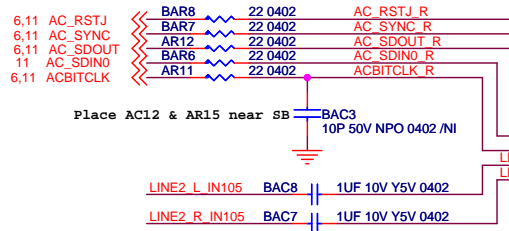
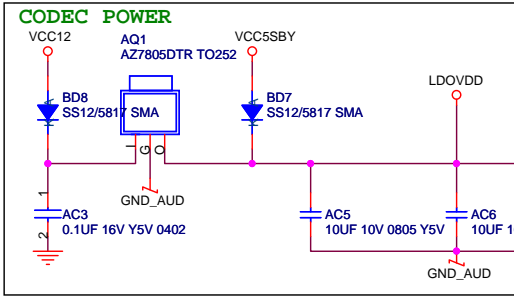




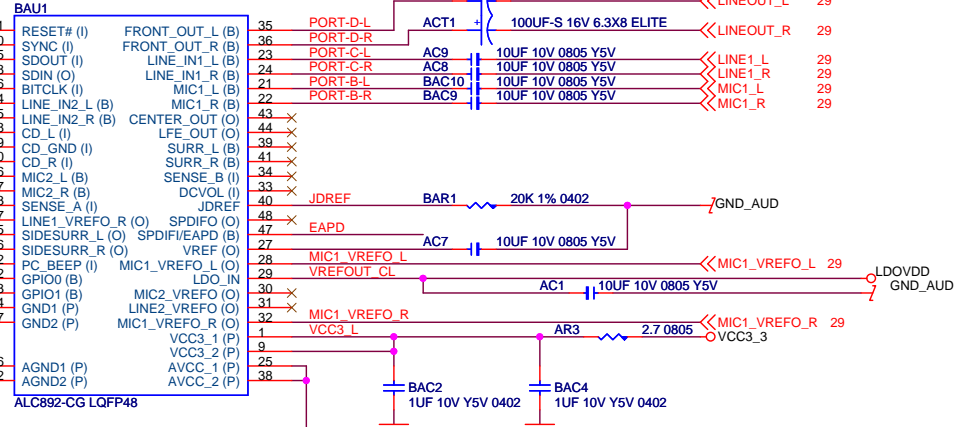


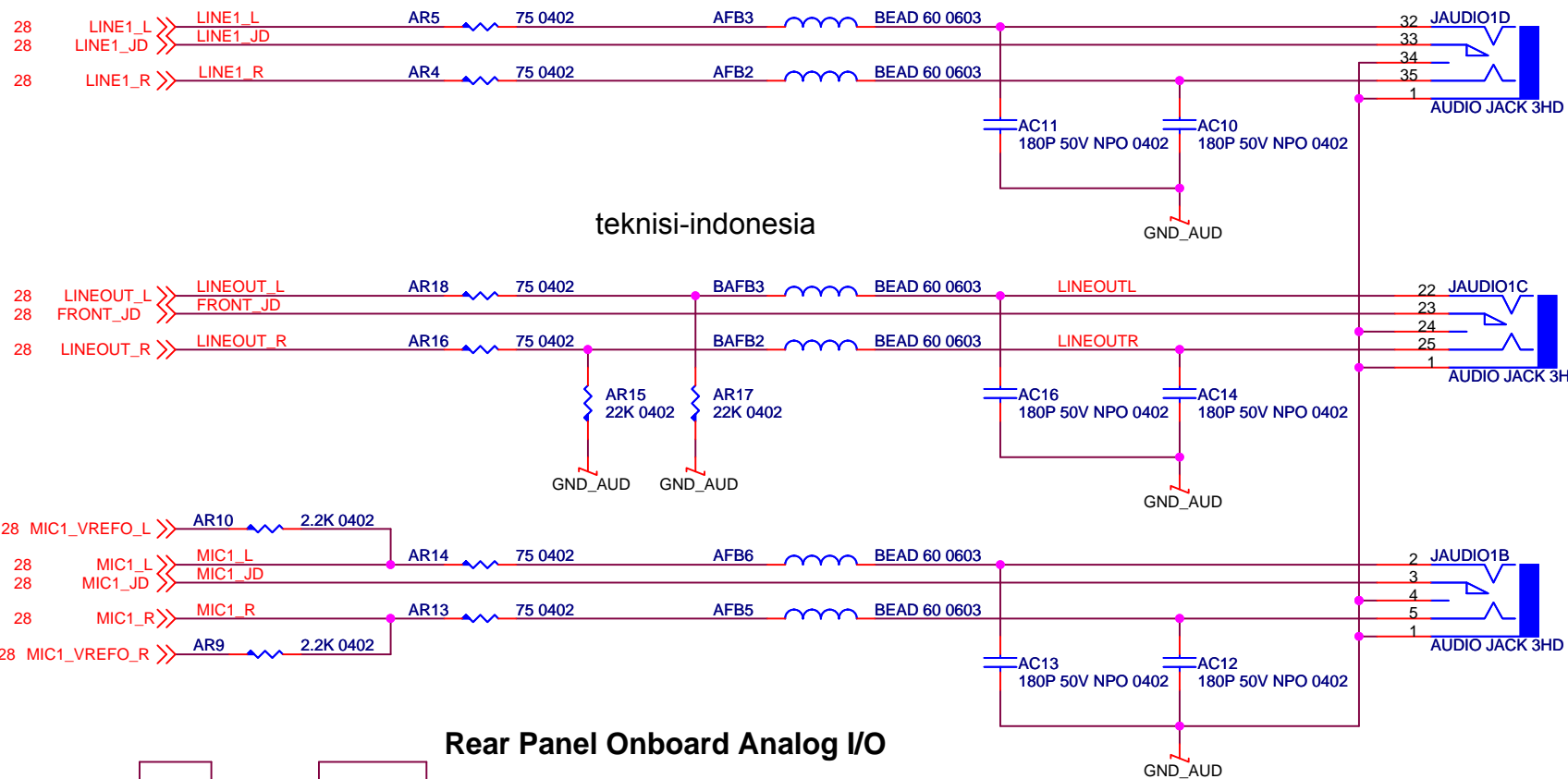
-12V





# ALC892 WITH FRONT AUDIO SUPPORT RE-TASKING



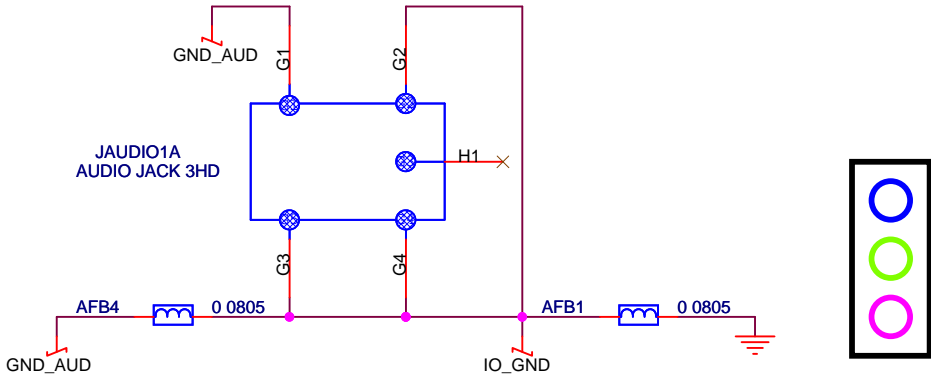


LINE\_IN  
BLUE

LINE\_OUT  
GREEN

MIC\_IN  
PINK

Rear Panel Onboard Analog I/O



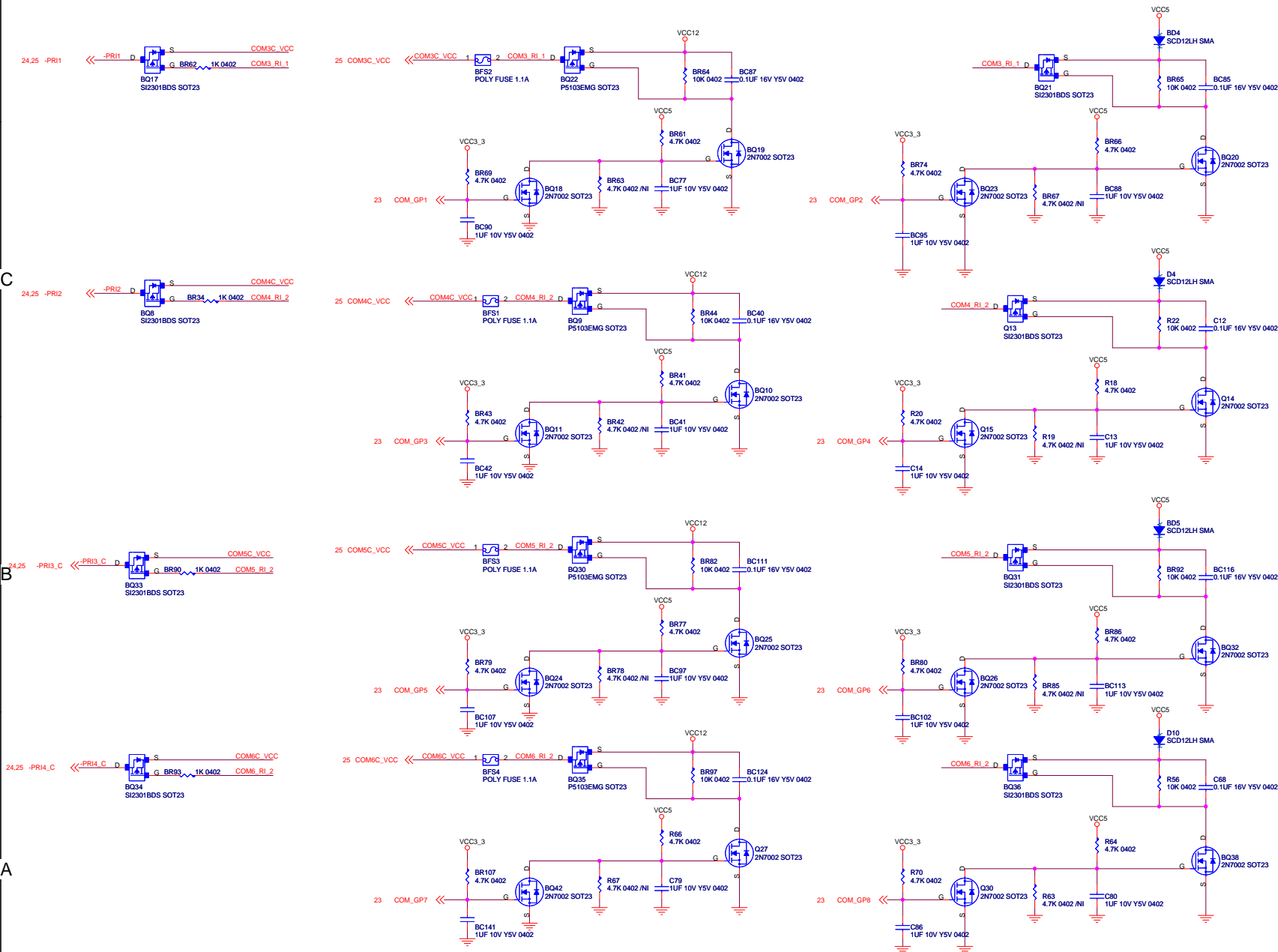


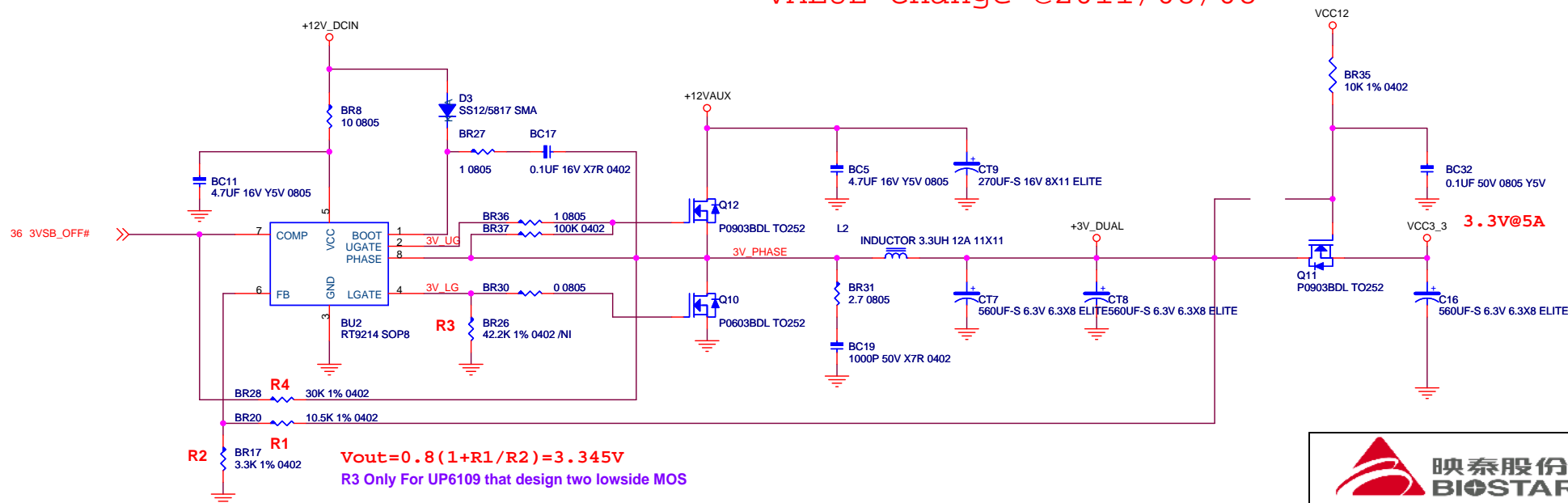
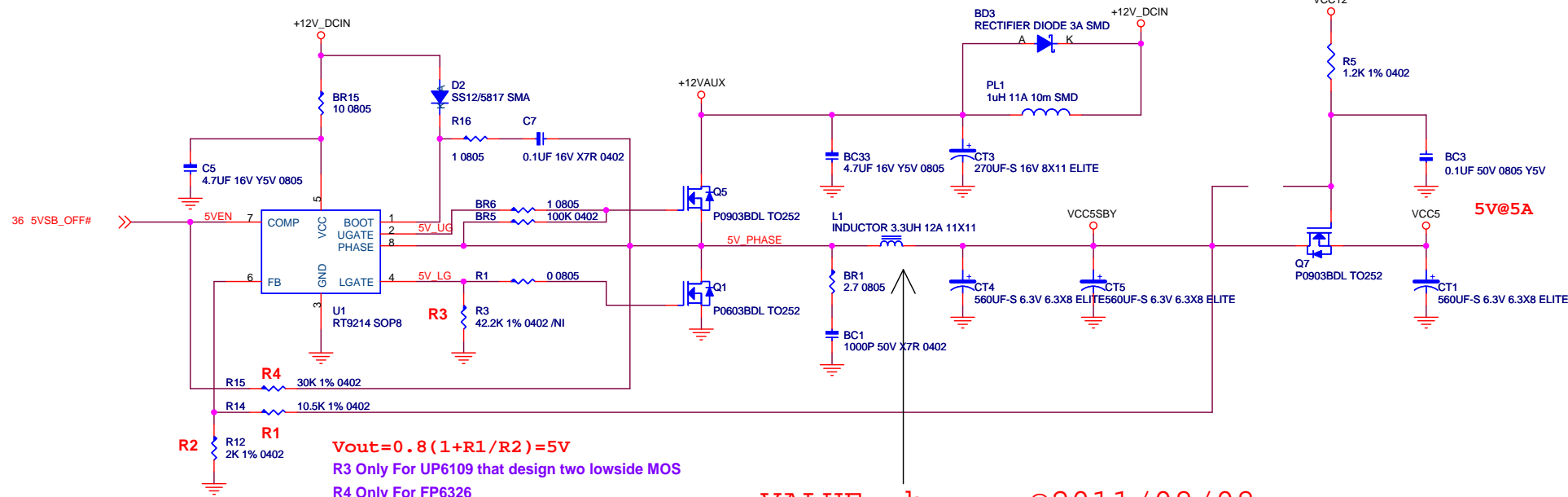
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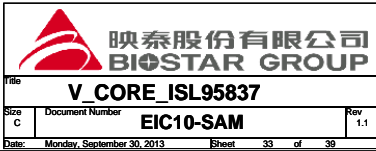
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VCC12	Low	High	Low	High	Low	High	Low	High
VCC5	High	Low	High	Low	High	Low	High	Low



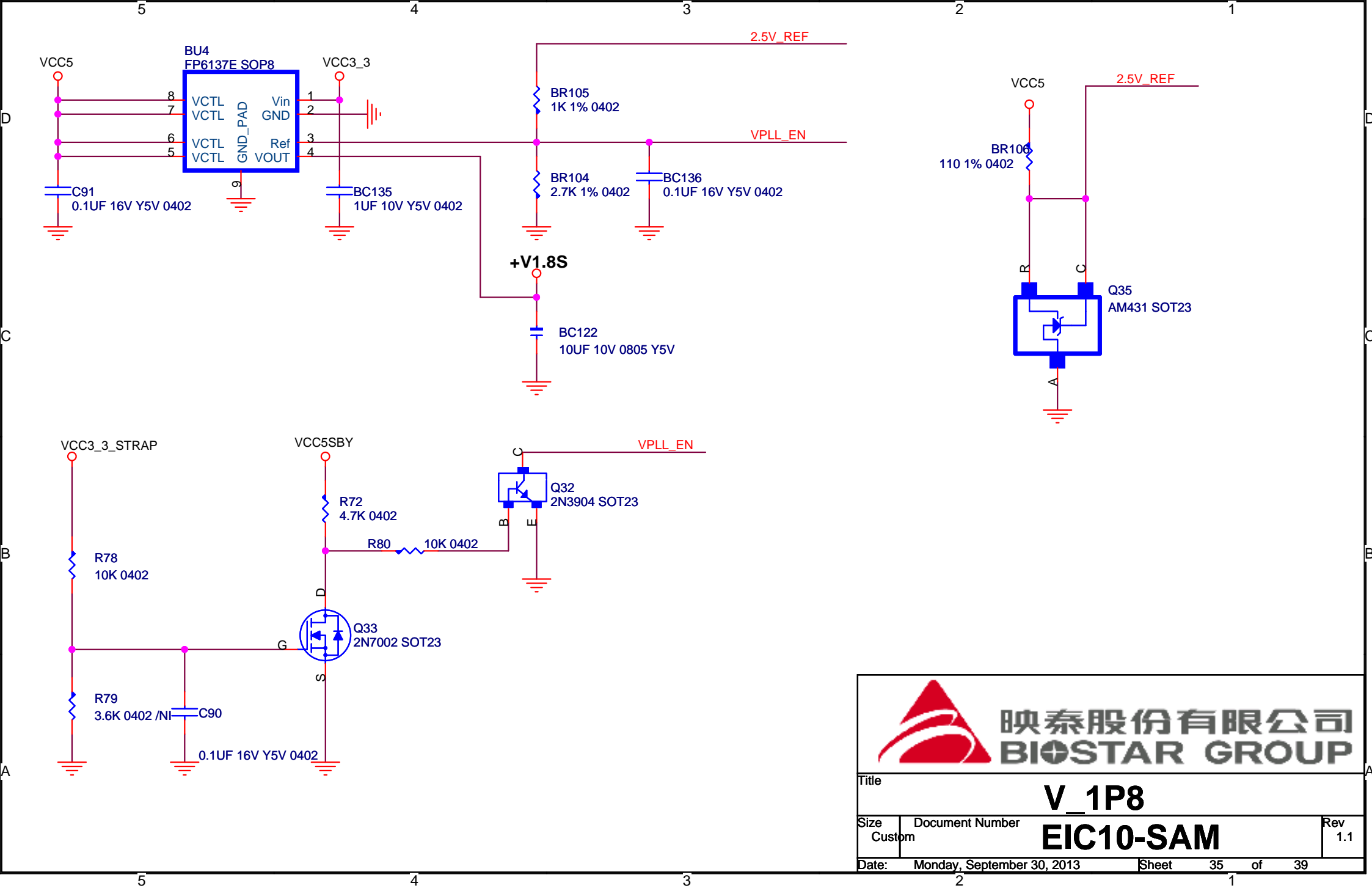



Title		
5VSB, VCC5, 3VSB, VCC3		
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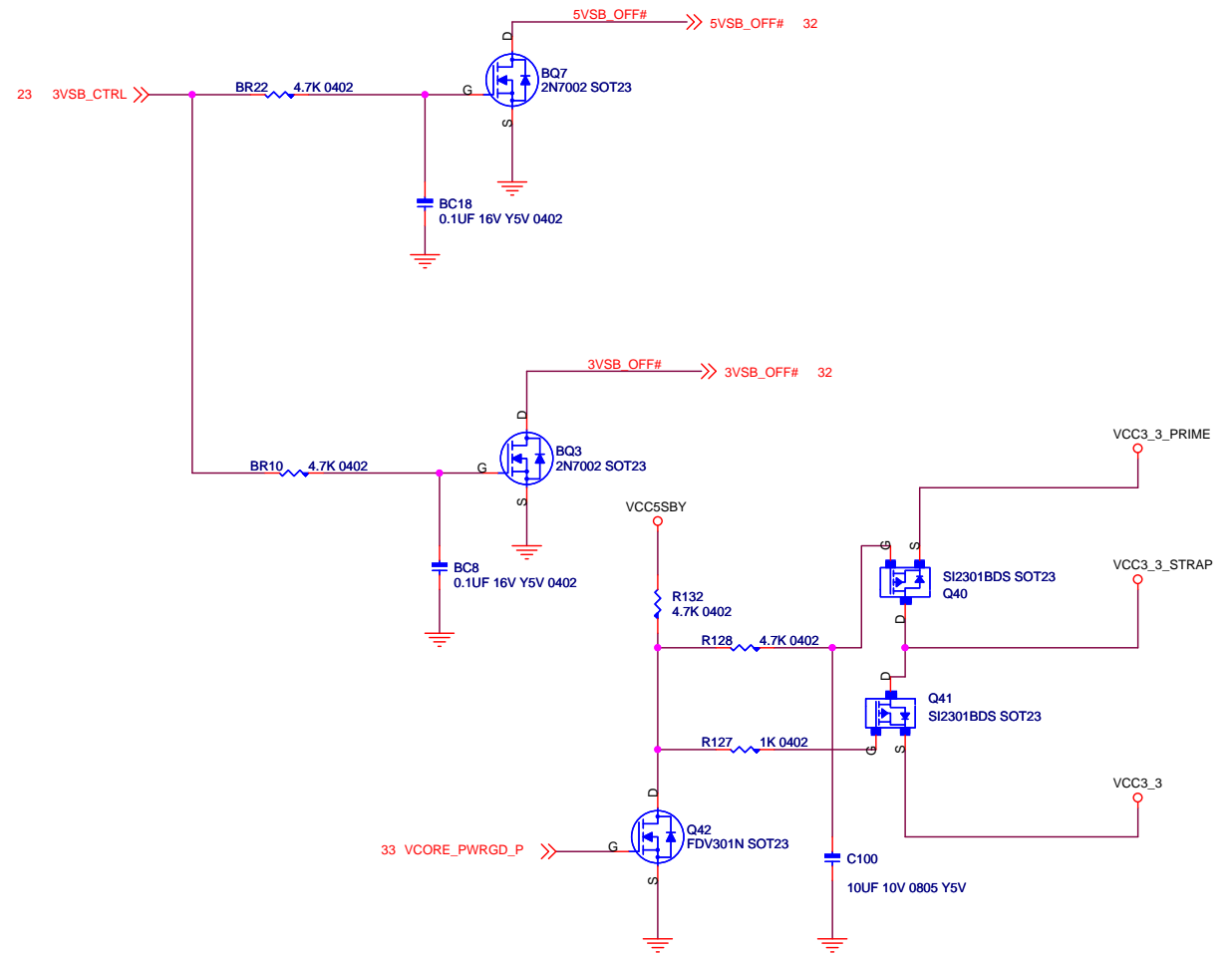
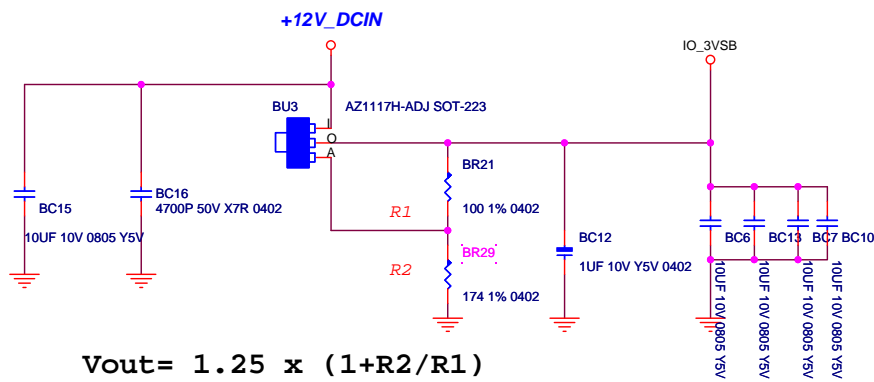







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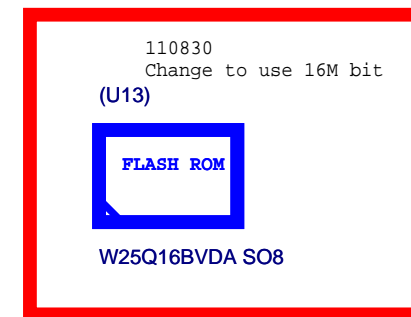
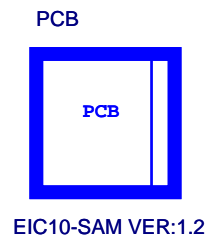
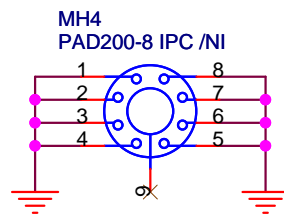
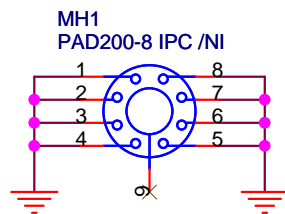
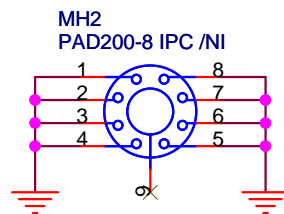
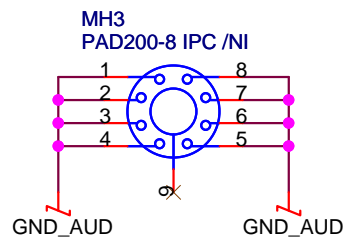




**映泰股份有限公司**  
**BIOSTAR GROUP**

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VCC3\_3\_PRIME Total current requirement of the board is 0.224A.



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V0.9 to V.095 Spec Update  
Change SYSTEM PWR TO ISL9440B  
Add SO-DDR3 Slot / DIMMA2  
Real COM Port 5V/12V/RI select by GPIO  
Audio Amplifier select by GPIO  
LVDS Connector 40P change to 30P  
One SATA Port Change to CFast header FOR Custormer  
Internal COM Port change to 20P BOX Header FOR Custormer  
Internal DIO and USB change to 20P BOX Header FOR Custormer  
Add Single USB pin header

		
Title		
Change List		
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