

Compal confidential

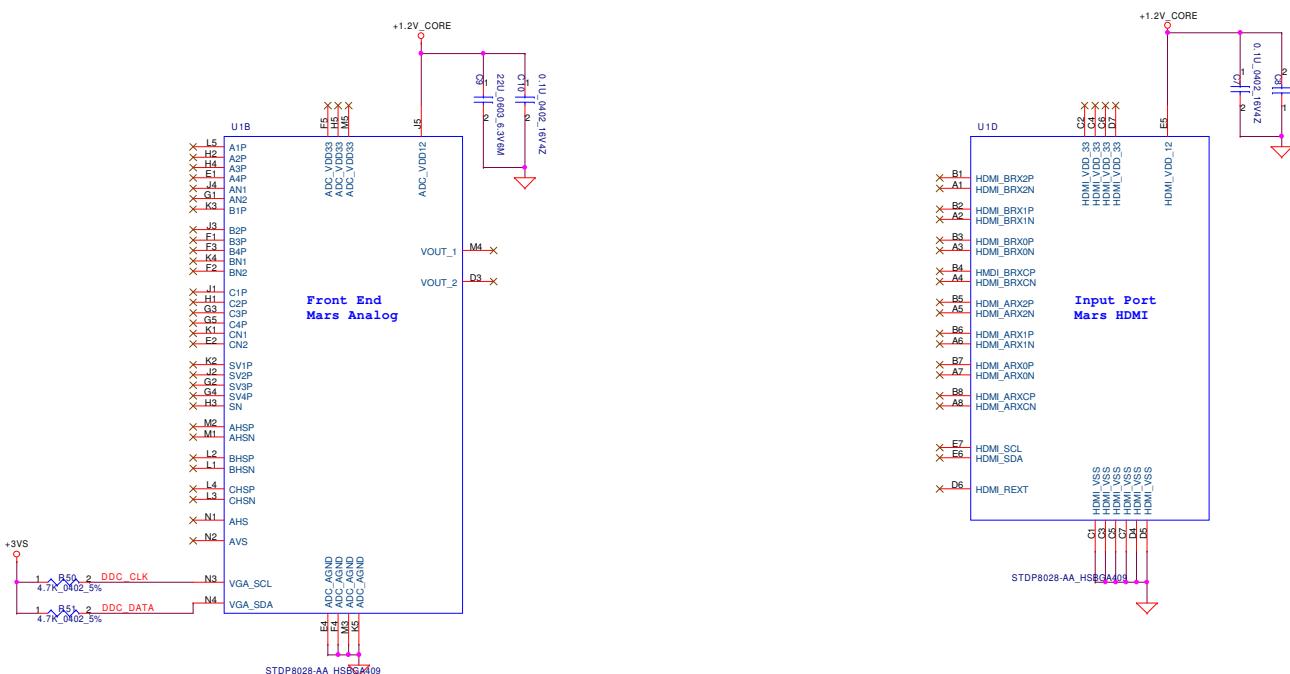
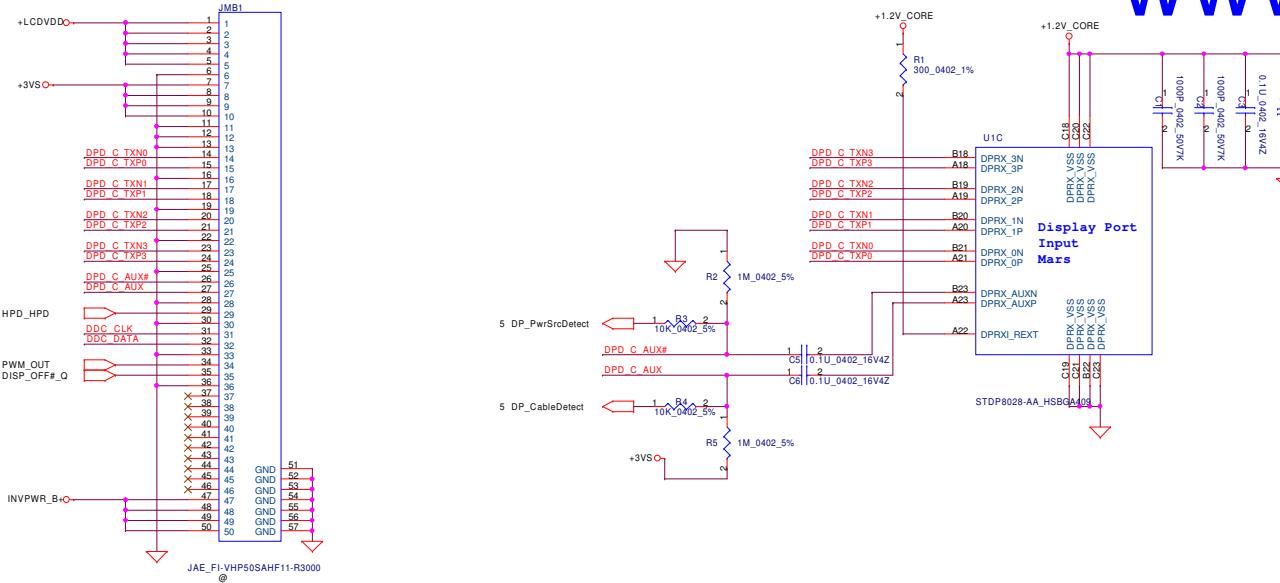
Schematics Document

Color Mapping Board / LS-495BP

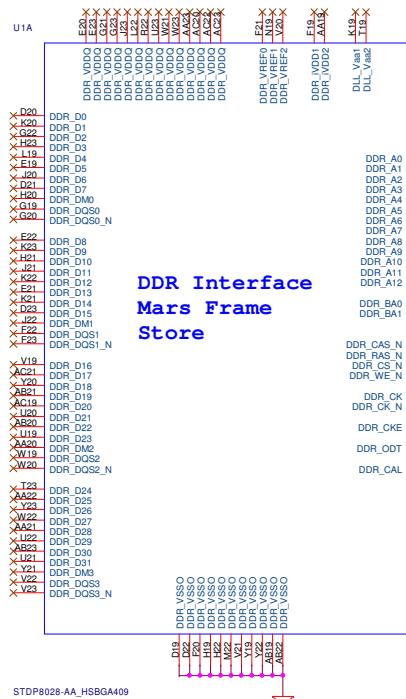
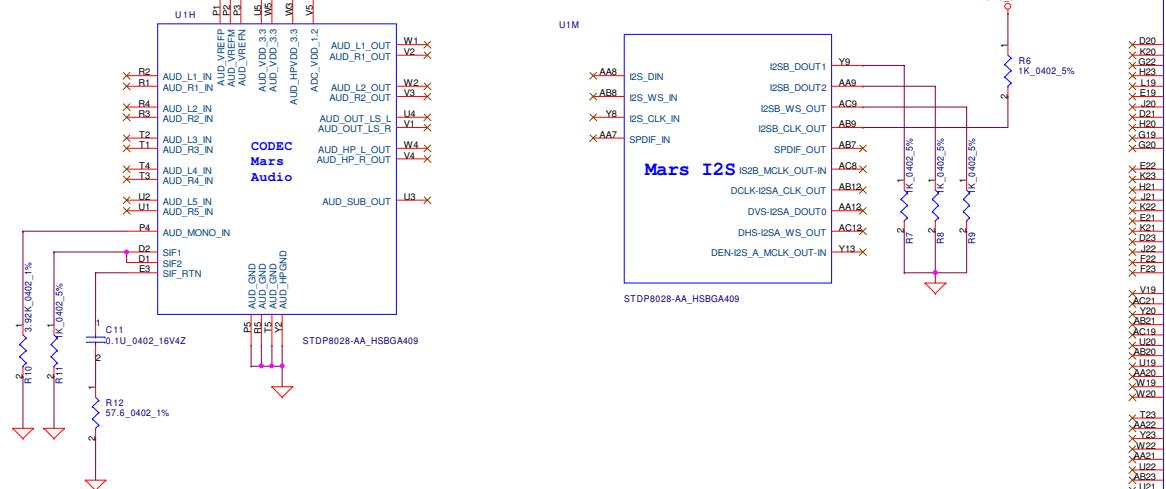
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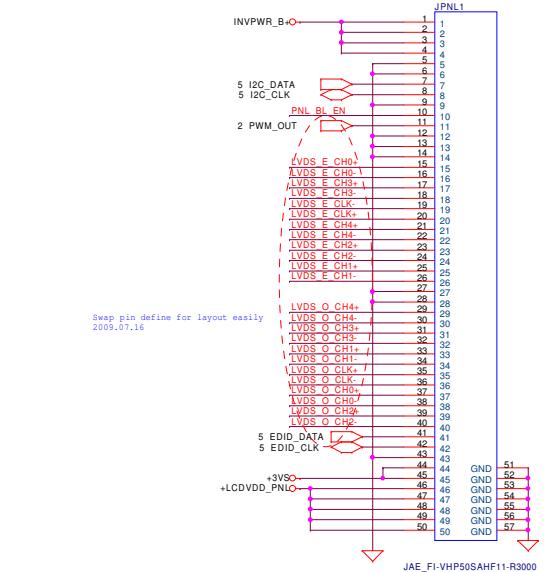
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Security Classification	Issued Date	Deciphered Date	2009/09/16	Title	Cover Sheet
Custom	Document Number	LS-495BP		Rev	0.1
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Swap pin define for layout easily
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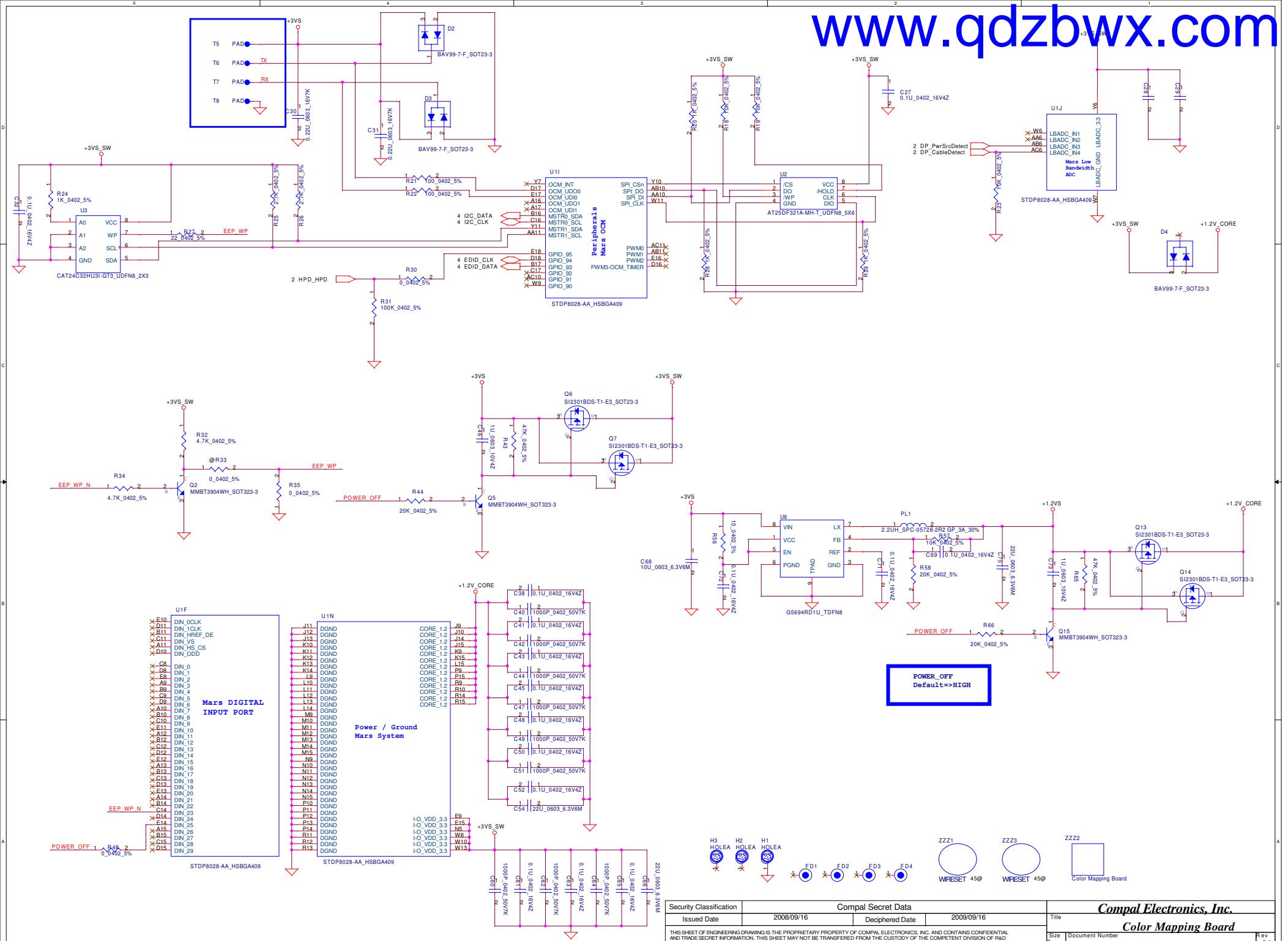
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Version Change List (P. I. R. List) for Color Mapping Board Circuit

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Item	Page #	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	4		2009.07.14		Crystal can not work	C26.2 connect to U1L.AC4	
2	4		2009.07.15		LVDS Signals layout that use too more vias	Swap JPNL1 Pin Define	
3	4		2009.07.15		HP Request	MARS pin Y12 (PPWR) gate GPU controlled +LCDVDD 3.3V power to the panel.	
4	4		2009.07.15		HP Request / ST Suggestion	MARS pin W12 (PBIAS) gate GPU controlled DISP_OFF#_Q to the panel.	
5	5		2009.07.15		Remove two regulators circuits for design easily	Add DC/DC Convertor to instead of regulator circuits	
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