

Compal confidential

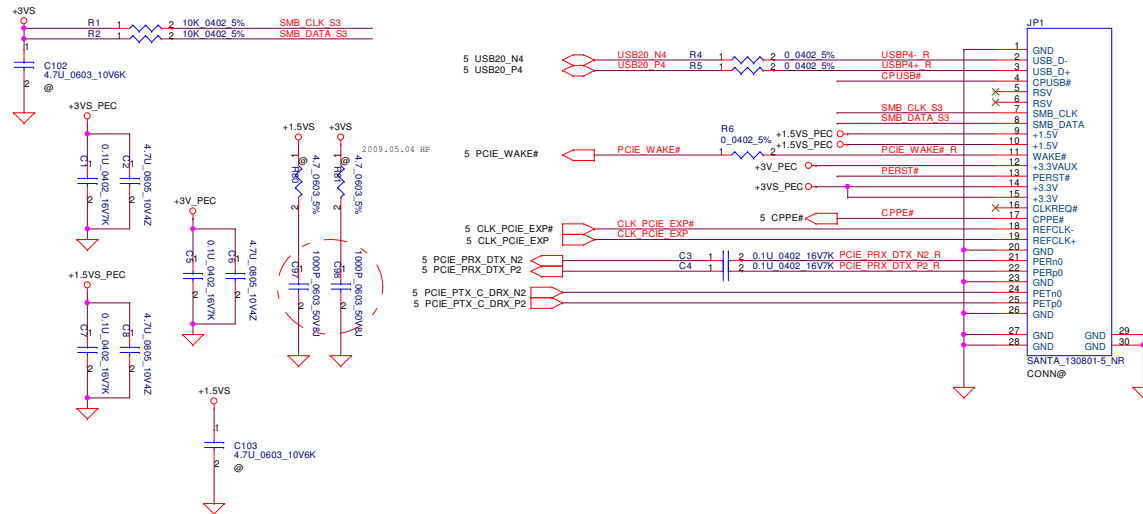
Schematics Document Mutil-Media Board

2009-07-21

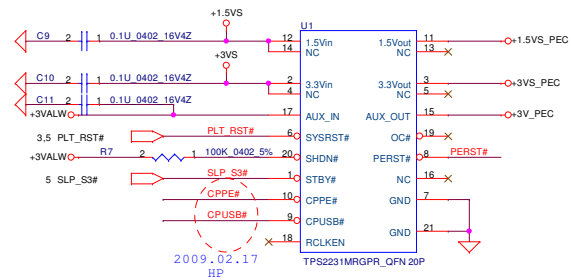
ver: 0.4

Security Classification		Compal Secret Data		Title	
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Express Card Slot



Express Card Power Switch

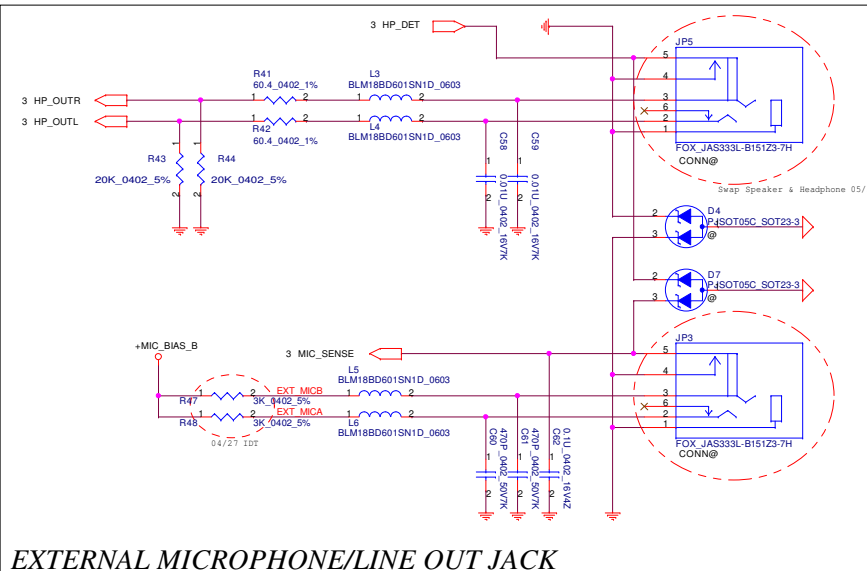
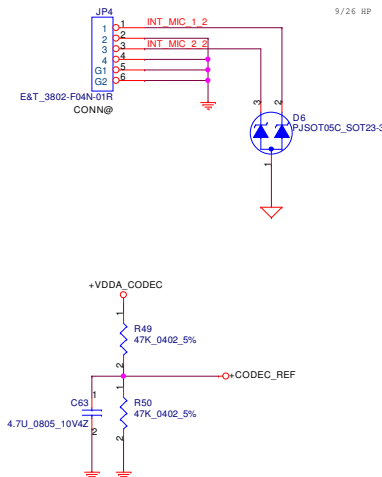


Security Classification	Compal Secret Data			<i>Compal Electronics, Inc.</i> Express Card		
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MIC Connector

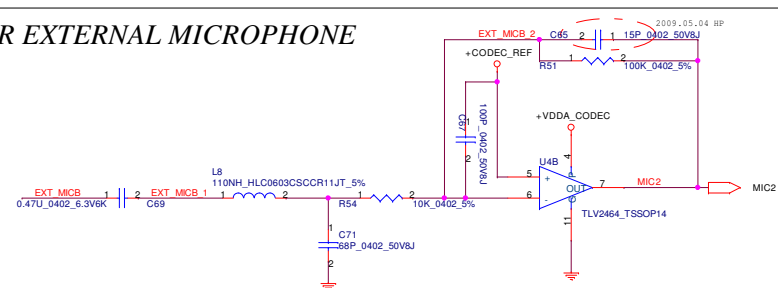
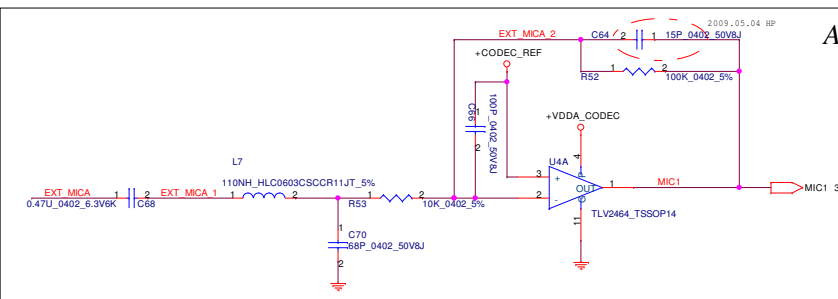
Must be placed on the audio daughter board, close to the mic preamplifier. Very important!

9/26 HP



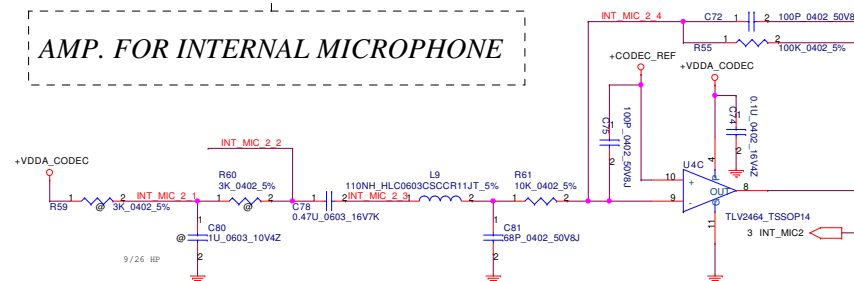
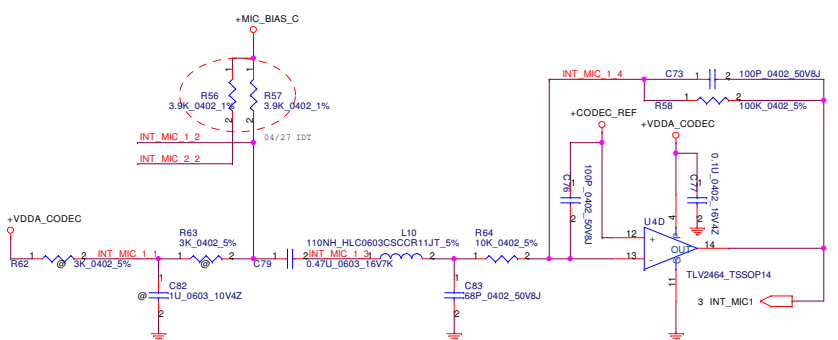
EXTERNAL MICROPHONE/LINE OUT JACK

AMP. FOR EXTERNAL MICROPHONE



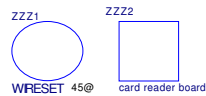
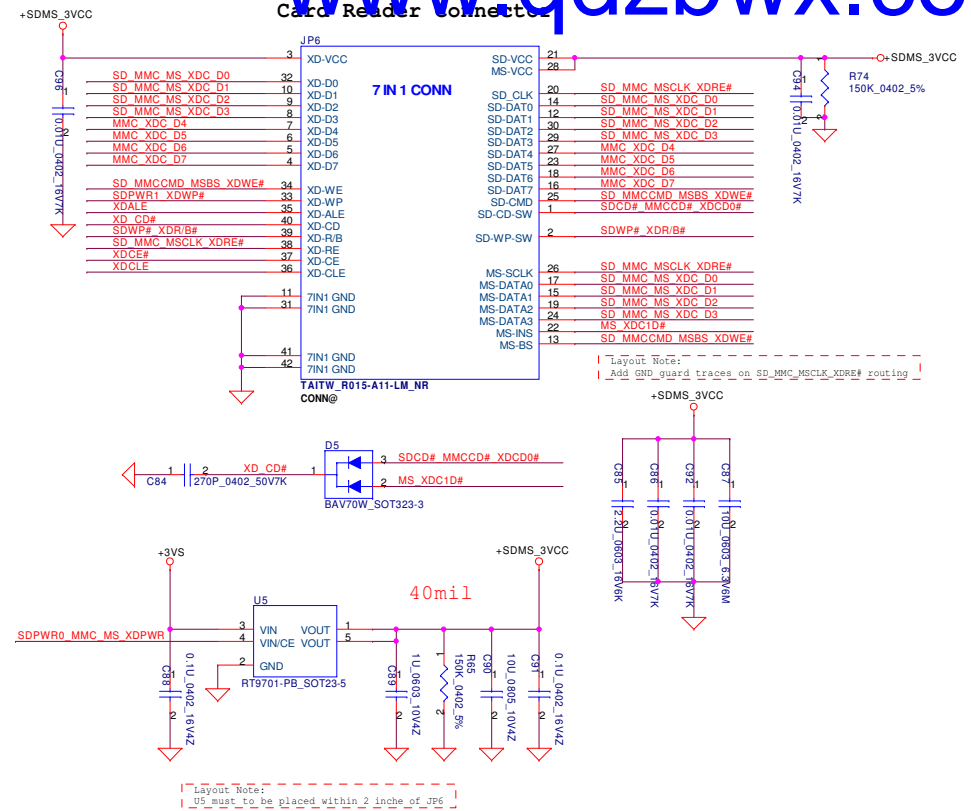
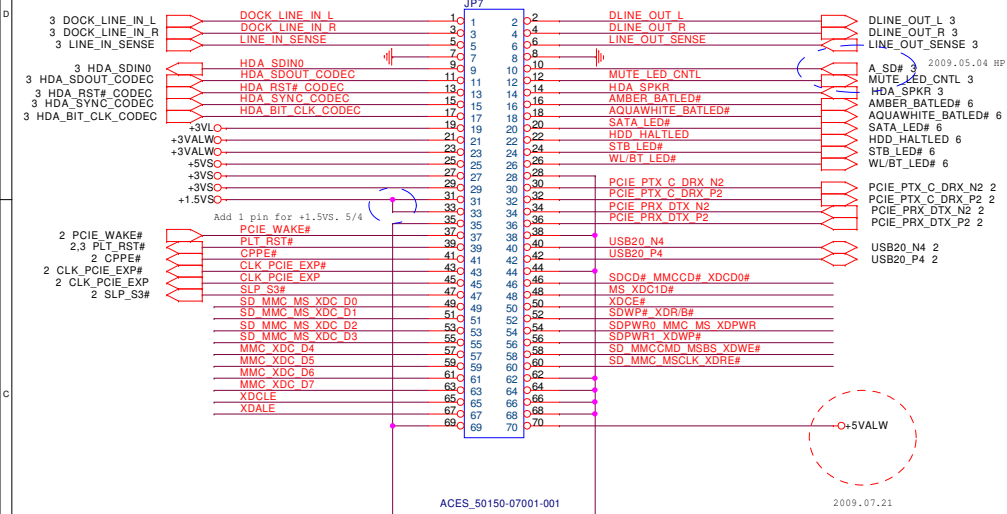
Close to the MIC connector 9/26 HP

AMP. FOR INTERNAL MICROPHONE



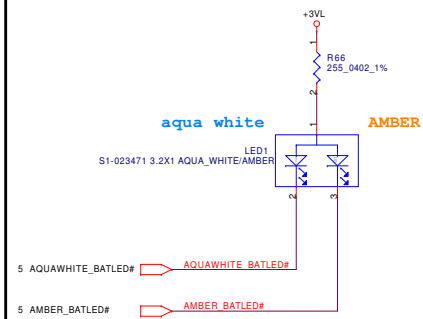
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Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	AMP & Audio Jack
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Audio/Express Card/Card Reader Conn

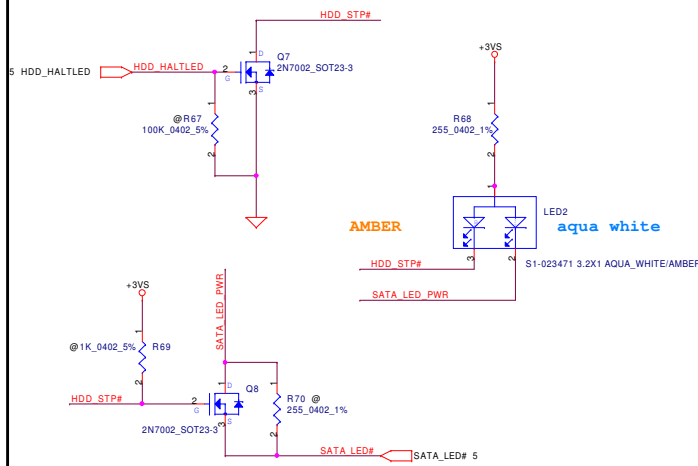


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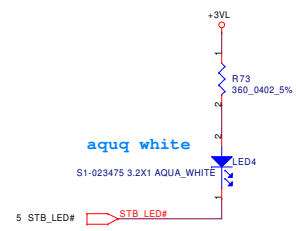
Battery LED



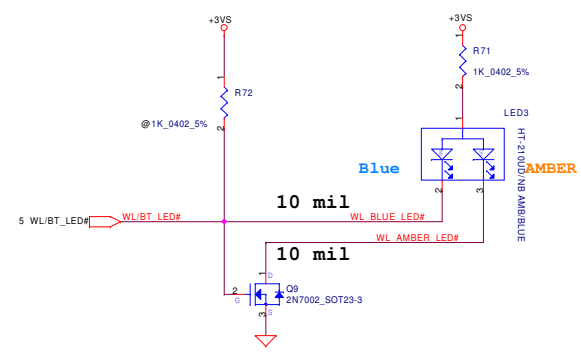
HDD active LED



POWER LED



Wireless LED



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Version Change List (P. I. R. List) for Multimedia Board Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	2		02/17		CPPE# and CPUSB# reversed of power switch	reverse power switch pin 9 and pin 10	0.2
2	4		02/17		remove the +V_AMP circuit	remove R45 and R46	0.2
3	3		02/17		IDT suggest	Change the C23/24 value to 2.2uF/10V 0805 for THD+N	0.2
4	3		02/17		codec output Port R/L signals swap	swap codec output port R/L signals	0.3
5	3		04/27		HP Request	Remove SLP_S3# connect to REG_EN Add pull up +5VS 100K ohm to REG_EN	0.3
6	3		04/27		For IDT Reference schematic	Change R47 , R48 to 3K Change R56 , R57 to 3.9K	0.3
7	3		05/04		add solution for Mute the audio of Display port	1. add a N MOS connect to EAPD and A_SD# 2. remove the D3 3. connect U3 pin 30 to MUTE_LED_CNTL 4. change JP1 and JP11 pin14 from EAPD to MUTE_LED_CNTL 5. add the D9 and pin2 connect to EAPD# , pin3 to A_SD# and pin1 to SPRK_EN 6. Change A_SD to A_SD# at JP7.10 Change EAPD to MUTE_LED_CNTL at JP7.12	0.3
8	4		05/04		fix audio precision fail issue (Magnitude Response fail)	change C64 and C65 form 33P to 15P	0.3
9	2		05/04		prevent the voltage droop	Reserve 4.7U 0603 cap (NO INSTALL) on +1.5VS and +3VS	0.3
10	3		05/12		HP Request	1. Change R76 to NO INSTALL 2. Add a 0.1UF cap (NO INSTALL) from R76.2 to GND. 3. And add a 0 ohm (INSTALL) to connect PLT_RST# to U2.25 (REG_EN). 4. Change net name from MUTE_LED_CNTL to MUTE_LED_CNTL. 5. Change the red label "EAPD" to MUTE_LED_CNTL.	0.3
11	3		05/14		HP Request	Add a NO INSTALLL 0 ohm to connect SPKR_EN to HP_EN	
12	4		05/14		HP Request	Swap JP3 & JP5	

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	<i>Item</i>	<i>Page#</i>	<i>Title</i>	<i>Date</i>	<i>Request Owner</i>	<i>Issue Description</i>	<i>Solution Description</i>	<i>Rev.</i>
D	13	3		07/15		HP Request	Change R9 to 300K Change R27 & R29 to 6.01K Change R28 & R30 to 2K	
C	14	3		07/15		HP Request	Change C35 (bypass cap for U2.24) from 0.47U to 10U.	
B								
A								

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