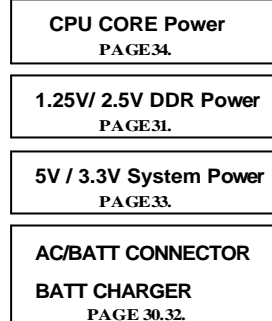


STACK LAYERS

L1 : TOP
L2 : VCC
L3 : IN1 (Hig Speed)
L4 : GND
L5 : IN2 (High Speed)
L6 : BOT

Power - Source Control**KT9D**

P/N: DAKT9DMB6A0

Northwood CPU

(Micro-FCPGA)

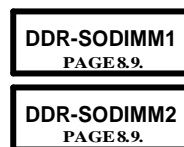
PAGE 2,3.

HA[3.. 31] **FSB** (400/533MHz) HD[0.. 63]

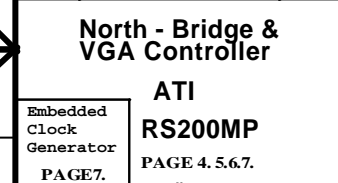
Panel Connector
PAGE27.

PCIBUSROUTINGTABLE

1394 -- AD23,-INTC,-REQ3,-GNT3
 LAN -- AD29,-INTB,-REQ2,-GNT2
 MINIPCI -- AD20,-INTC,-INTD,-REQ0,-GNT0
 PCI HUB -- -REQ1,-GNT1
 PCMCIA --AD21,-INTF,-REQ4,-GNT4
 USB 2.0 -- AD22,-INTC,-INTD,-INTF,-REQ5,-GNT5



14.318MHz

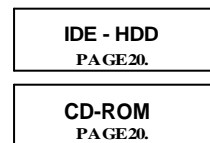
LVDS
PCICLK_INT**S-Video**
PAGE28.**CRT**
PAGE27.

CLK-BUFFER
ICS-9112
PAGE7.

-REQ1
-GNT1

PCI HUB
ENE_ASIC1
PAGE17.

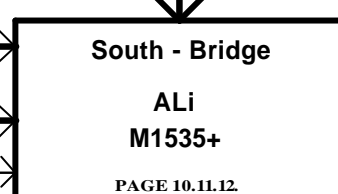
-REQ4,-GNT4
PCLK_PGM
PCLK_USB20
PCLK_GNT5



ATA 66/100 MHz

ATA 66/100 MHz

AC-Link



CARDBUS
O2 - 6912
PAGE 16,17.

CardBus Conn
PAGE16.

IEEE1394
TI-TPA43AB21
PAGE14.

1394 Conn

MINI-PCI
Wireless LAN
Modem/LAN
PAGE19.

LAN
NS-83816ANG
PAGE15.

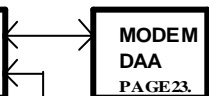
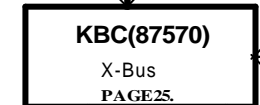
RJ45 Conn
PAGE15.

USB 2.0
VIA_6202T
PAGE18.

3 Rear USB Ports
PAGE27.



AMP
TI TPA0132
PAGE22.

Audio Jacks**MIC Jacks****RJ11 Conn****FDD**
PAGE11.**IR**
PAGE13.**Serial Parallel**
PAGE 13,29.

PS/2

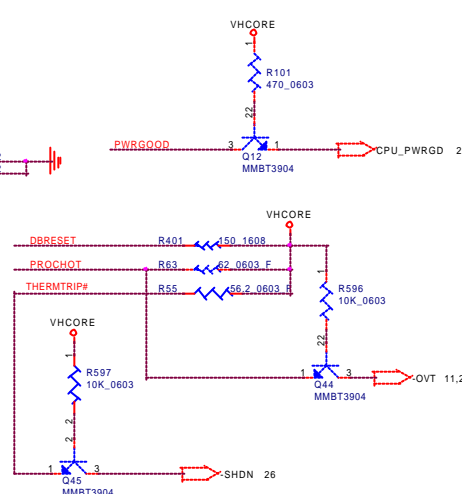
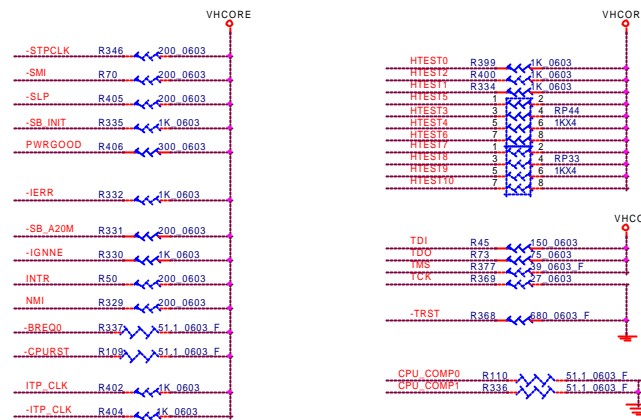
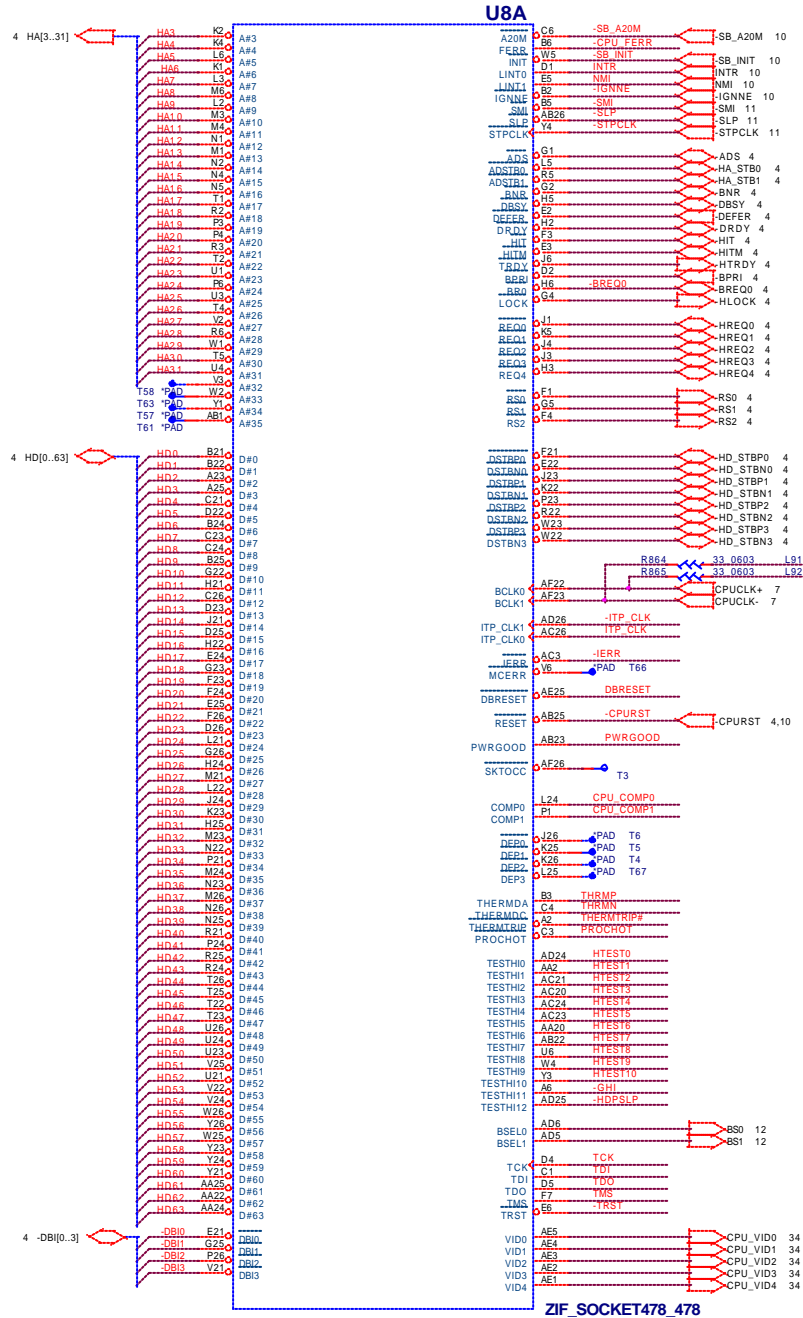
Touchpad/ Stick point
PAGE27.**Keyboard**
PAGE26.**BIOS**
PAGE25.**PORT REPLICATOR**

PROJECT : KT9D
Quanta Computer Inc.

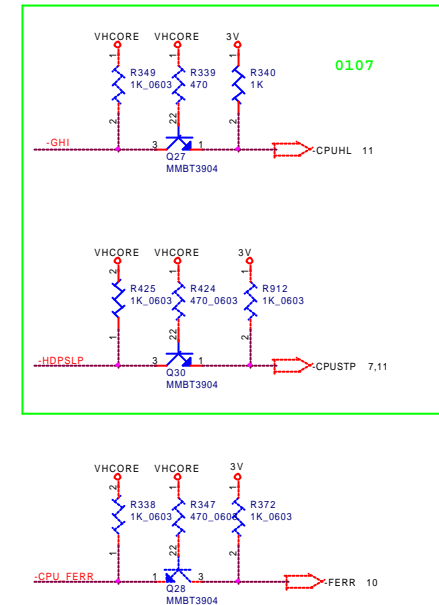
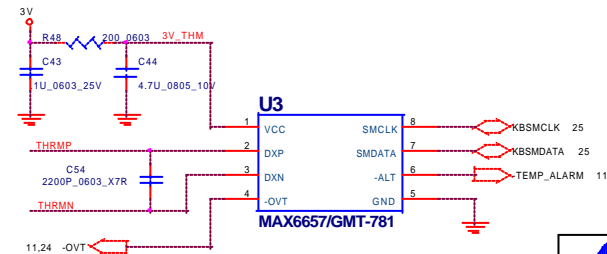
Size Document Number
BLOCK DIAGRAM
 Date: Monday, March 17, 2003 Sheet 1 of 35 Rev 1A

NORTHWOOD CPU -A

02



H/W MONITOR



PROJECT : KT9D

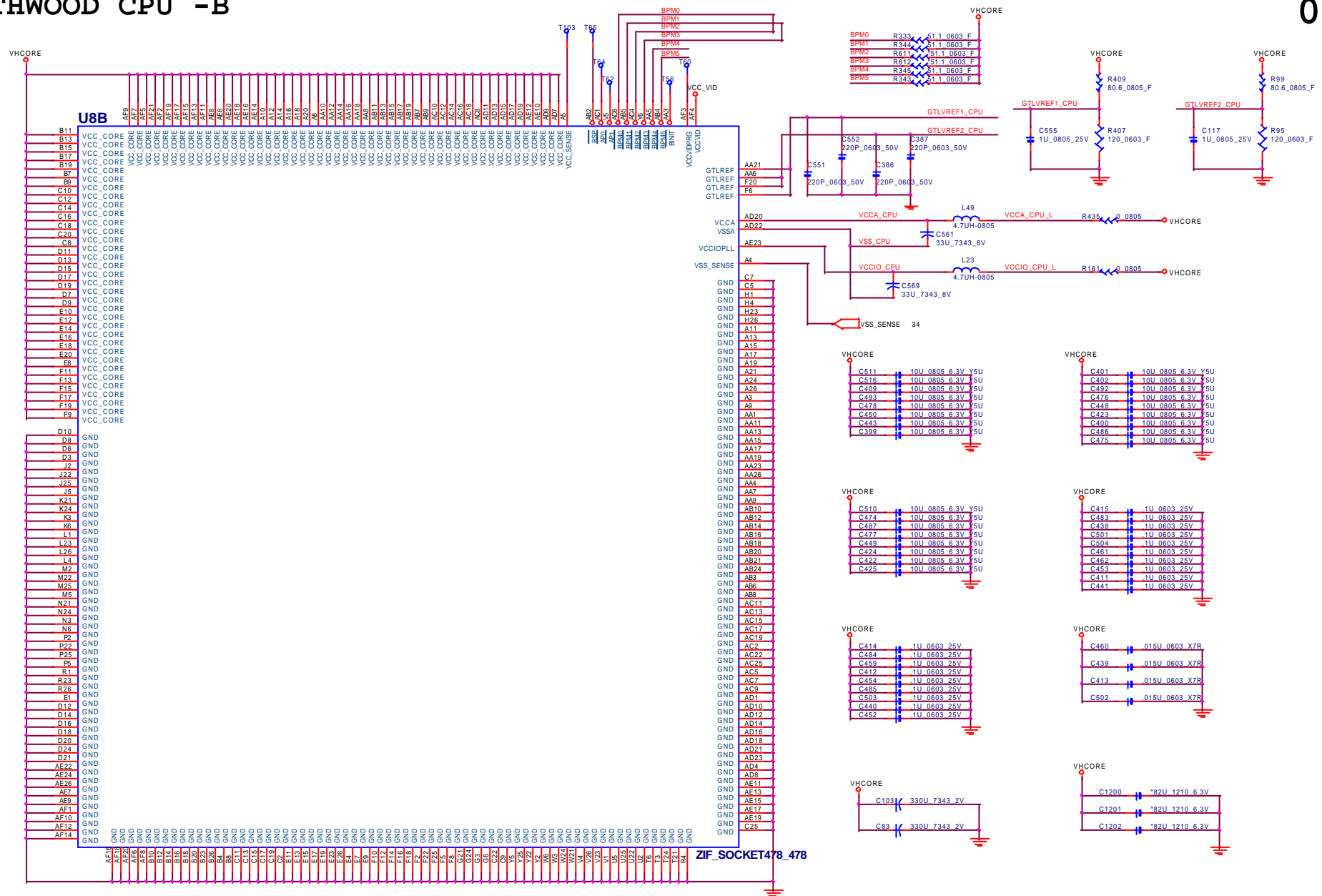
Quanta Computer Inc.

Size Document Number
NORTHWOOD CPU -A

Date Monday, March 17, 2003 Sheet 2 of 35

NORTHWOOD CPU -B

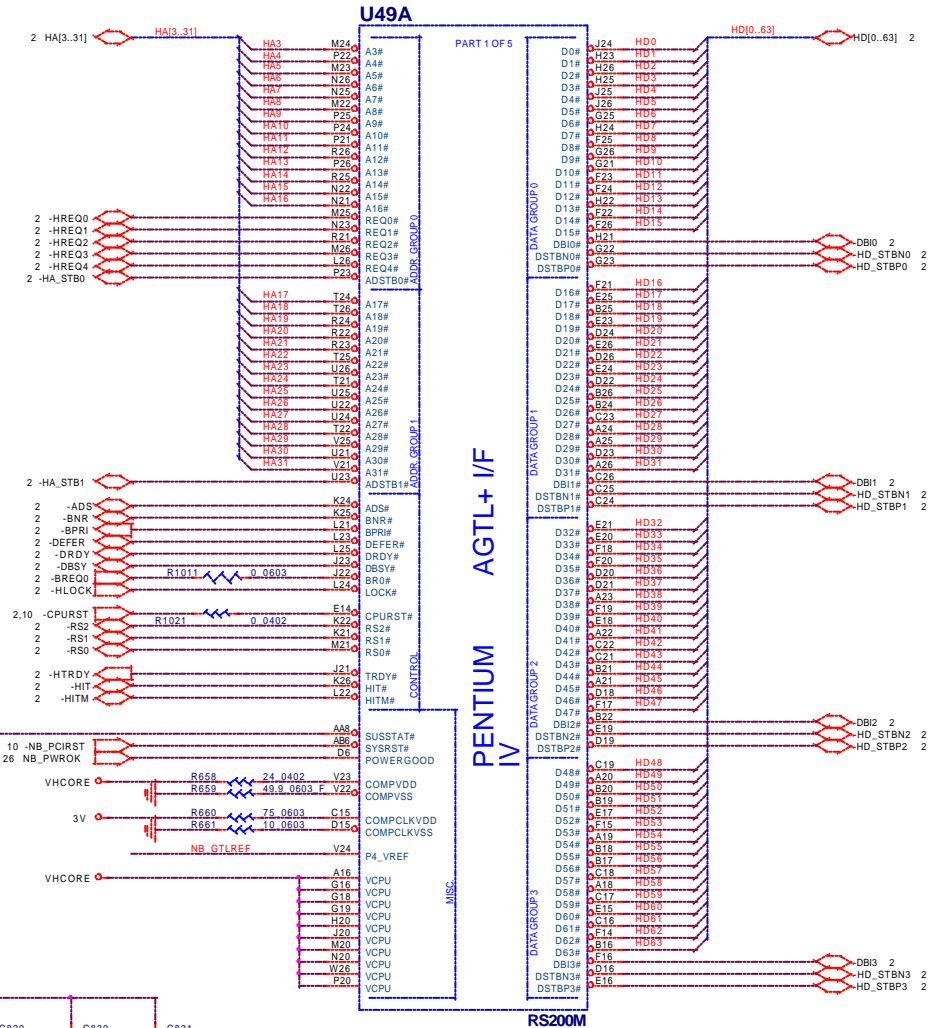
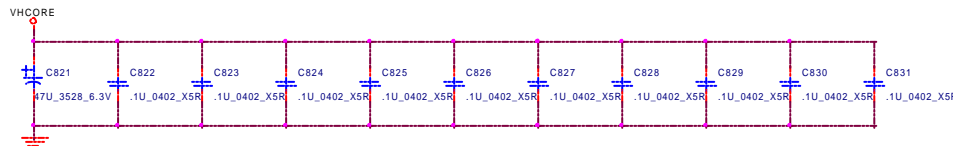
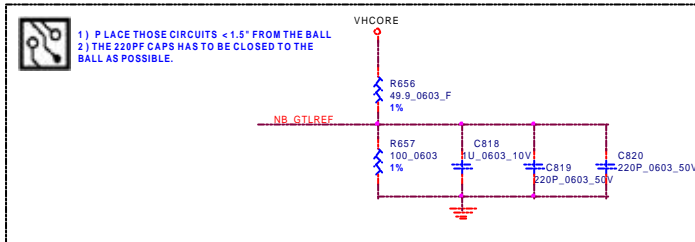
03

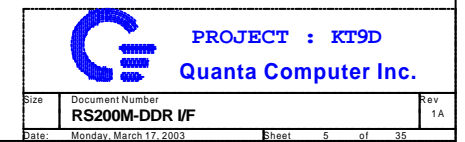


PROJECT : KT9D
Quanta Computer Inc.

Size	Document Number	Rev
	NORTHWOOD -B	1A
Date:	Monday, March 17, 2003	Sheet 3 of 35

Date:	Monday, March 17, 2003	Sheet	3	of	35
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06

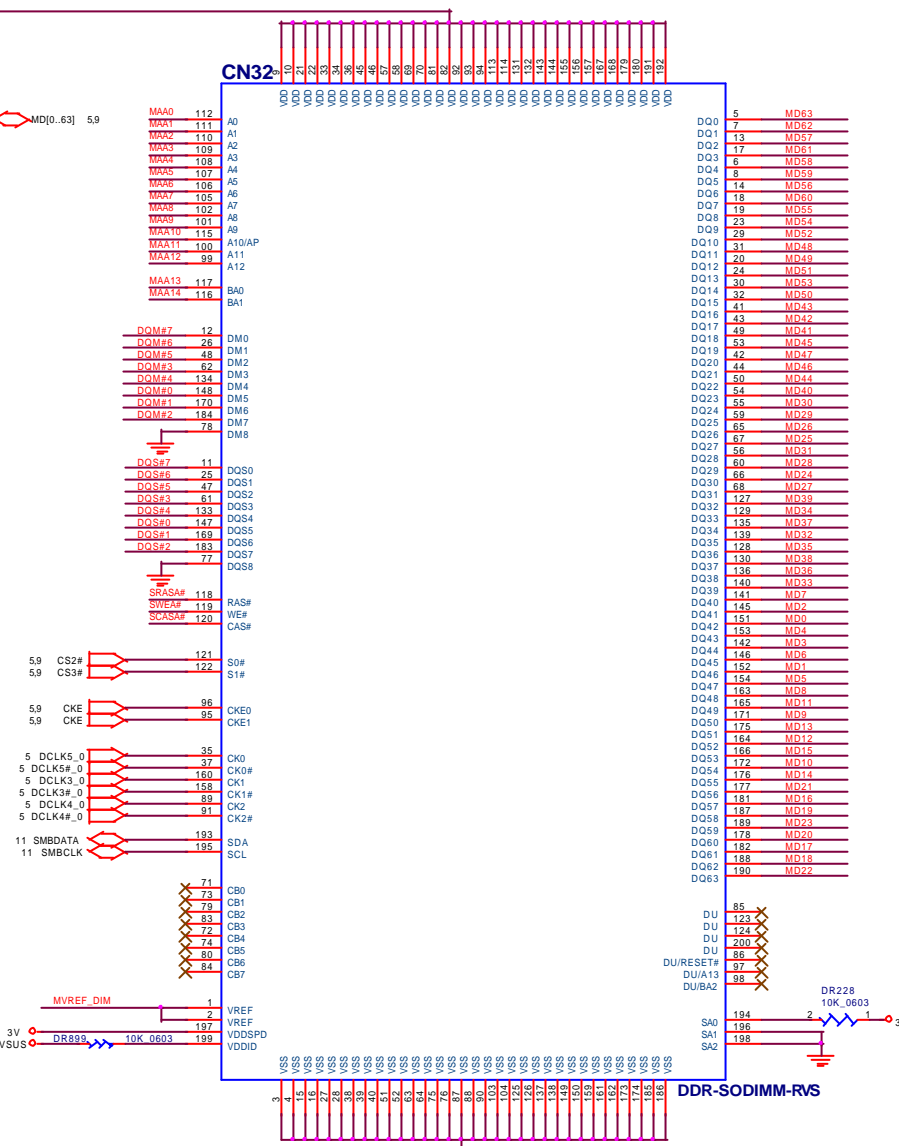
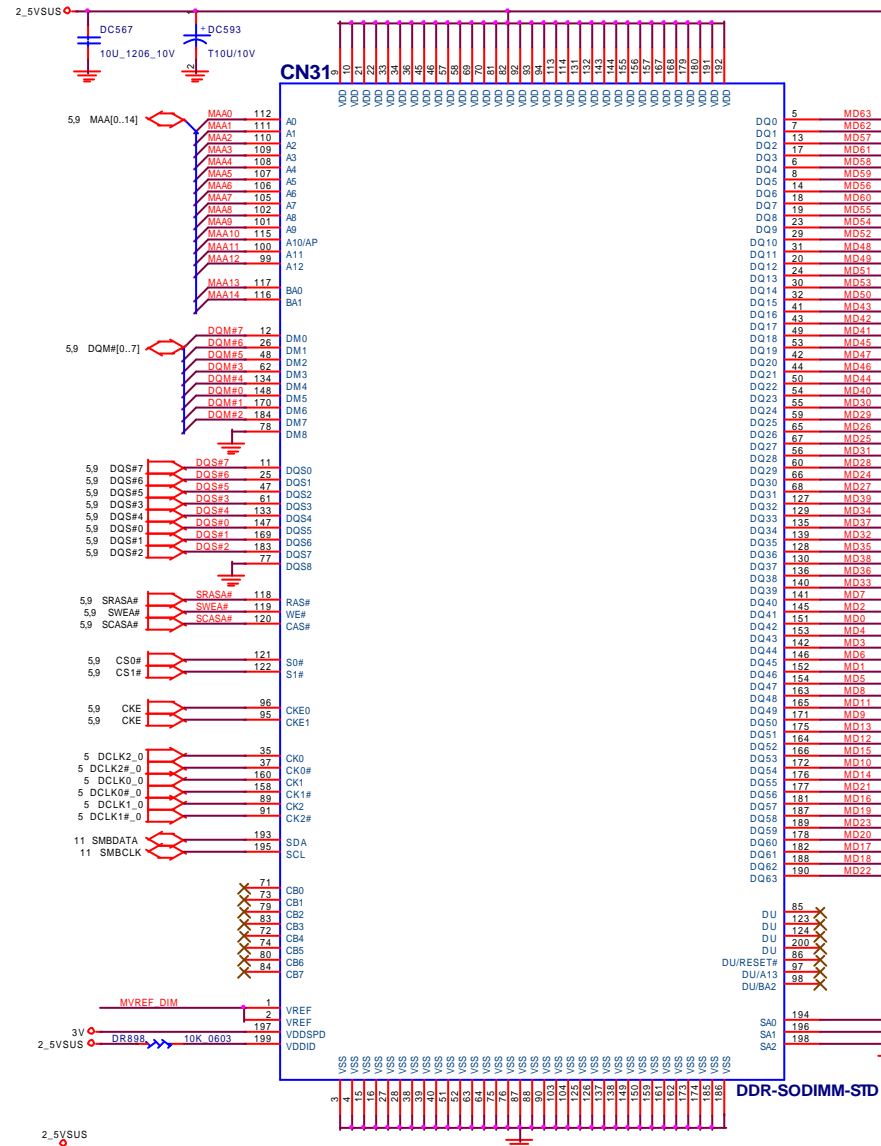


Size Document Number Rev
RS200M-PCI/AGP I/F 1A
Date: Monday, March 17, 2003 Sheet 6 of 35



DDR - Modules

08



PROJECT : KT9D

Quanta Computer Inc.

Size	Document Number	Rev
	DDR MODULES	1A
Date	Monday, March 17, 2003	Sheet 8 of 35

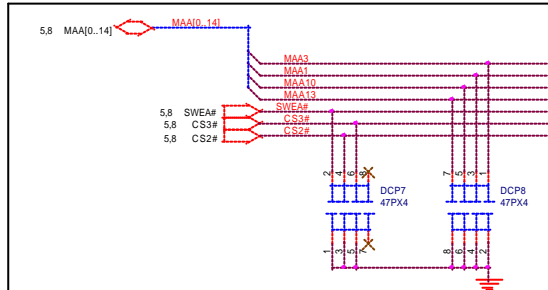
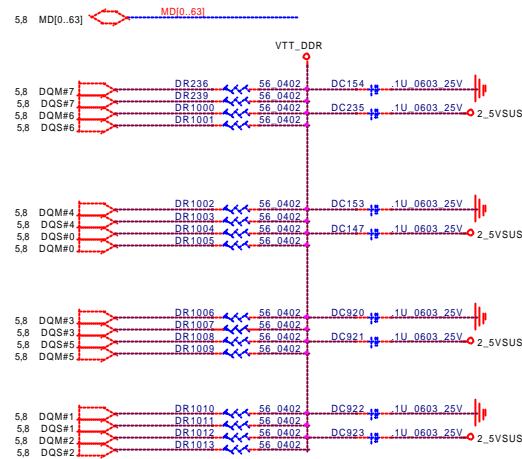
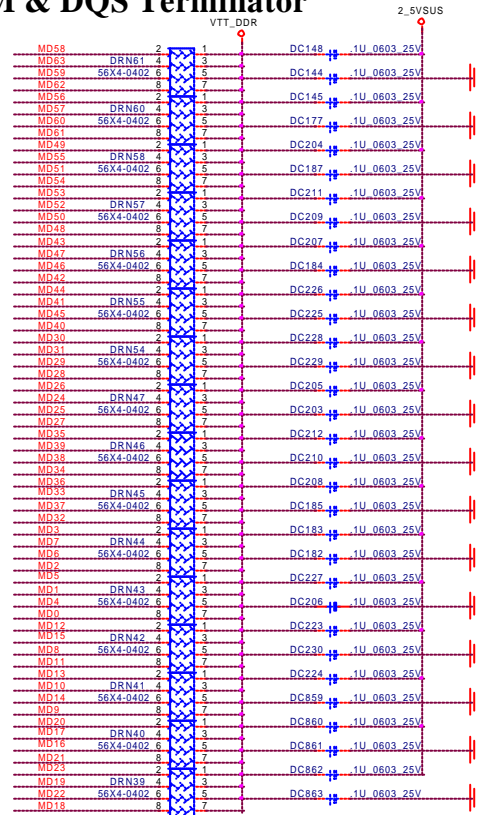
DDR TERMINATOR



Could swap pin for layout .

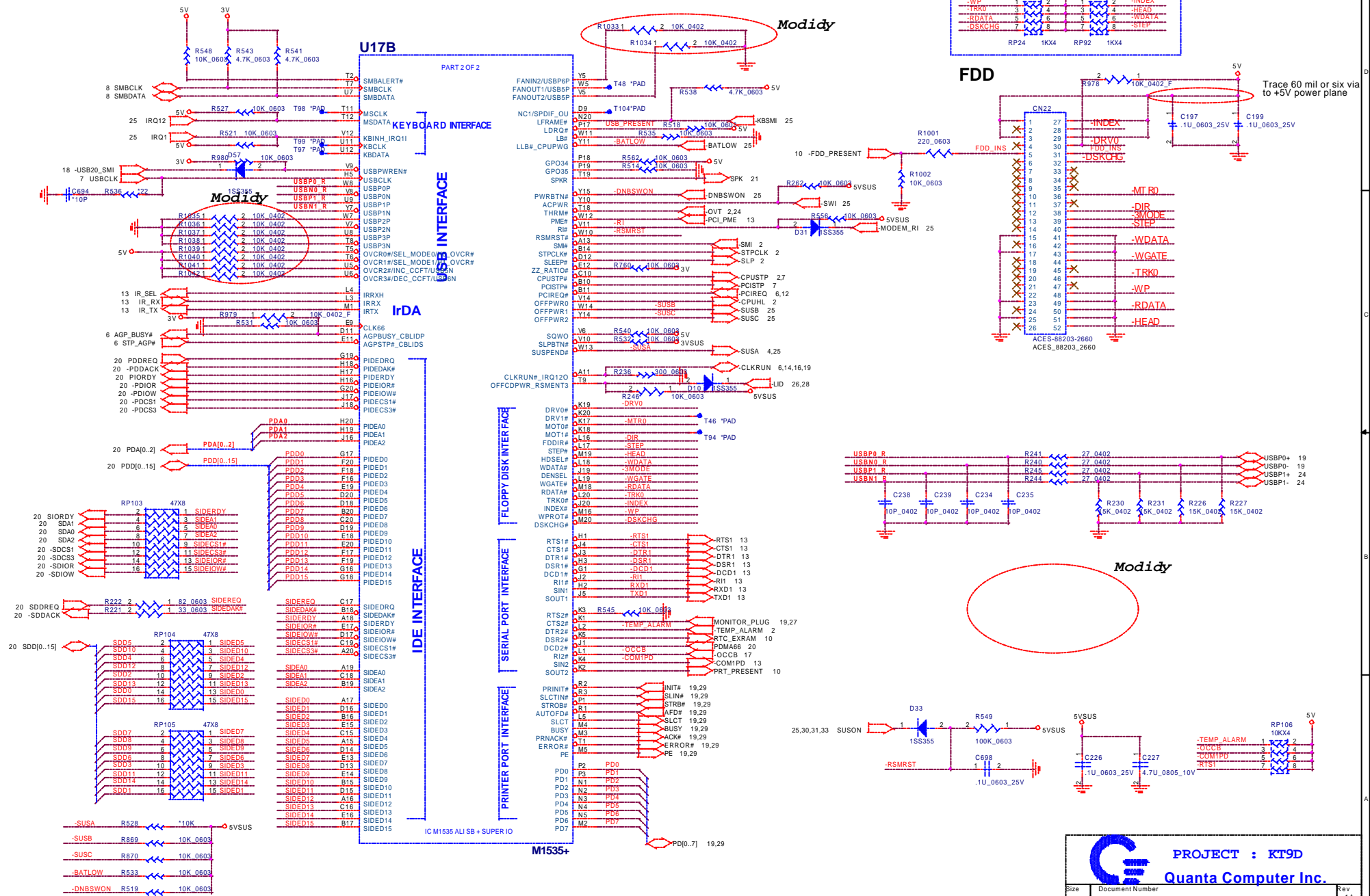
09

MD & DQM & DQS Terminator

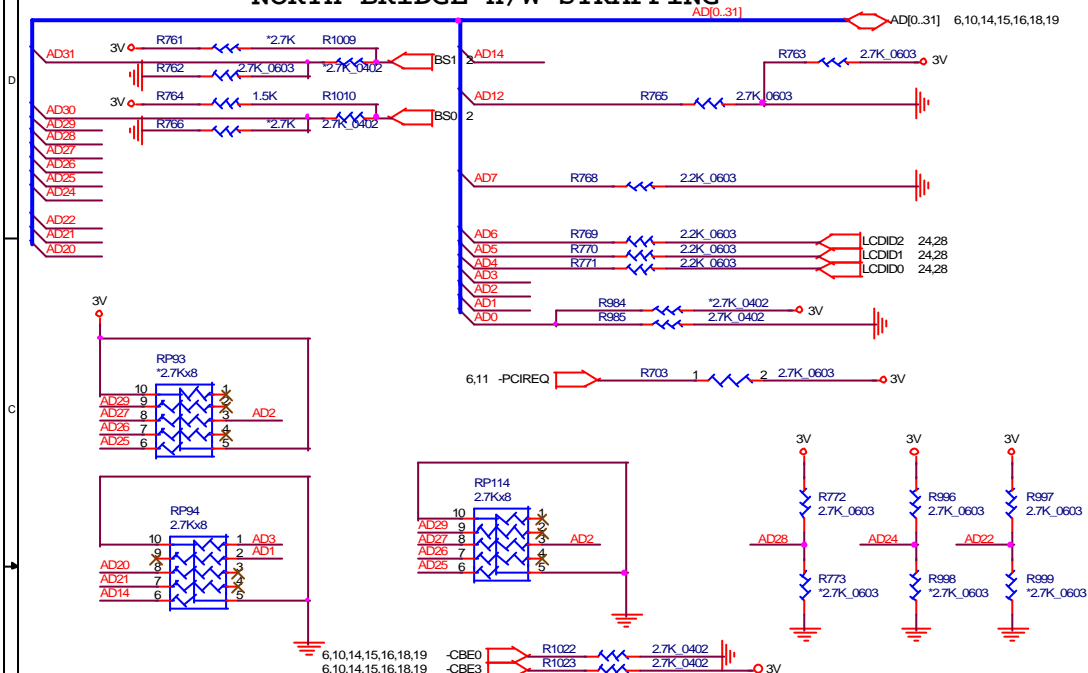


M1535+_USB,IDE,FDD INTERFACE

11



NORTH BRIDGE H/W STRAPPING



AD[31..30] : CLK SPEED DEFAULT: 00	00: 100 MHz 01: 133 MHz 10: 166MHz 11: 66 MHz	AD15 : RESERVED	
AD29: RS200M FULL CONFIGURATION DEFAULT: 0	0: FULL CONFIGURATION 1: USES DEFAULT VALUES	AD14 : AGTL+ VDDQ DEFAULT: 0	0: 1.2V MOBILE PART 1: 1.7V DESKTOP PART
AD28: SPREAD SPECTRUM ENABLE DEFAULT: 0	0: DISABLE 1: ENABLE	AD13: RESERVED	
AD27: MCERR OBSERVATION DEFAULT: 0	0: DISABLE 1: ENABLE	AD[12] : FLAT PANEL ID MSB	
AD26: QUICK START SELECT DEFAULT: 0	0: DISABLE 1: ENABLE	AD10 : RESERVED	
AD25: INIT OBSERVATION DEFAULT: 0	0: DISABLE 1: ENABLE	AD09 : RESERVED	
AD24: IOQ ENABLE DEFAULT: 1	0: DISABLE 1: ENABLE	AD[7..4] : FLAT PANEL ID	
AD23 : RESERVED		AD3: PC166 MODE DEFAULT: 0	0: 33MHz 1: 66MHz
AD22: 27MHz/33 MHz CLOCK SELECTION DEFAULT: 1	0: 33 MHz CLOCK OUTPUT 1: 27 MHz CLOCK INPUT	AD2: CAL DEFAULTS FOR CPU DEFAULT: 0 (FOR BRING UP)	0: DISABLE 1: ENABLE
AD21 : RESERVED	0: PLL Bypass mode disable(Normal mode) 1: PLL Bypass mode enable(Debug only)	AD1: FrcShortReset DEFAULT: 0	0: DISABLE 1: ENABLE
AD20 : PCICLK EXPANSION DEFAULT: 0	1: PCI_REQ#3, PCI_GNT#3 USED AS PCICLK 0: PCI_REQ#3, PCI_GNT#3 USED AS REQ/GNT	AD0: PCI/AGP CONFIG. DEFAULT: 0	0: 33 MHz 1: 66 MHz
CBE#3: PROD TEST DEFAULT: 1	0: SHORT TIMERS 1: NORMAL OPERATION	-PCI_REQ: INTERNAL CLOCK GEN. DEFAULT: 1	0: DISABLE 1: ENABLE

	DTR2#	RTS2#	RTS1#	CLK320	SPLED	SQWO
PULL HIGH	INTERNAL KEYBOARD PS2 MODE DEFAULT	ENABLE INTERNAL KEYBOARD DEFAULT	SIO BASE ADDRESS 0X3F0h DEFAULT	32K CLOCK TEST MODE ENABLE DEFAULT	AT POWER MODE DEFAULT	PENTIUM II MODE DEFAULT
PULL LOW	INTERNAL KEYBOARD AT MODE DEFAULT	DISABLE INTERNAL KEYBOARD DEFAULT	SIO BASE ADDRESS 0X370h DEFAULT	32K CLOCK TEST MODE DISABLE DEFAULT	ATX POWER MODE DEFAULT	PENTIUM / K7 MODE

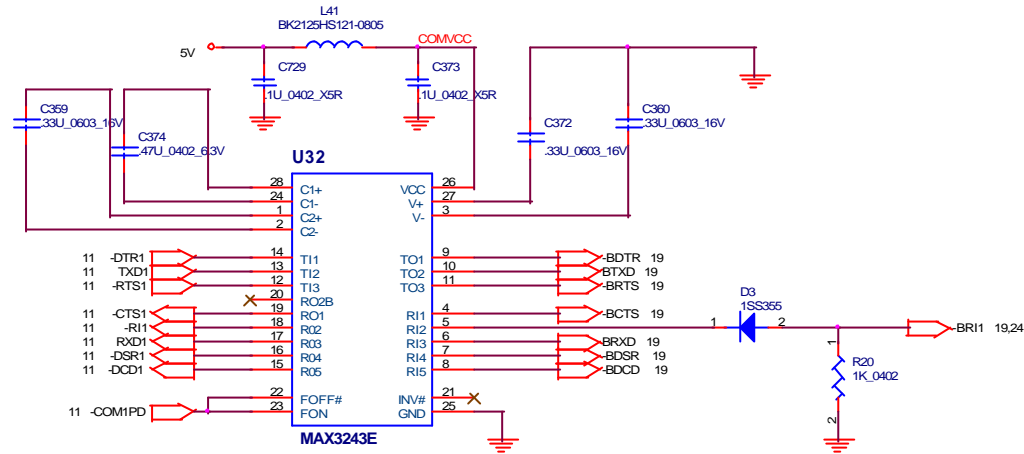
PCS1#	PCS0#	GPO34	LB#	GPO30[AEN]	GPO29	ROMCS#	GPO35
GPI24 & GPO30 FUNCTIONS	DISABLE 4Mb FLASH ROM MODE	CHIP TEST MODE DISABLE DEFAULT	ACTIVE HIGH TO POWER OFF	L,L - USB TEST MODE L,H - AUDIO/MODEM TEST MODE H,L - SUPER IO TEST MODE		<u>FLASH ROM MODE SELECTION</u> L,L - RESERVED L,H - ENABLE 8Mb Flash ROM SIZE H,L - DISABLE ALL FUNC. IN THIS CONFIG. H,H - ENABLE LPC FLASH ROM	
IOCHRDY & AEN FUNCTIONS	ENABLE 4Mb FLASH ROM MODE	CHIP TEST MODE ENABLE	ACTIVE LOW TO POWER OFF	H,H - NORMAL OPERATION			
DEFAULT	DEFAULT		DEFAULT	DEFAULT: H,H		DEFAULT: H,L	



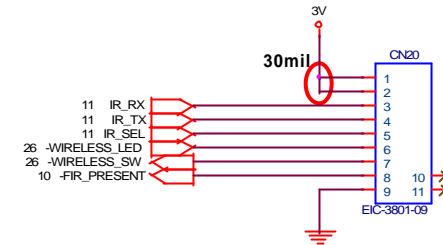
PROJECT : KT9D
Quanta Computer Inc.

Size	Document Number	Rev
	N.B & S.B H/W STRAPPING	1A
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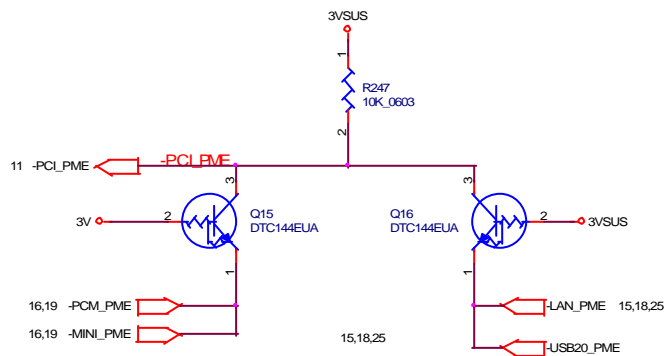
SERIAL PORT



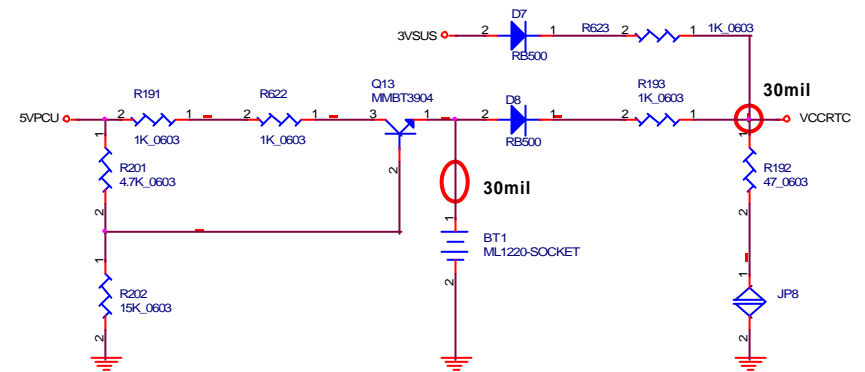
FIR CON



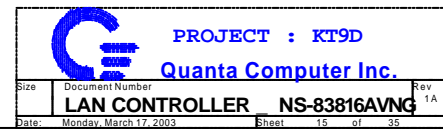
PME CONTROL

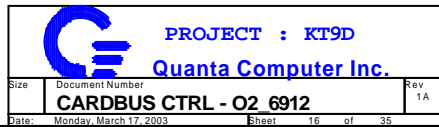


RTC POWER CIRCUIT

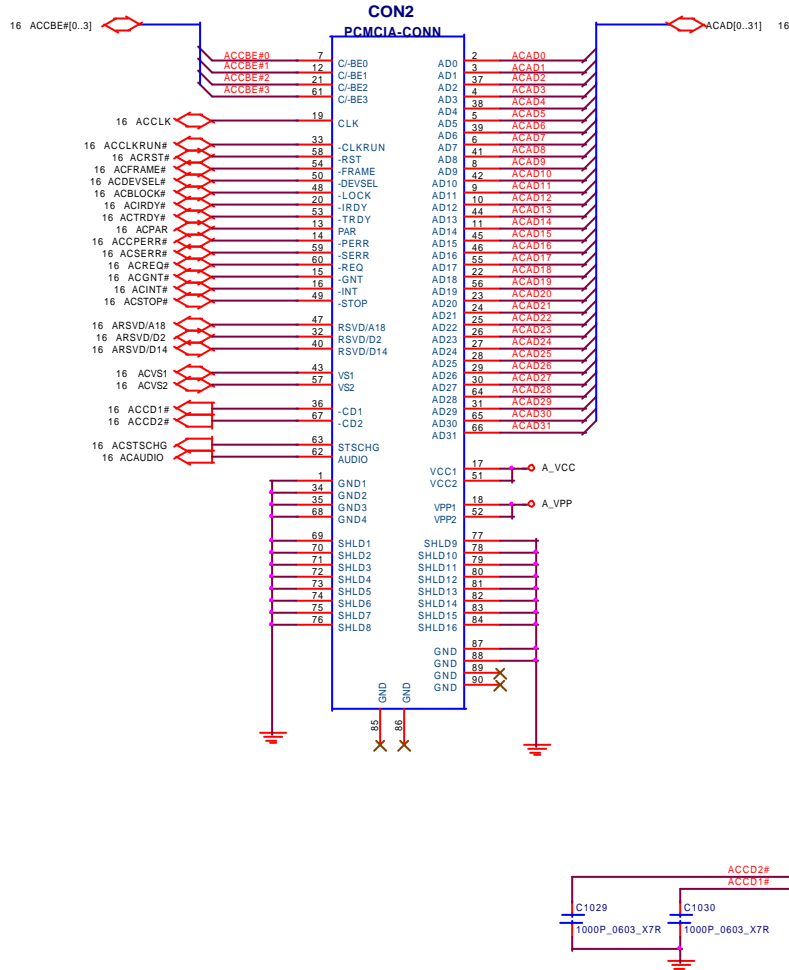


14

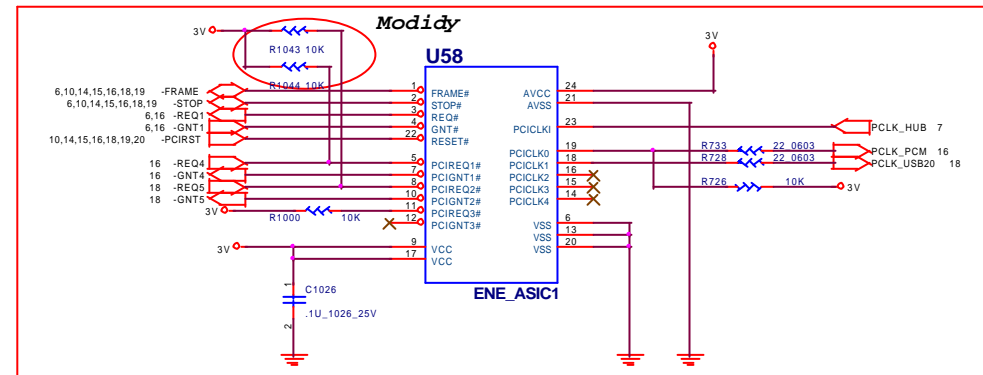


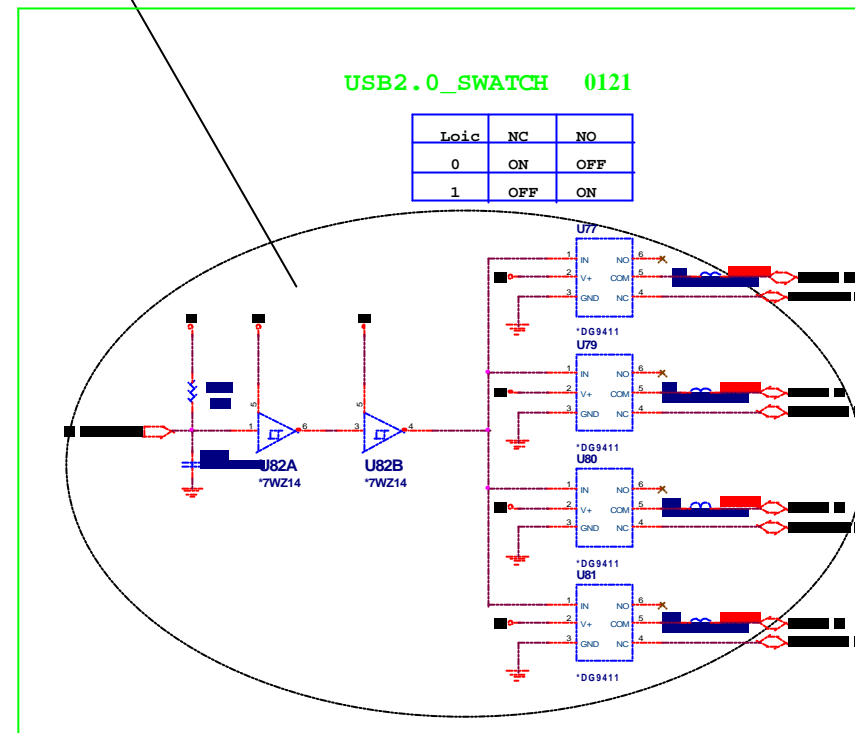
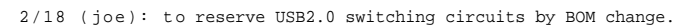


CARDBUS POWER SWITCH



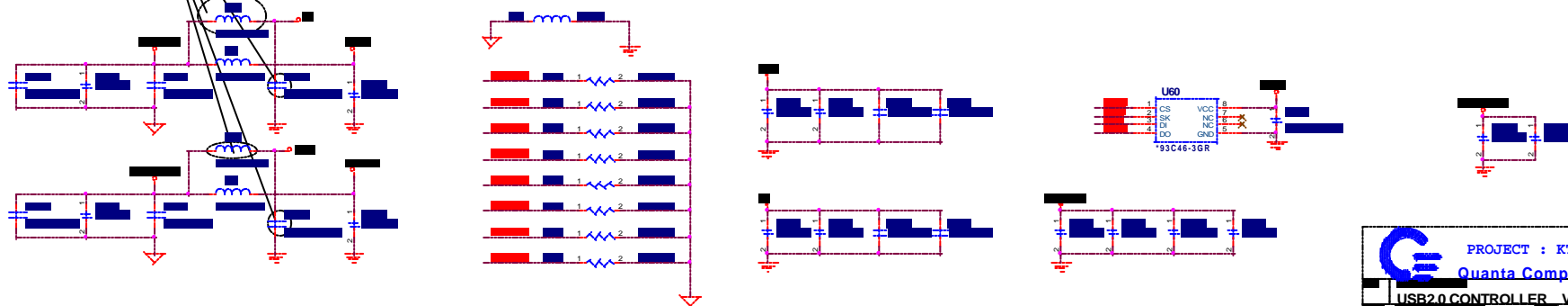
PCI HUB_ENE-ASIC1



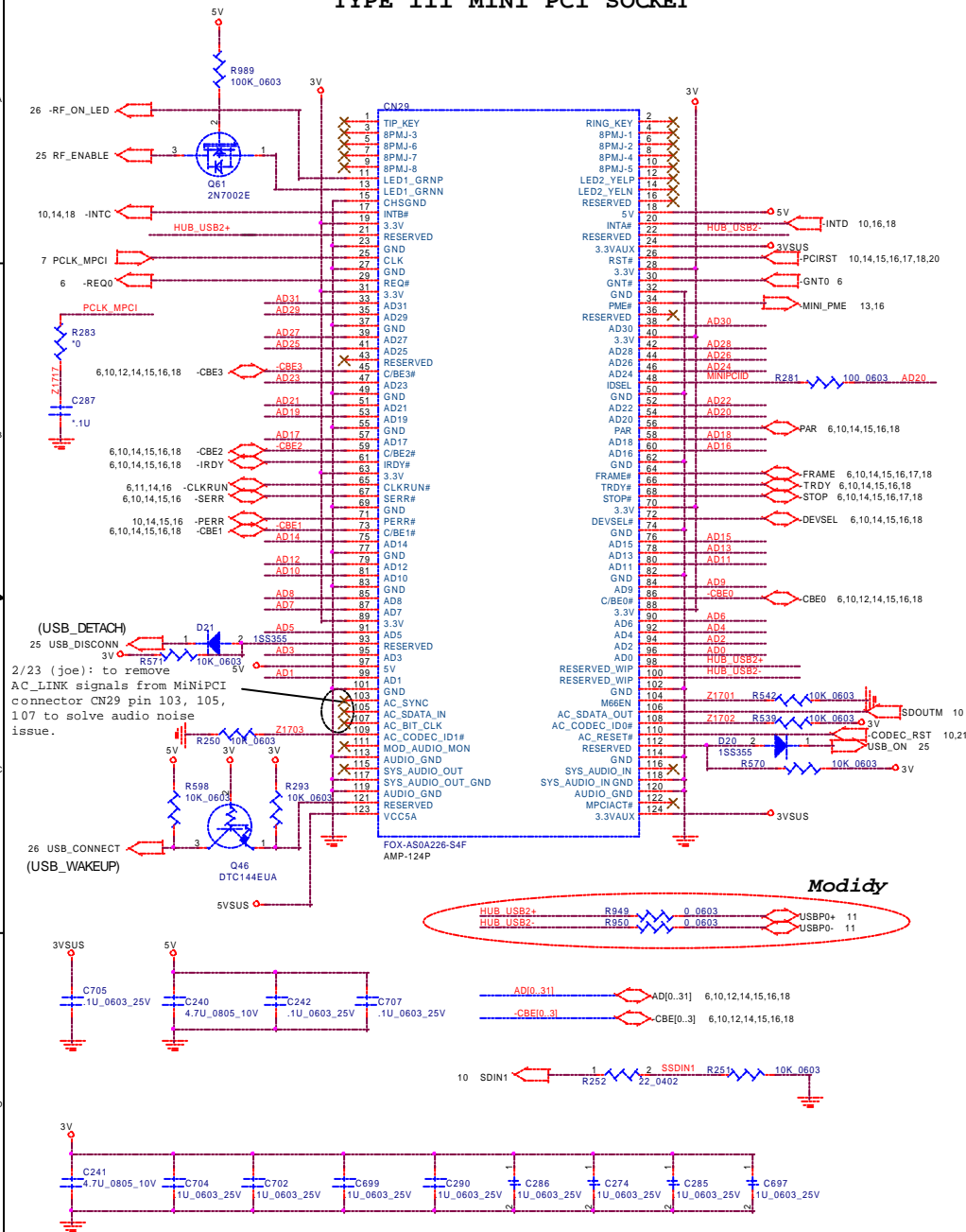


2/27 page 18 (joe): to
add L102, L103, C1209
and C1210 for power
source select.

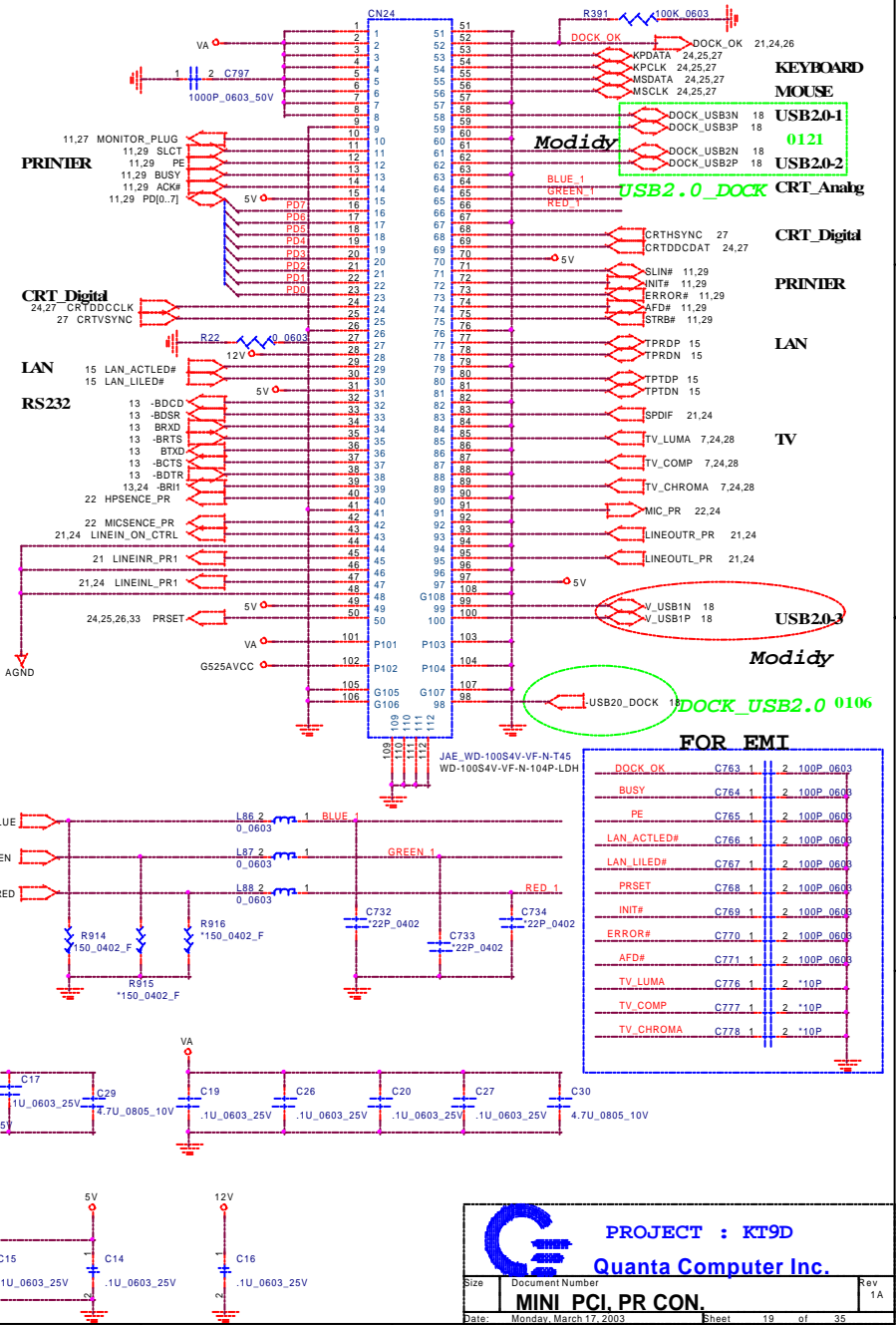
2/23 page 18 (joe): to
remove R805 and mount
R806 as disable USB
wake up feature.



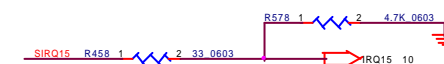
TYPE III MINI PCI SOCKET



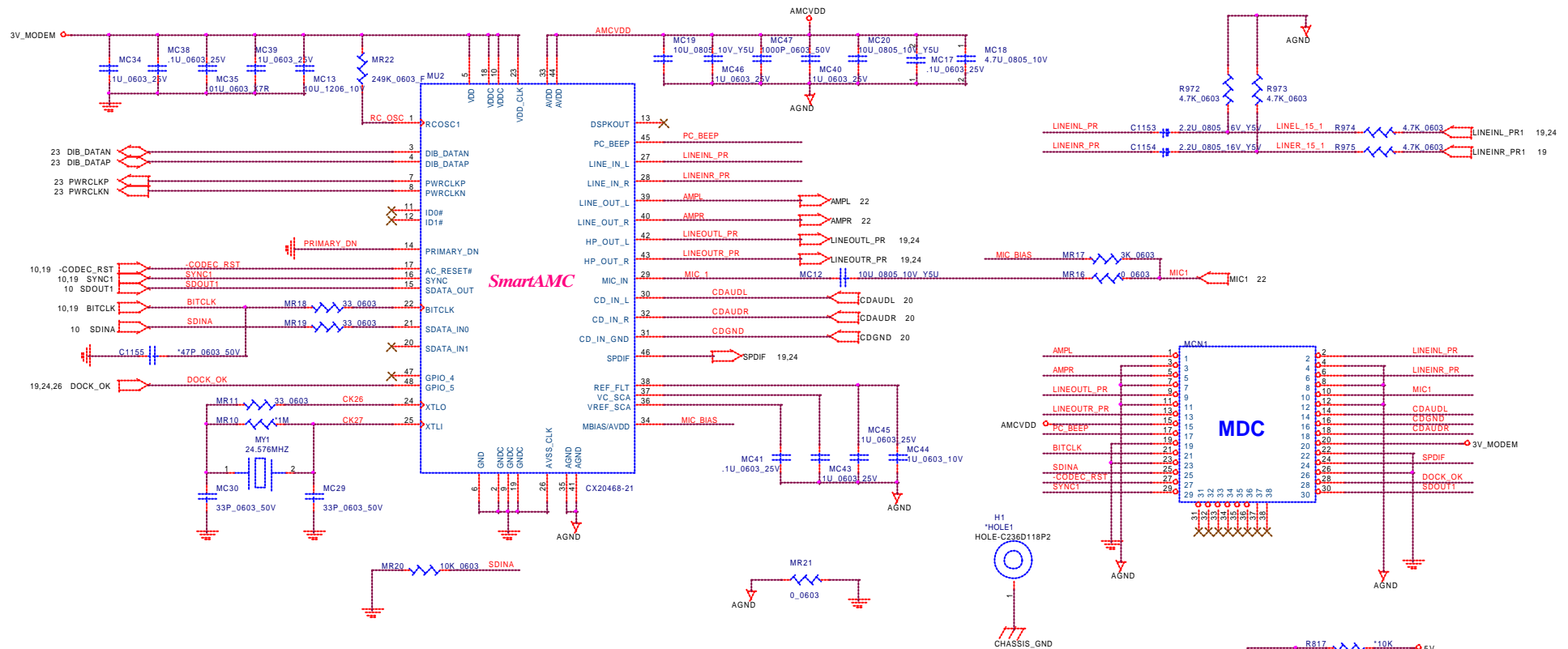
PORT REPLICATOR Connector



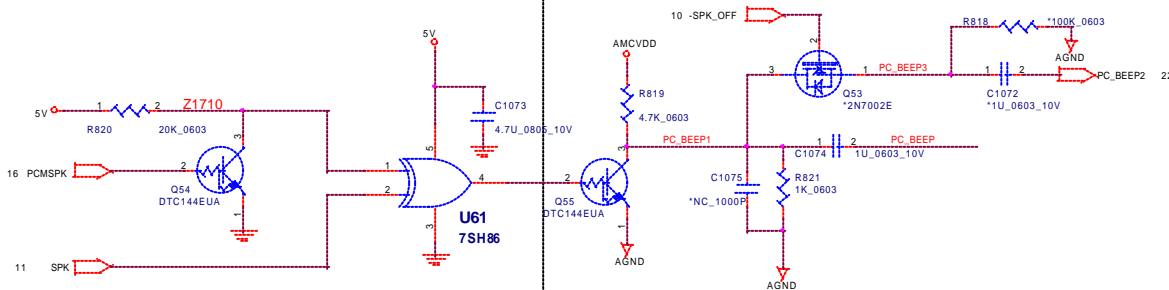
40MIL




The AMC20493-001 modem is used for mother board family MBAMC20493-006.



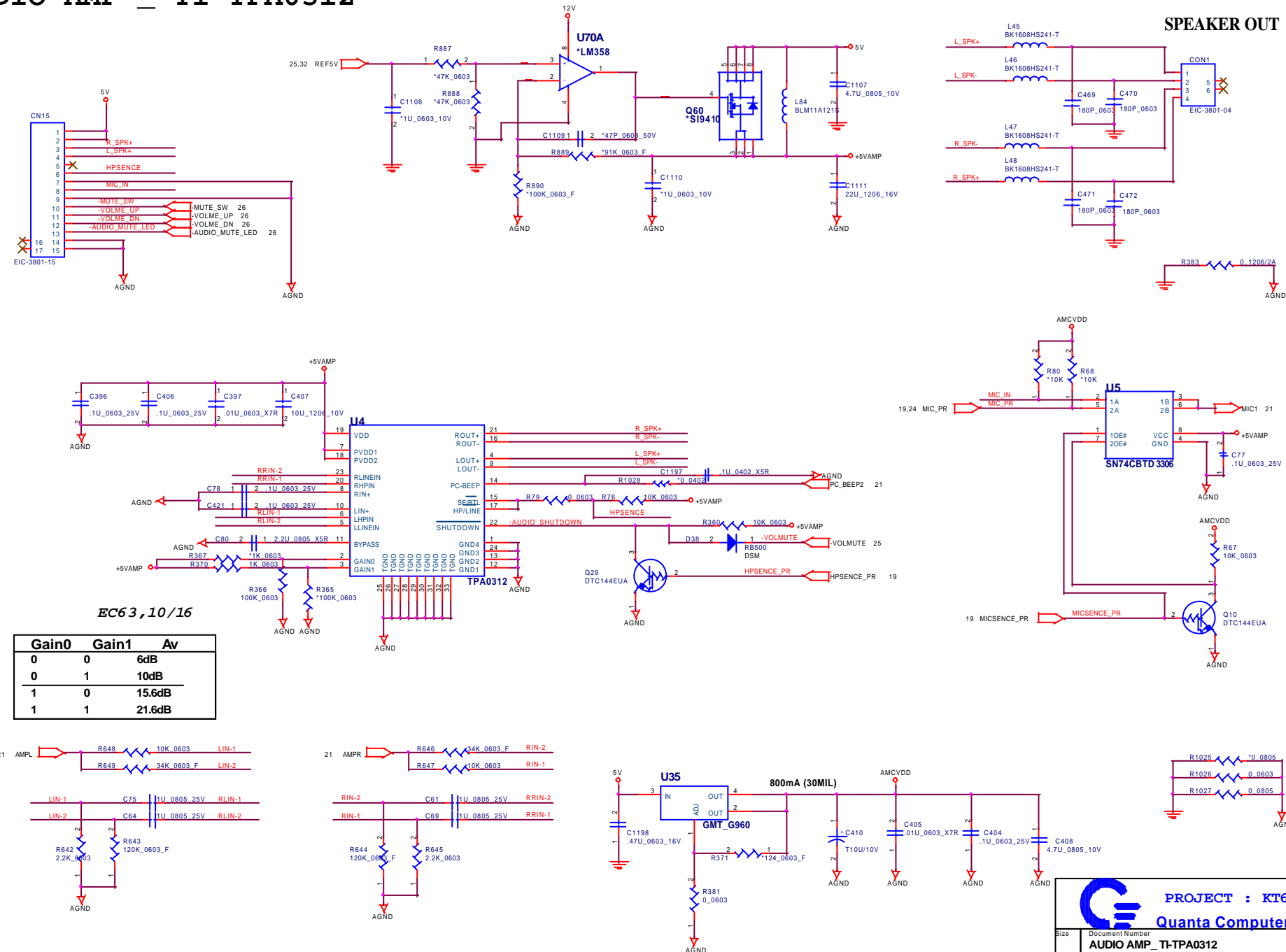
PC SPEAKER





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Quanta Computer Inc.

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	AMC97 CODEC	1A
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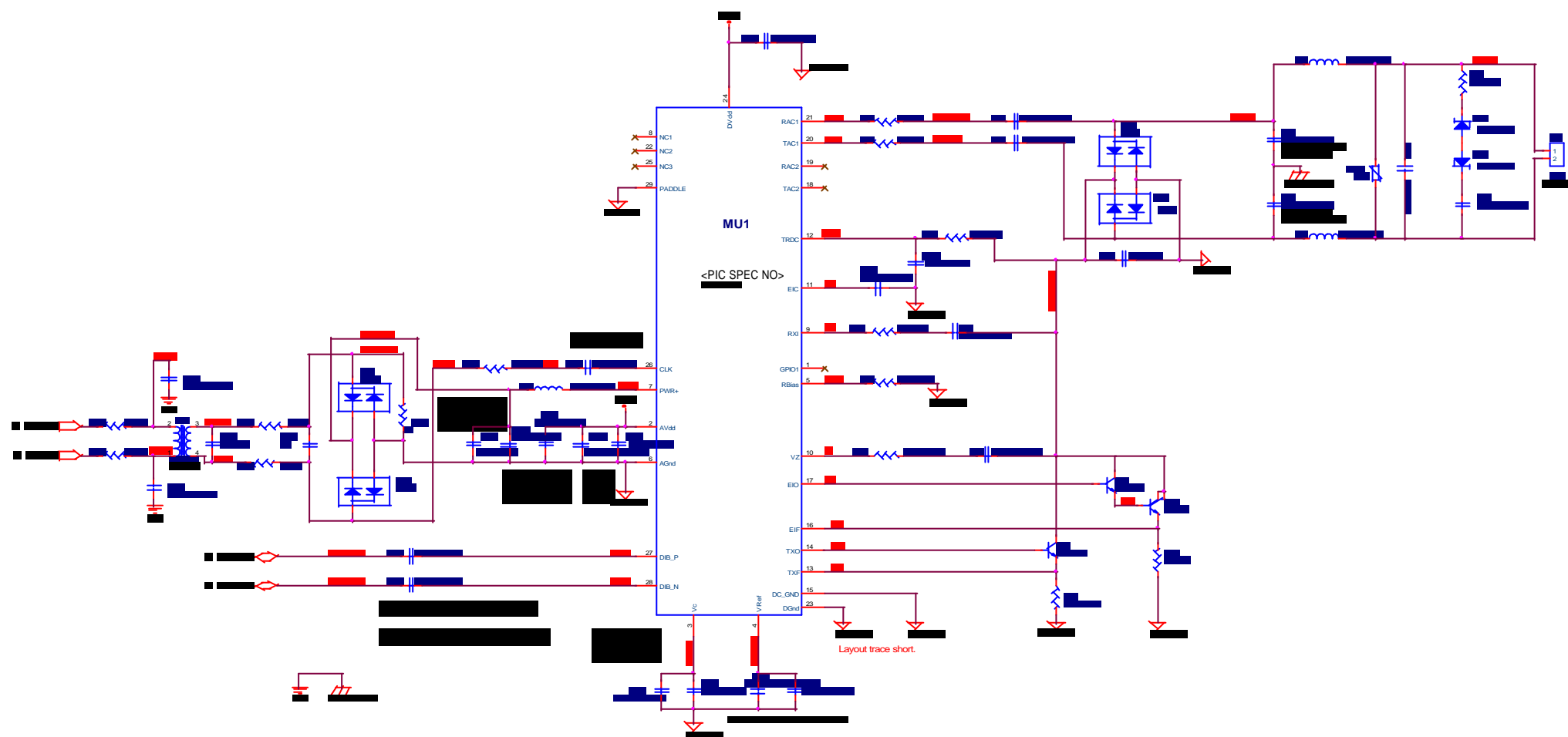


SPEAKER OUT

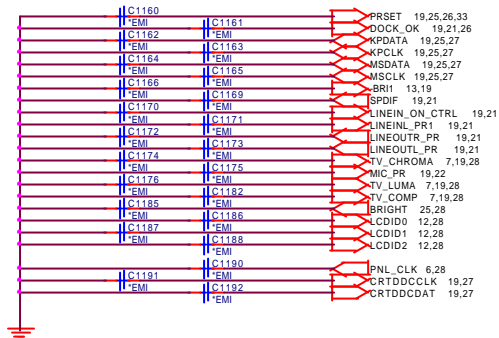


PROJECT : KT6B
Quanta Computer Inc.

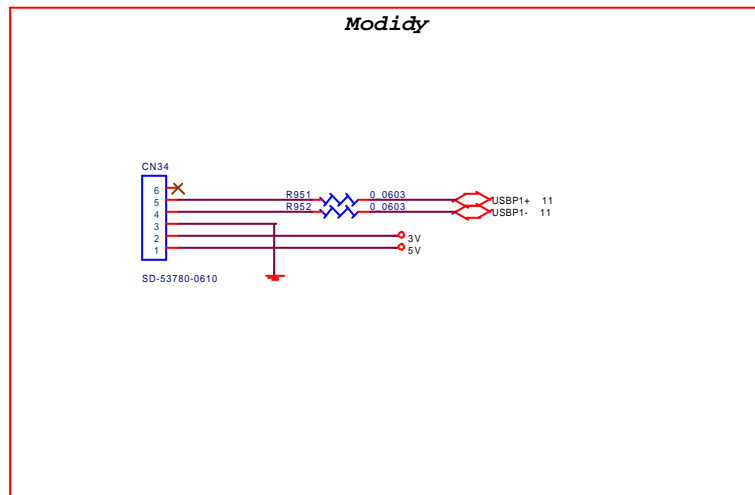
Size	Document Number	Rev
	AUDIO AMP _ TI-TPA0312	1A
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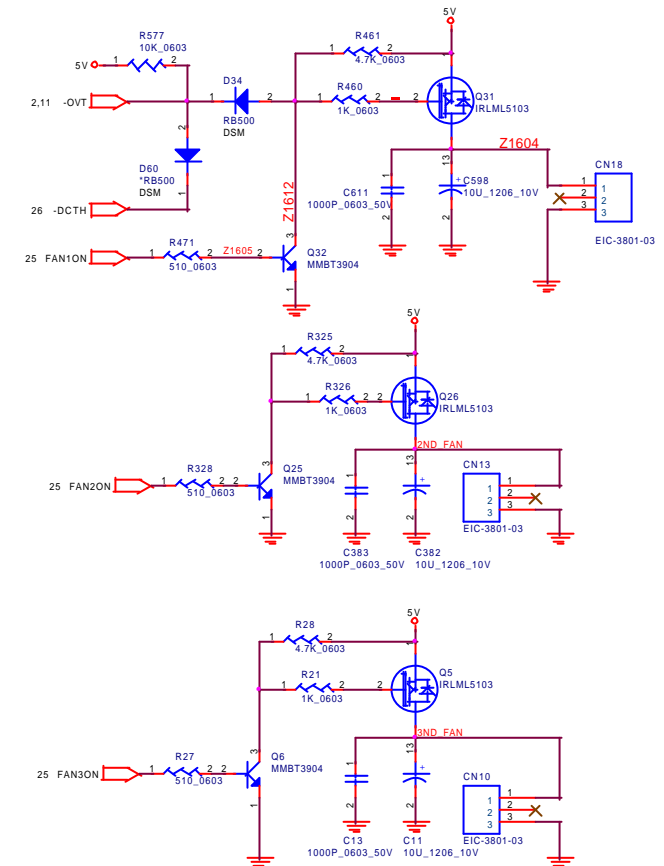
EMI



CARD READER (Option)



FAN CONTROL



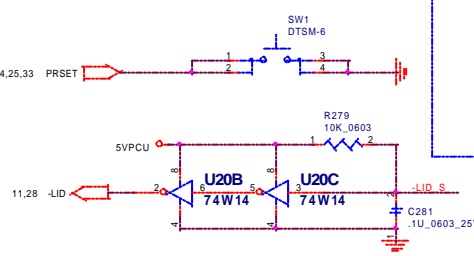
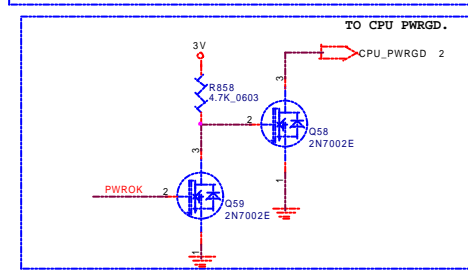
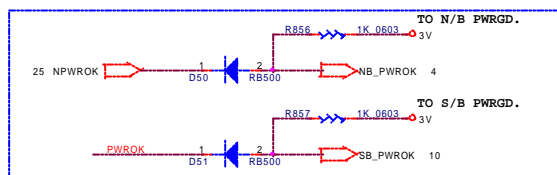
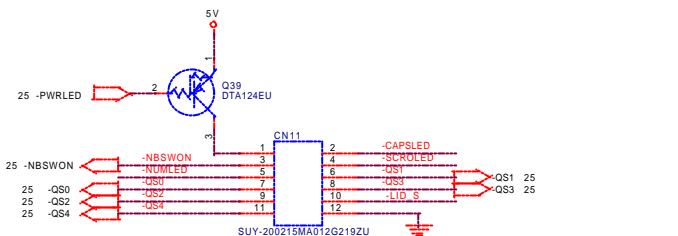


LED & SWITCH & KBC & TOUCHPAD INTERFACE

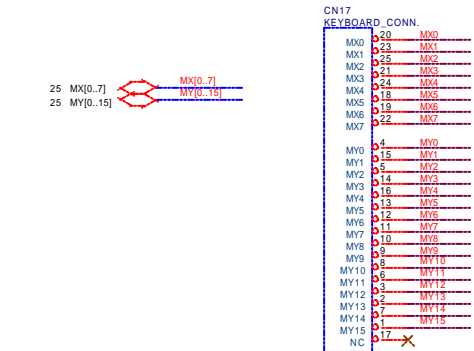
26

LED BOARD CON

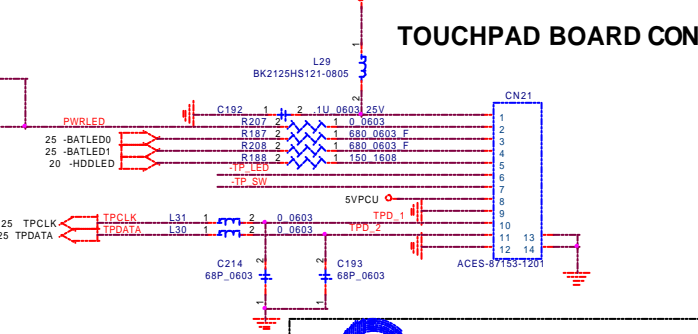
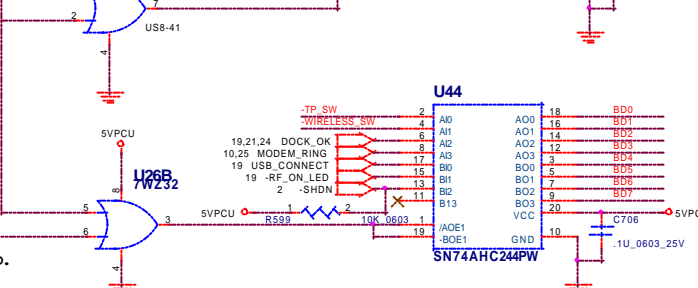
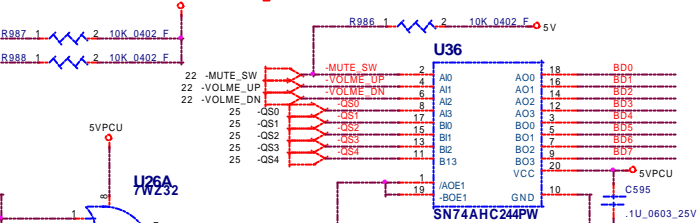
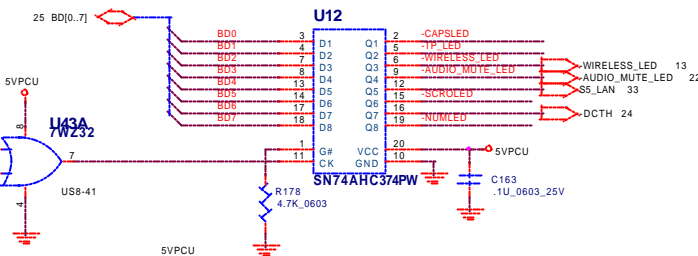
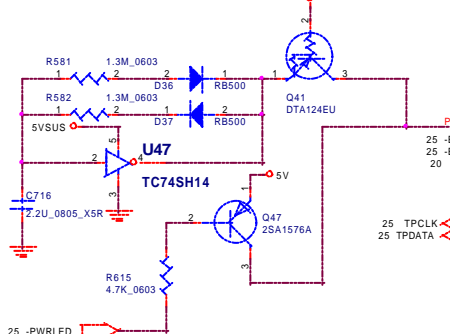
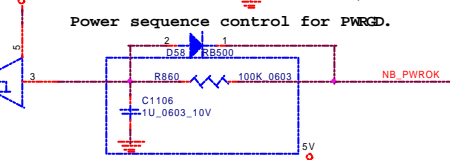
-NBSWON	C805	100P_0603
-QS0	C806	100P_0603
-QS4	C807	100P_0603
-NUMLED	C808	100P_0603
-QS2	C809	100P_0603
-CAPSLED	C810	100P_0603
-SCROLED	C811	100P_0603
-QS1	C812	100P_0603
-QS3	C813	100P_0603
-LID_S	C814	100P_0603



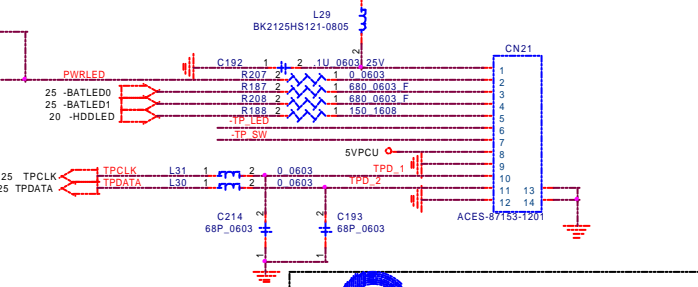
KEYBOARD CONNECTOR



For_ATI_PWG function



TOUCHPAD BOARD CON

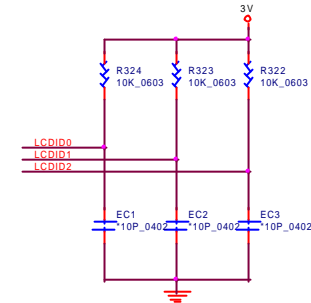
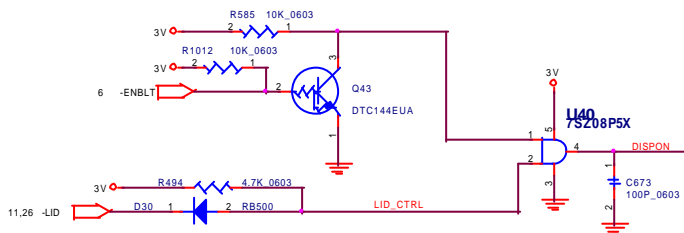
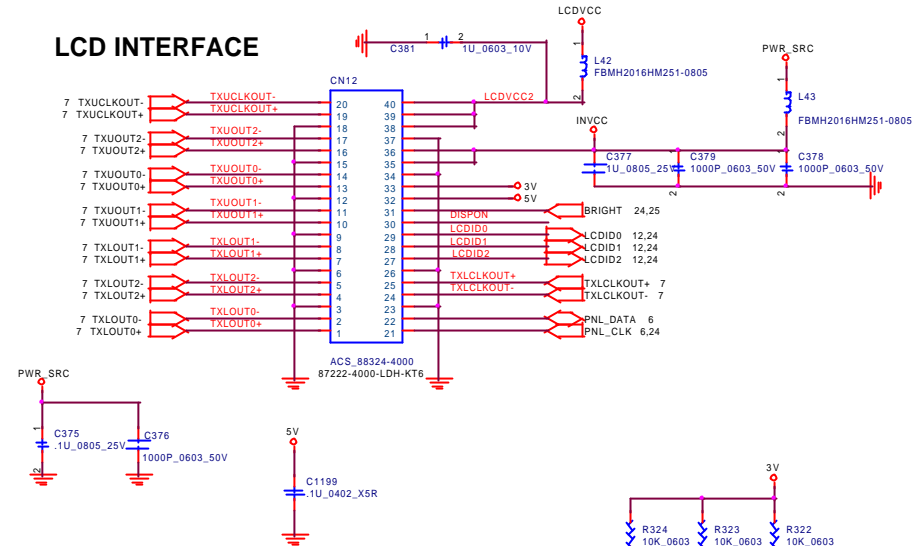


PROJECT : KT9D
Quanta Computer Inc.

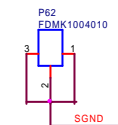
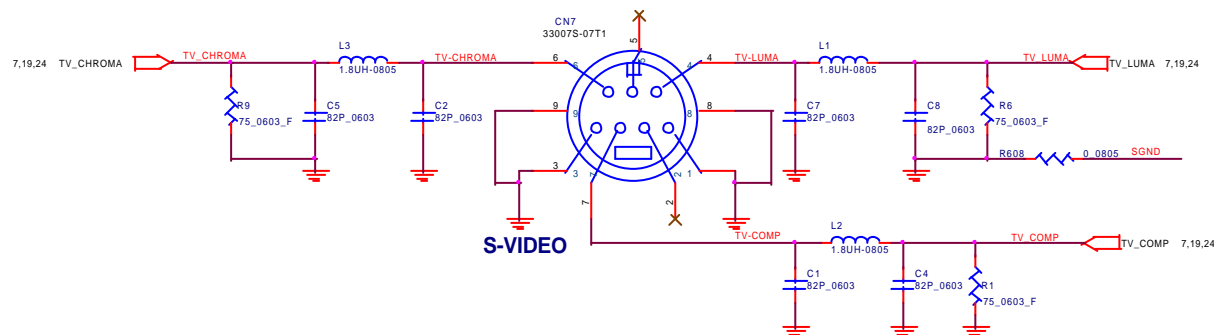
Size Document Number
LED & SWITCH & KBC & TOUCHPADIF Rev 1A

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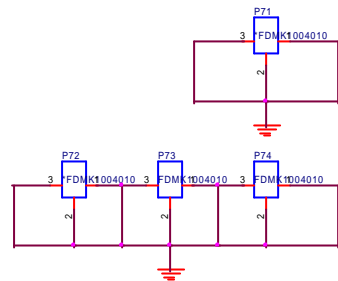
LCD INTERFACE



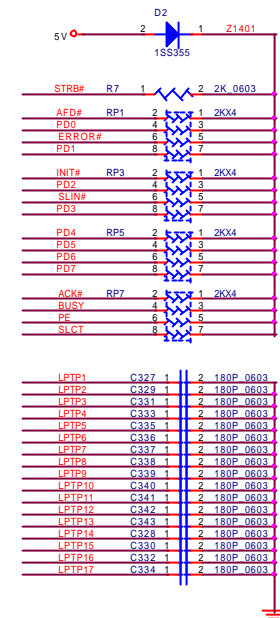
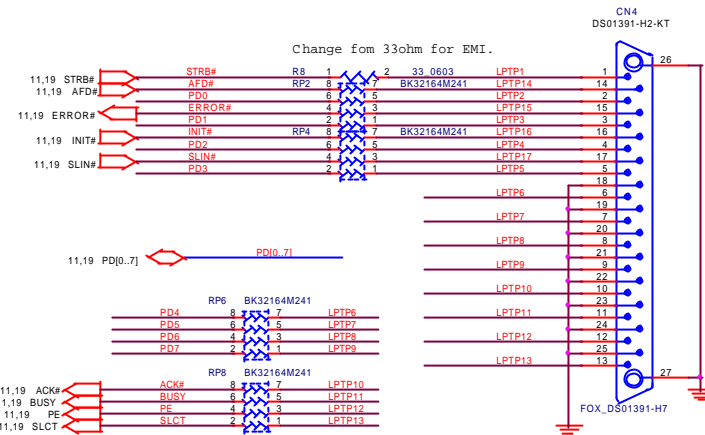
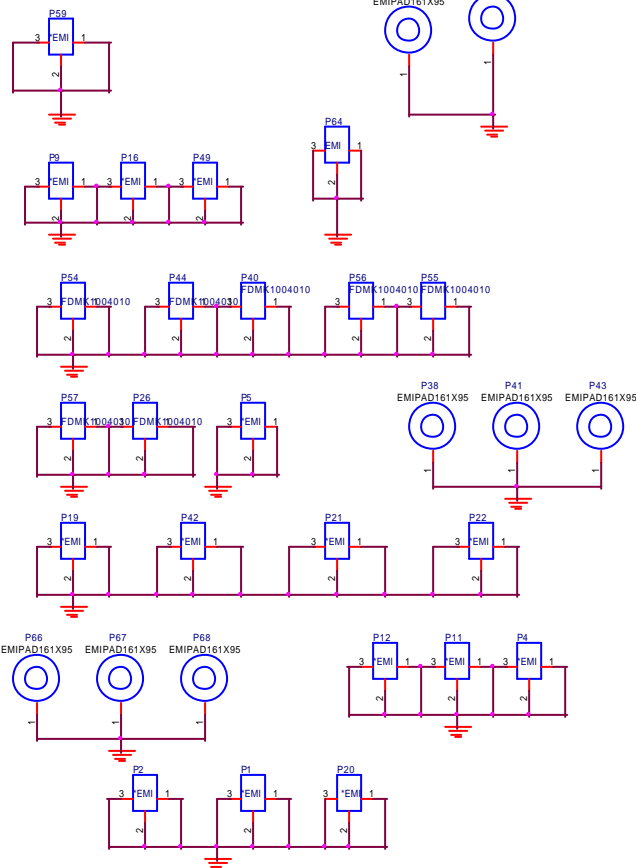
S-VIDEO



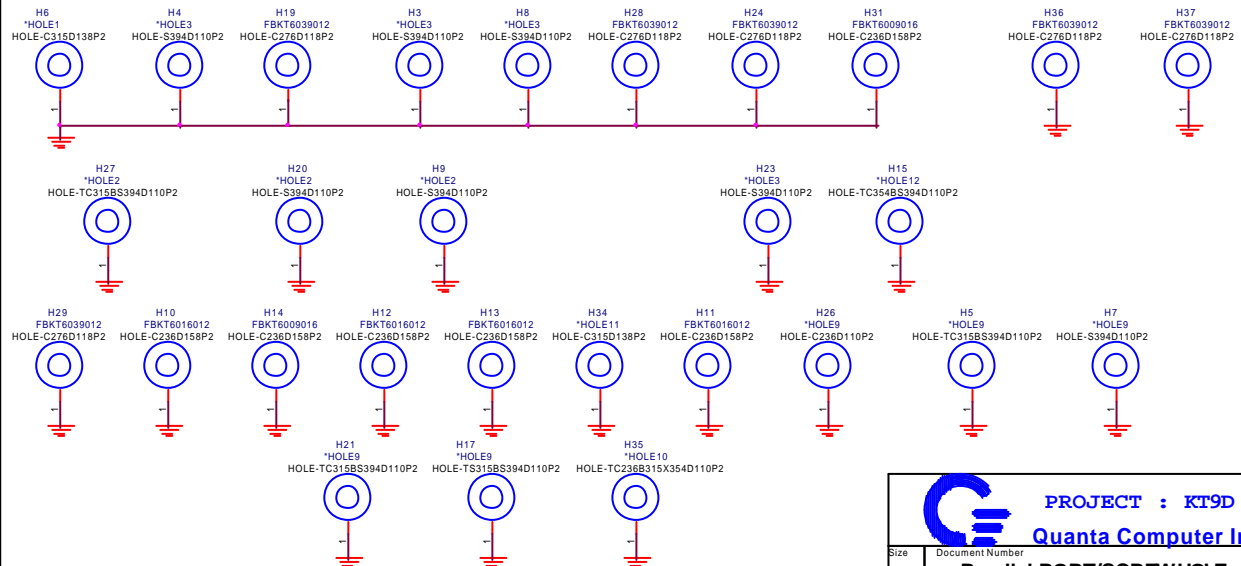
PARALLEL PORT INTERFACE



EMI FENSES

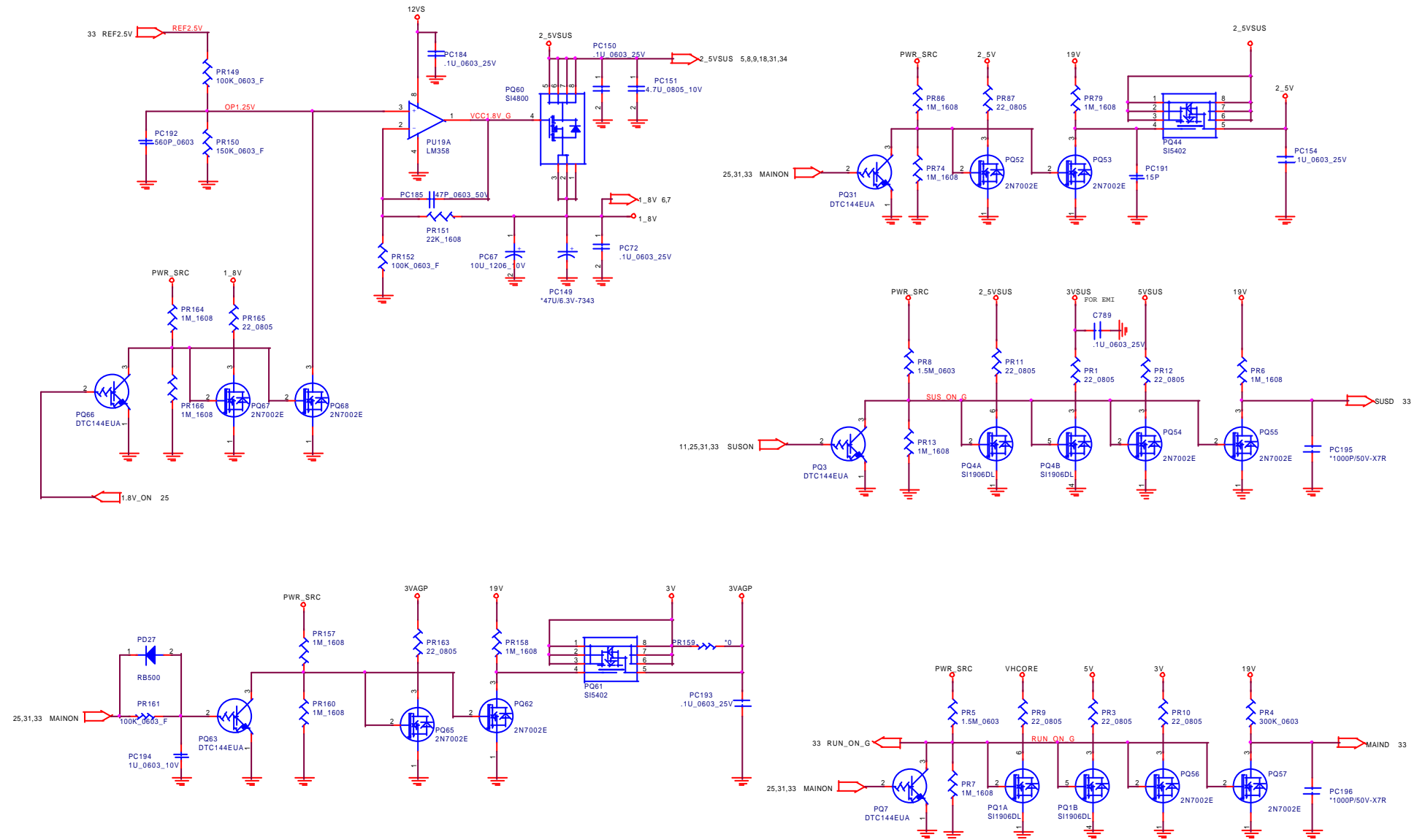


SCREW HOLES



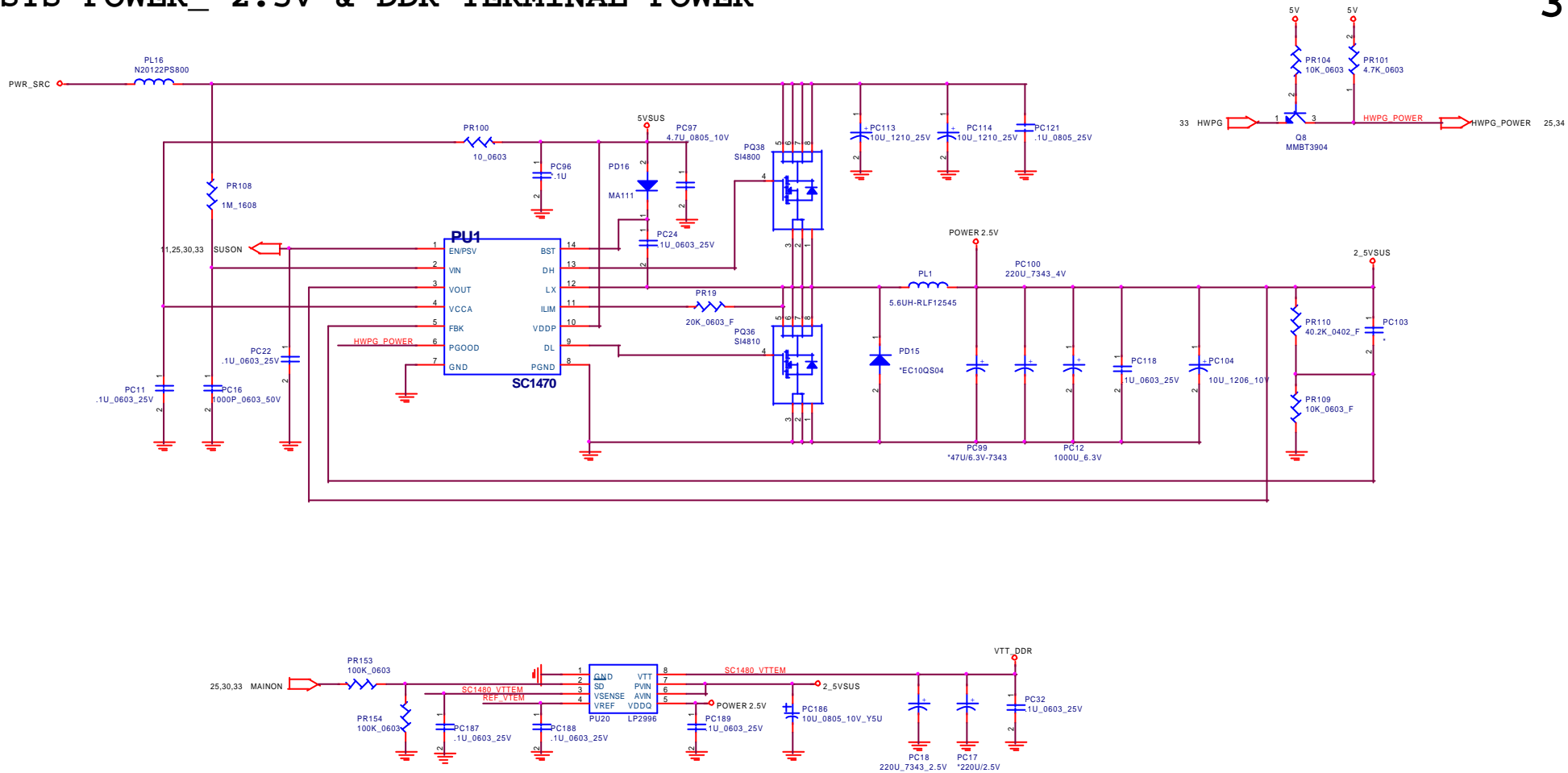
DISCHARGE CIRCUIT

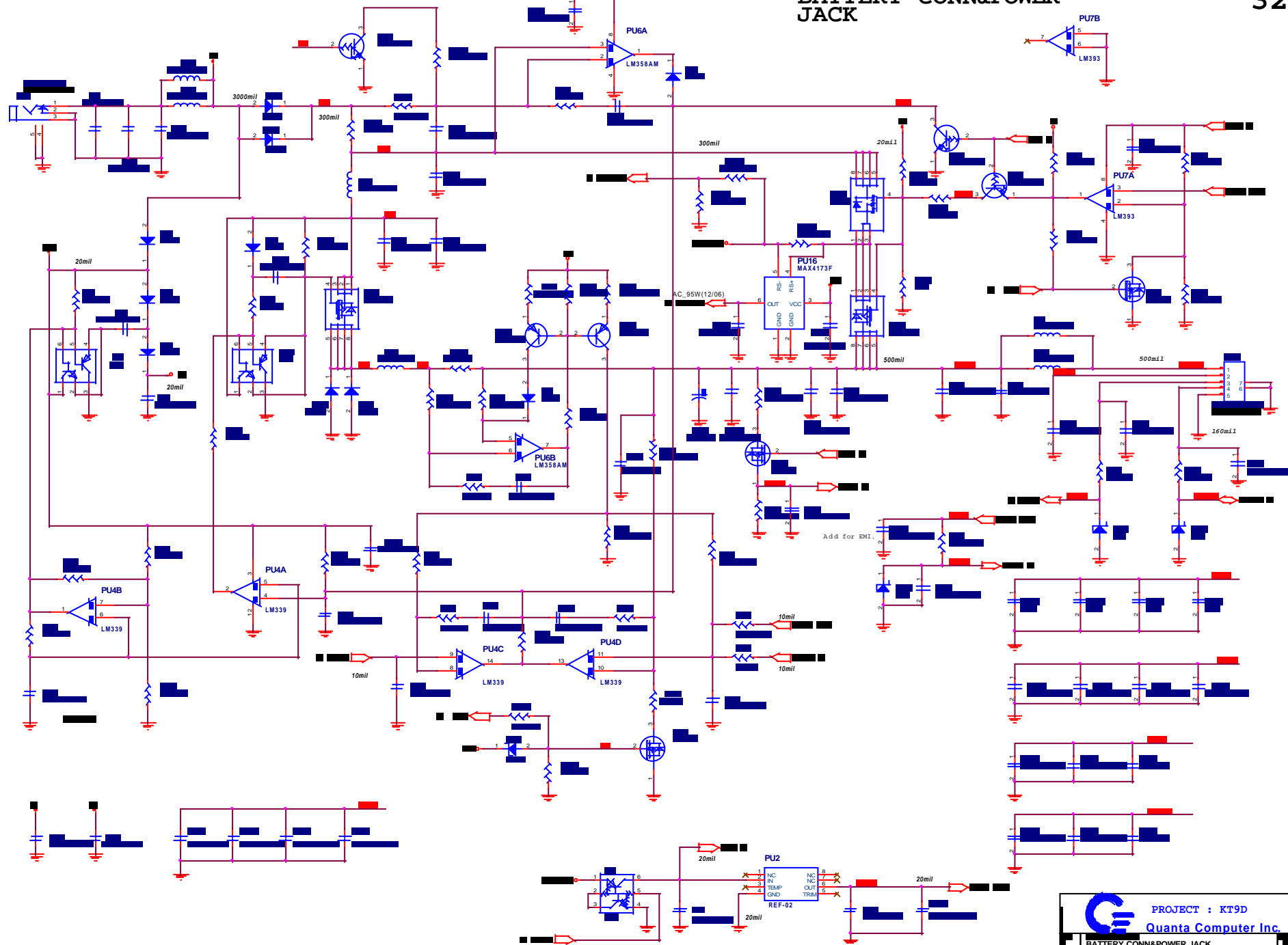
30



SYS POWER_ 2.5V & DDR TERMINAL POWER

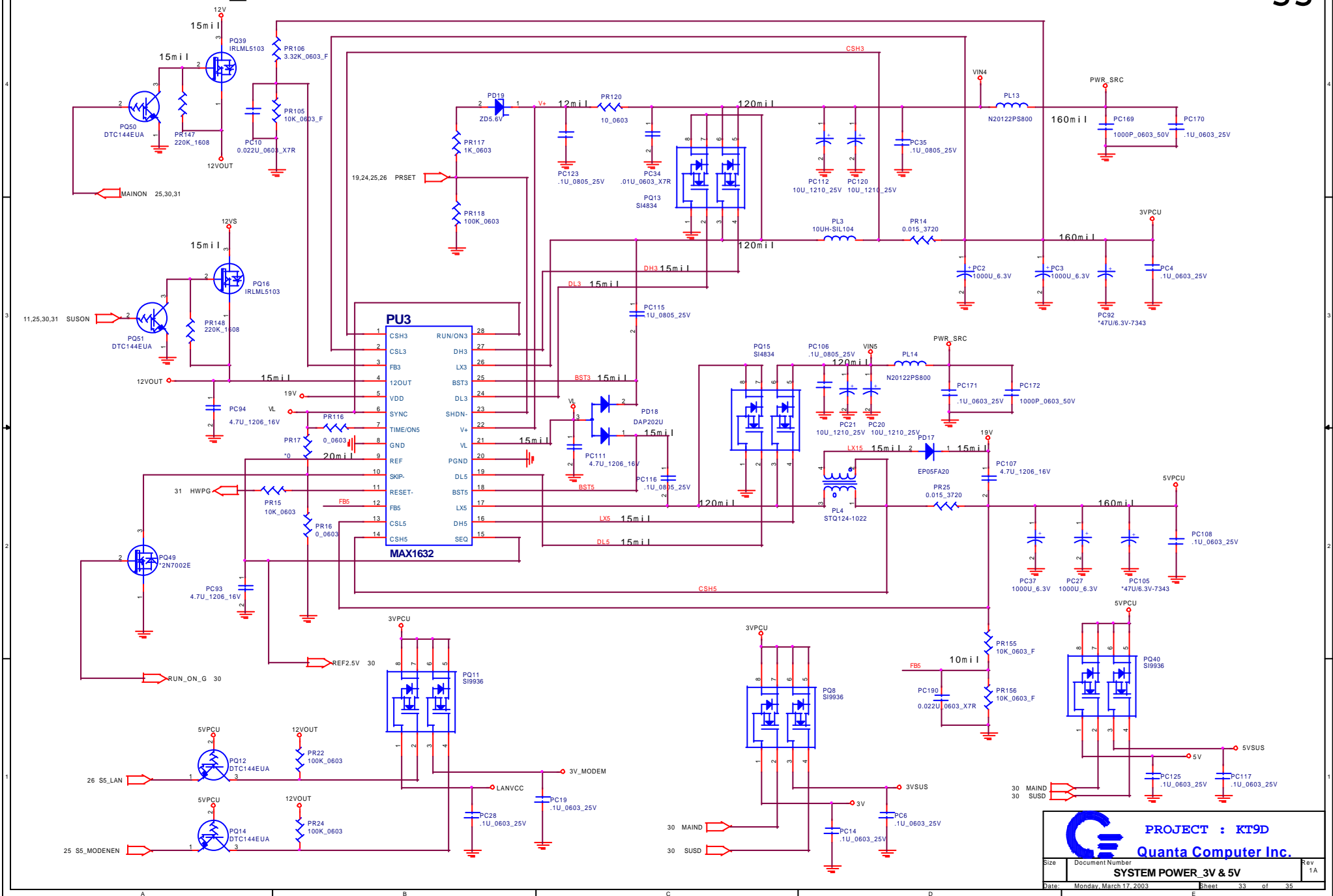
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SYSTEM POWER_3V & 5V

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PROJECT : KT9D

Quanta Computer Inc.

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