

KL1 BLOCK DIAGRAM

LAYER 1 : TOP
LAYER 2 : SGND1
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : IN3
LAYER 7 : SGND2
LAYER 8 : BOT

DDR III (TPS51116REGR)
1.5VSUS/SMDDR_VTERM
/SMDDR_VREF
 PAGE 39

DDR III-SODIMM1
DDRIII 800/1066 MHz
 PAGE 11

DDR III-SODIMM2
DDRIII 800/1066 MHz
 PAGE 12

CPU CORE(MAX17021)
 PAGE 41

CPU (Penryn)

478P (uPGA)/35W

CPU THERMAL SENSOR

PAGE 5

FSB 667/800/1066

PAGE 4, 5

CLK_CPU_BCLK, CLK_CPU_BCLK#
CLK_MCH_BCLK, CLK_MCH_BCLK#
DREFCLK, DREFCLK#
DREFSSCLK, DREFSSCLK#

CLOCK GEN
ICS9LPRS365BGLFT
 PAGE 3

VCCP +1.05V AND GMCH
+1.5V(RT8204)
 PAGE 28

NORTH BRIDGE

Cantiga

PAGE 6~10

DMI LINK

32.768KHz

NVIDIA
N10M-GS1(64bit)
N10P-GE1(128bit)
 PAGE 13~19

Level-Shifter(PS8101)

PAGE 22

GDDR3 X 2

(256MB)

PAGE 18

GDDR3 X 2

(256MB)

PAGE 19

HDMI CONN

PAGE 22

CRT CONN

PAGE 20

VGACORE(OZ8119)

PAGE 42

LCD CONN

PAGE 21

NBSRCLK, NBSRCCLK#

USB2.0

PCI-E

Azalia

X2

X1

X1

X1

X1

AUDIO CODEC

ALC269Q-GR

PAGE 31

Mini PCI-E Card

(WLAN/ WWAN)

PAGE 33

LAN

BCM5784M

(10/100/1G LAN)

PAGE 29, 30

RJ45 CONN

PAGE 30

25MHz

1394 CONN

PAGE 27

6-IN-1 Card Reader CONN

PAGE 28

24.576MHz

SYSTEM POWER(ISL6237)

PAGE 40

SYSTEM CHARGER(ISL6251A)

PAGE 28

Touch Pad

PAGE 36

Keyboard

PAGE 37

CIR

PAGE 34

FAN

PAGE 37

SPI BIOS

PAGE 34

Digital MIC

(Internal MIC)

PAGE 31

Audio Jack

(External MIC)

PAGE 31

Head-Phone Jack

+ SPDIF

PAGE 31

Size

Custom

Document Number

Rev A1A

Date:

Monday, January 05, 2009

Sheet

1 of 43

BLOCK DIAGRAM

1

8

of 43

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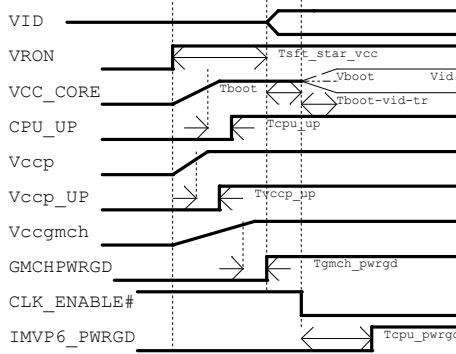
PROJECT : KL1
Quanta Computer Inc.

Board Stack up Description

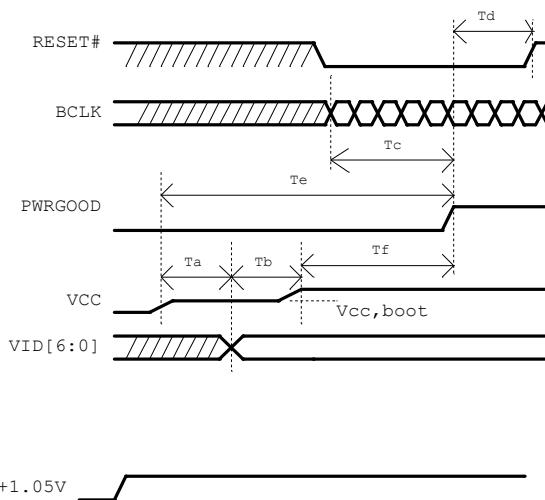
PCB Layers

Layer 1		TOP
Layer 2		GND
Layer 3		IN1
Layer 4		IN2
Layer 5		SVCC
Layer 6		IN3
Layer 7		GND
Layer 8		BOTTOM

Power On Sequencing Timing Diagram



Penryn Power-up Timing Specifications

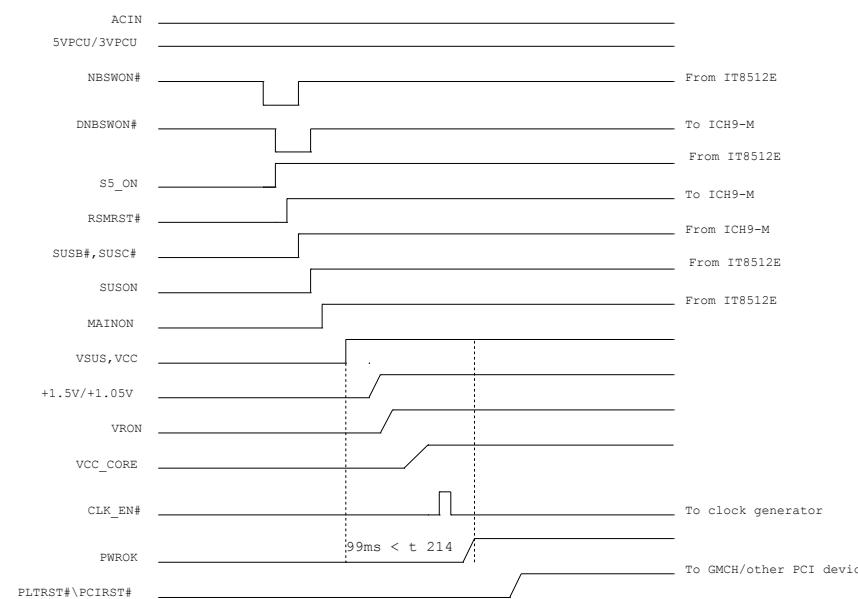


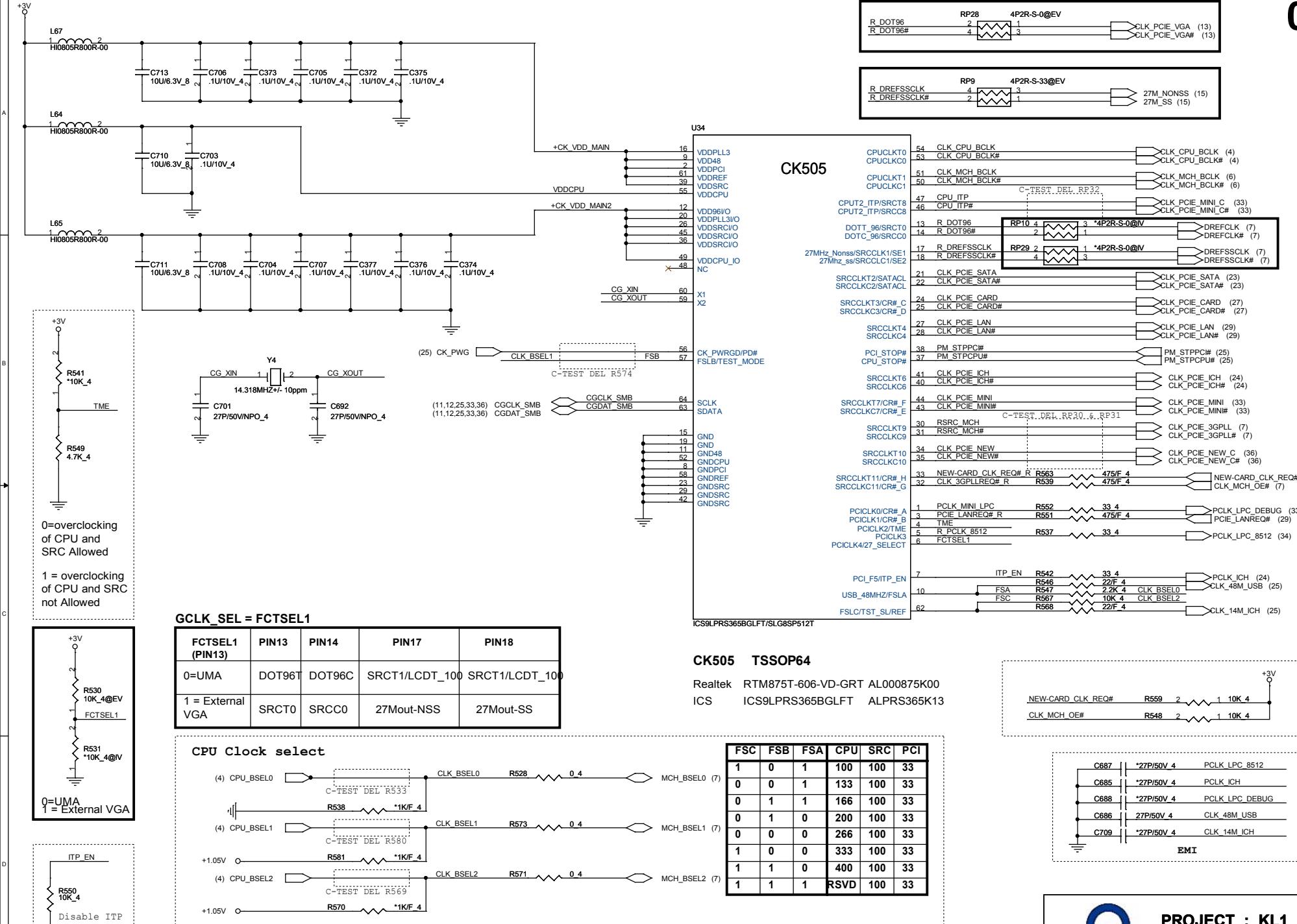
Ta=VCC and VCCP assertion to VID[6:0] valid
Tb=VID[6:0] stable to VCC valid
Tc=BCLK stable to PWRGOOD assertion
Te=PWRGOOD to RESET# de-assertion time
Tf=VCC,boot valid to PWRGOOD assertion time

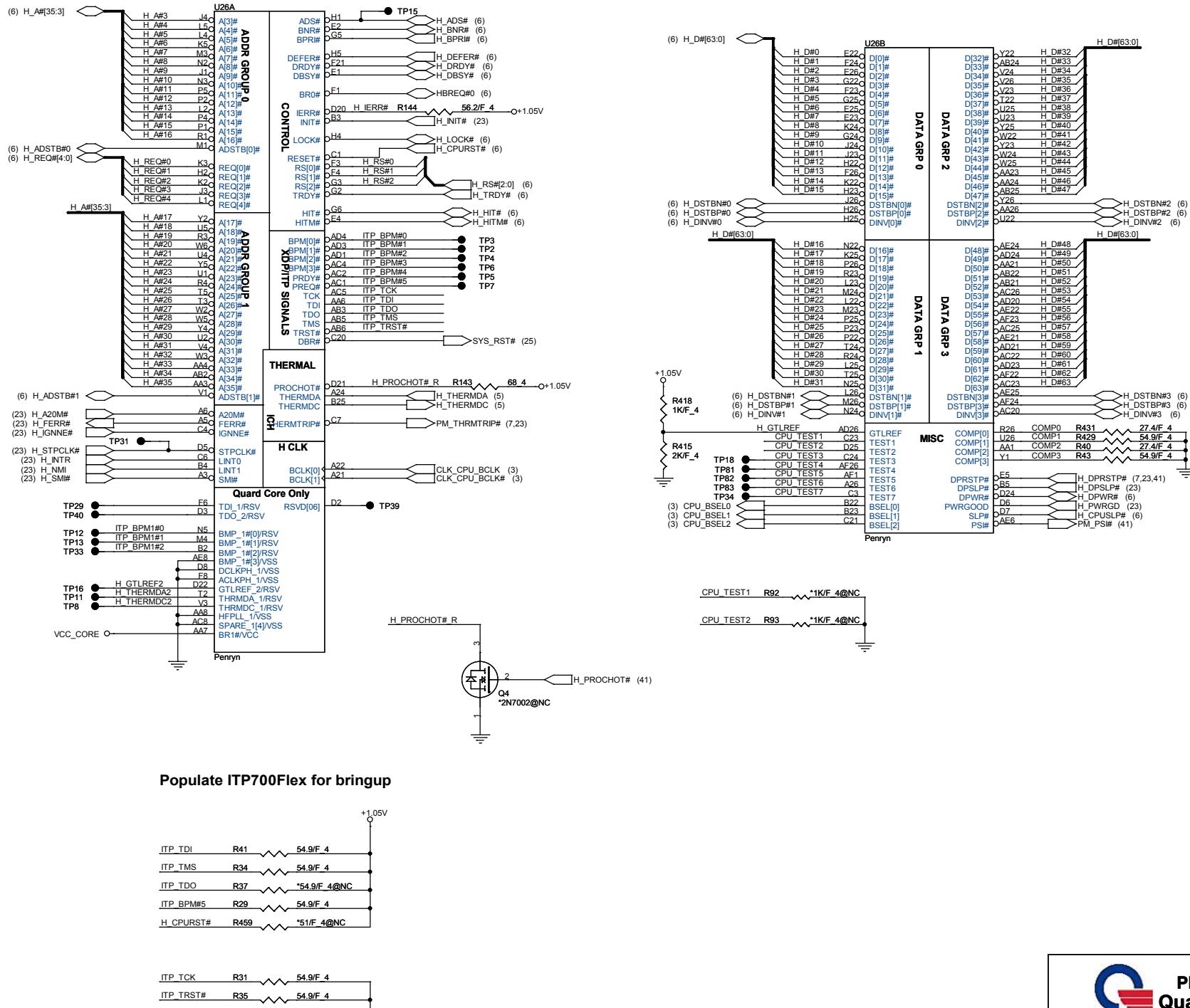
Voltage Rails

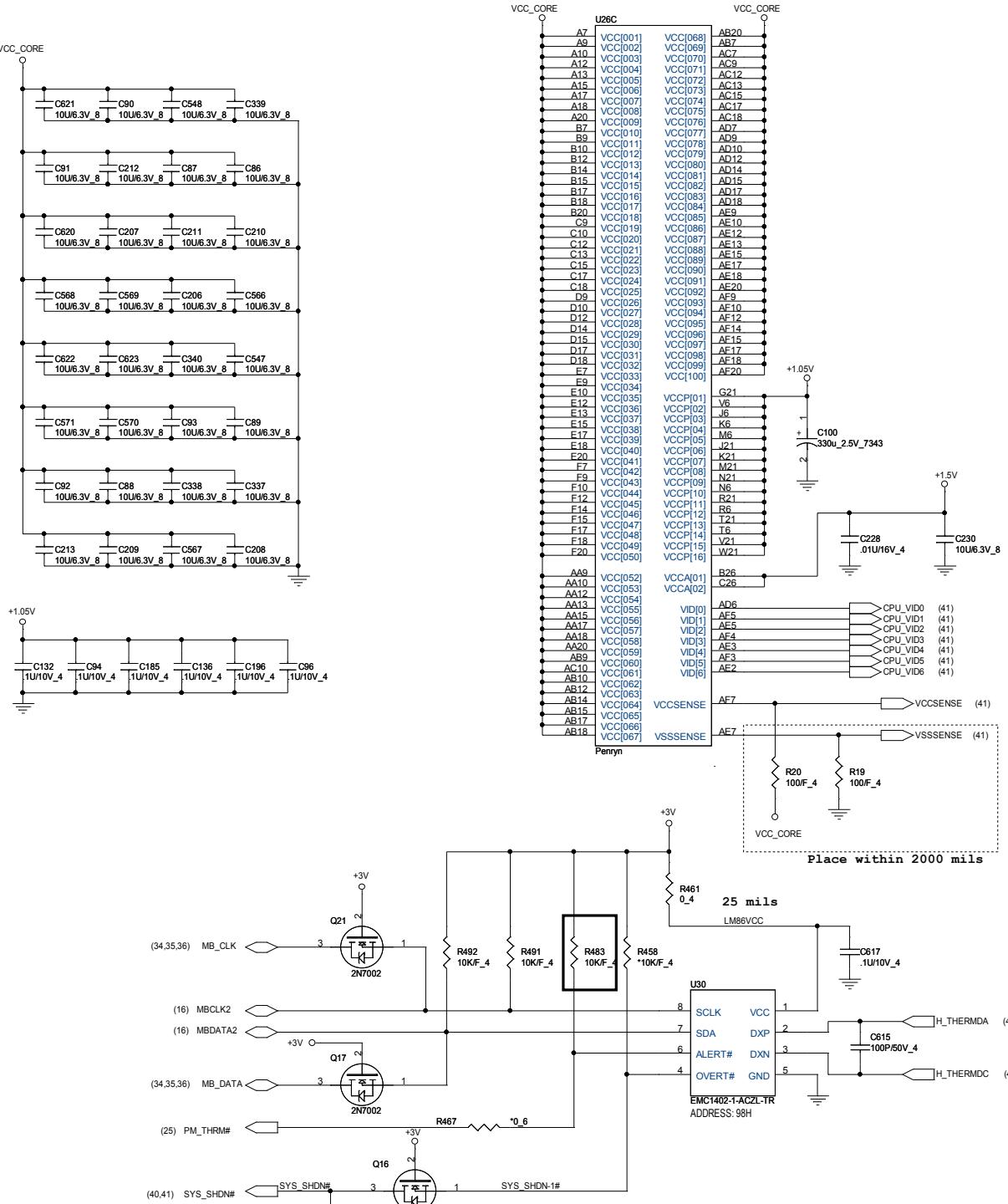
Voltage Rails	ON S0~S2	ON S3	ON S4	ON S5	Control signal
VCC_CORE	V				VRON
+1.5V	V				MAINON
+1.05V	V				MAINON
5V_S5/3V_S5	V	V	V	V	S5_ON
5VSUS/3VSUS/1.5VSUS	V	V			SUSON
SMDDR_VTERM/+3V/+5V/+15V/+1.8V	V				MAINON
+VGACORE/+VGA1.1V	V				MAINON
LANVCC	V	V			LAN_ON
3VPCU	V	V	V	V	VL
5VPCU	V	V	V	V	VL

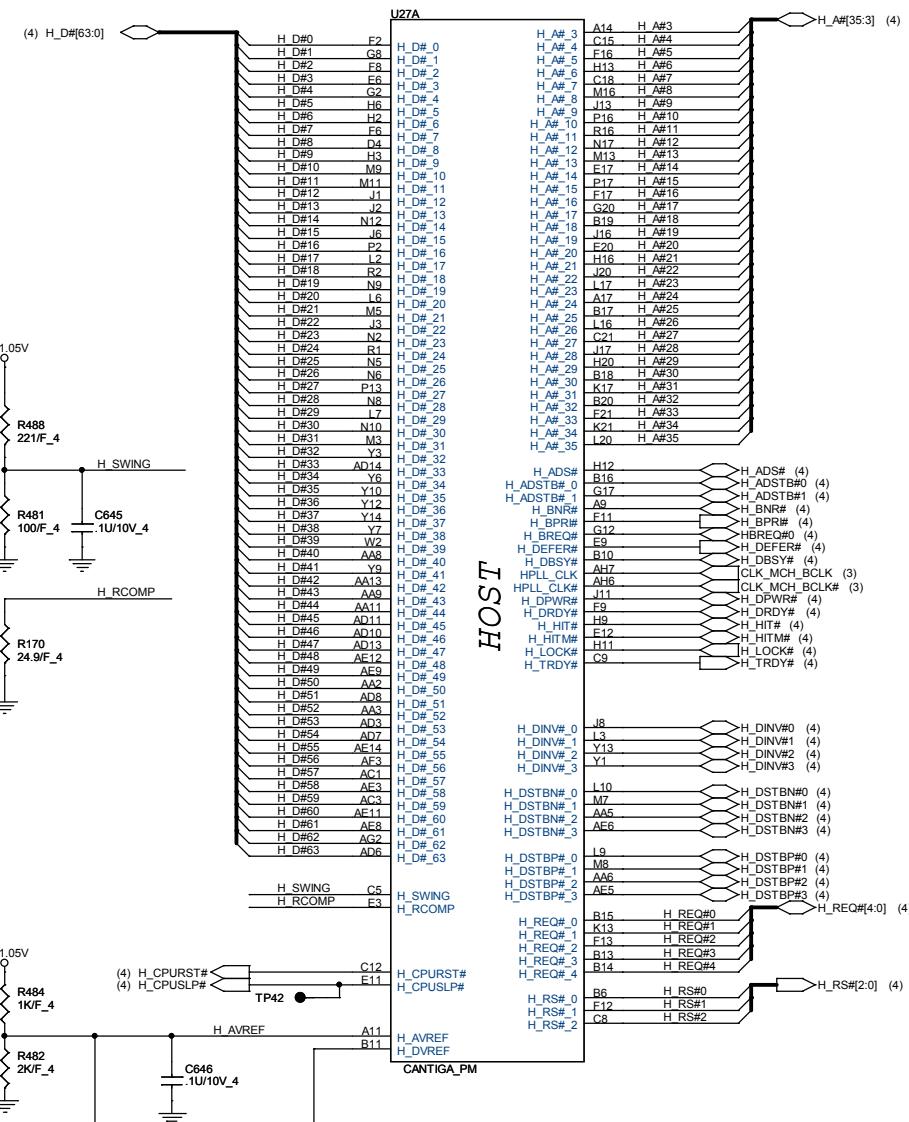
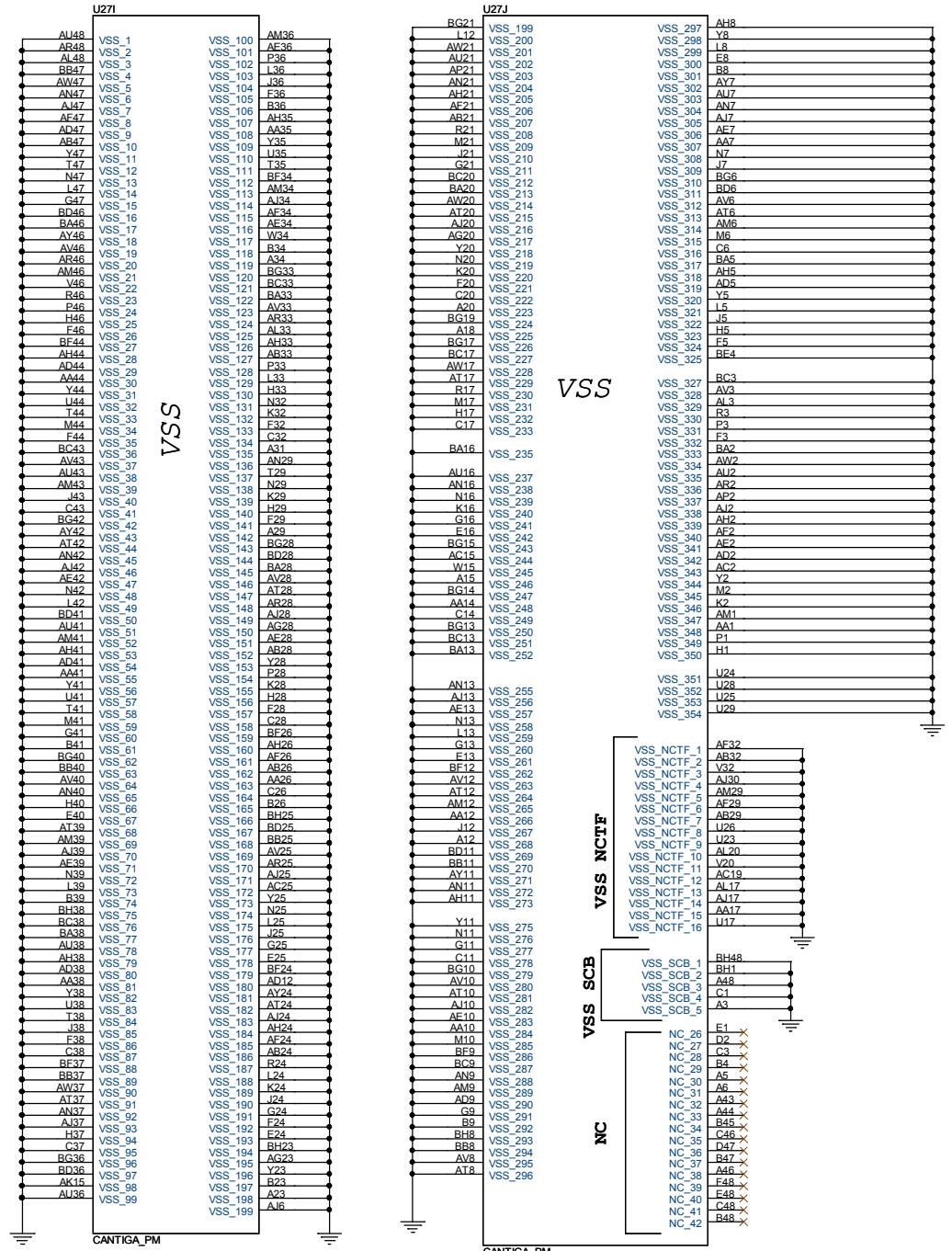
ACIN POWER ON TIMING



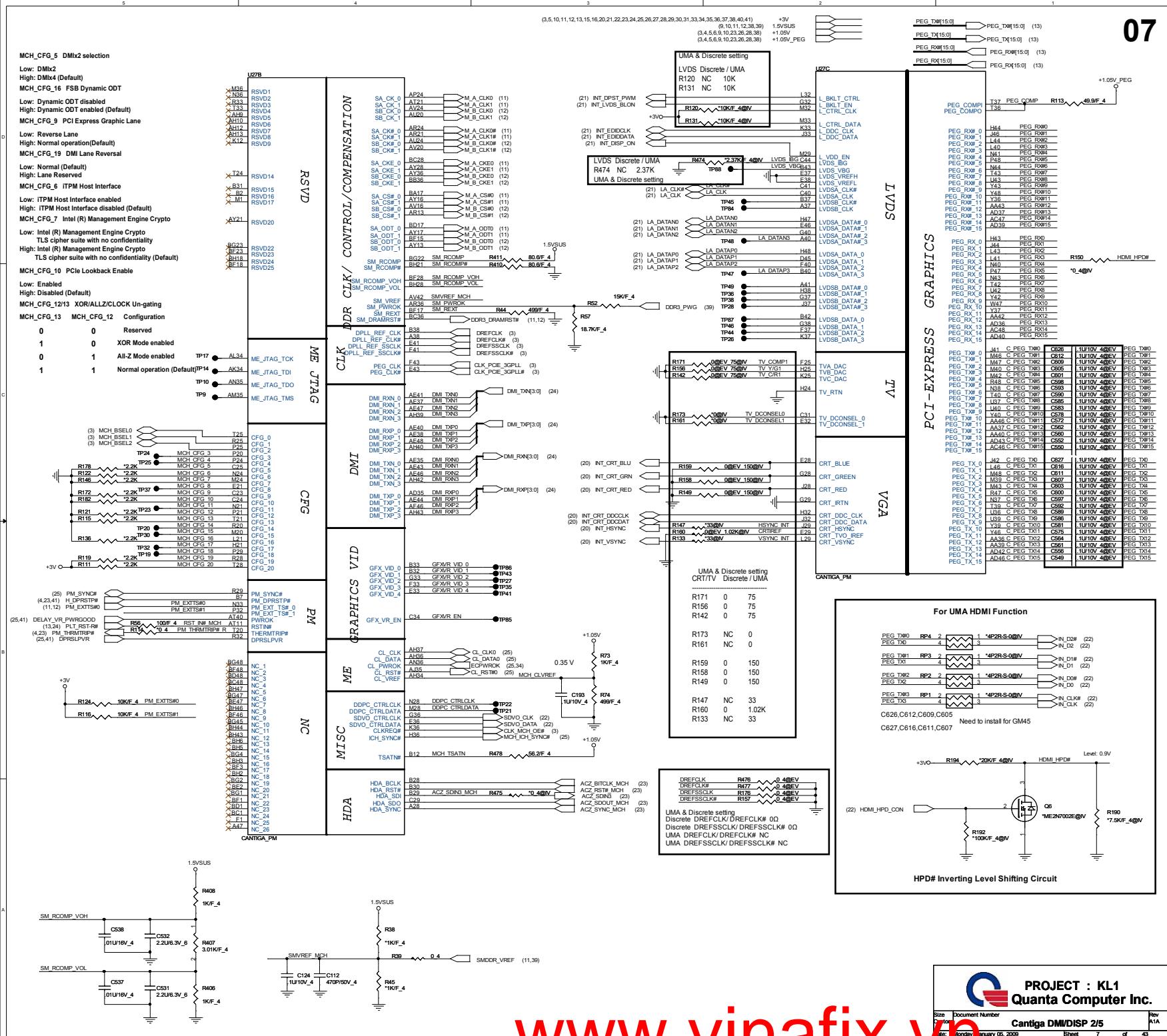


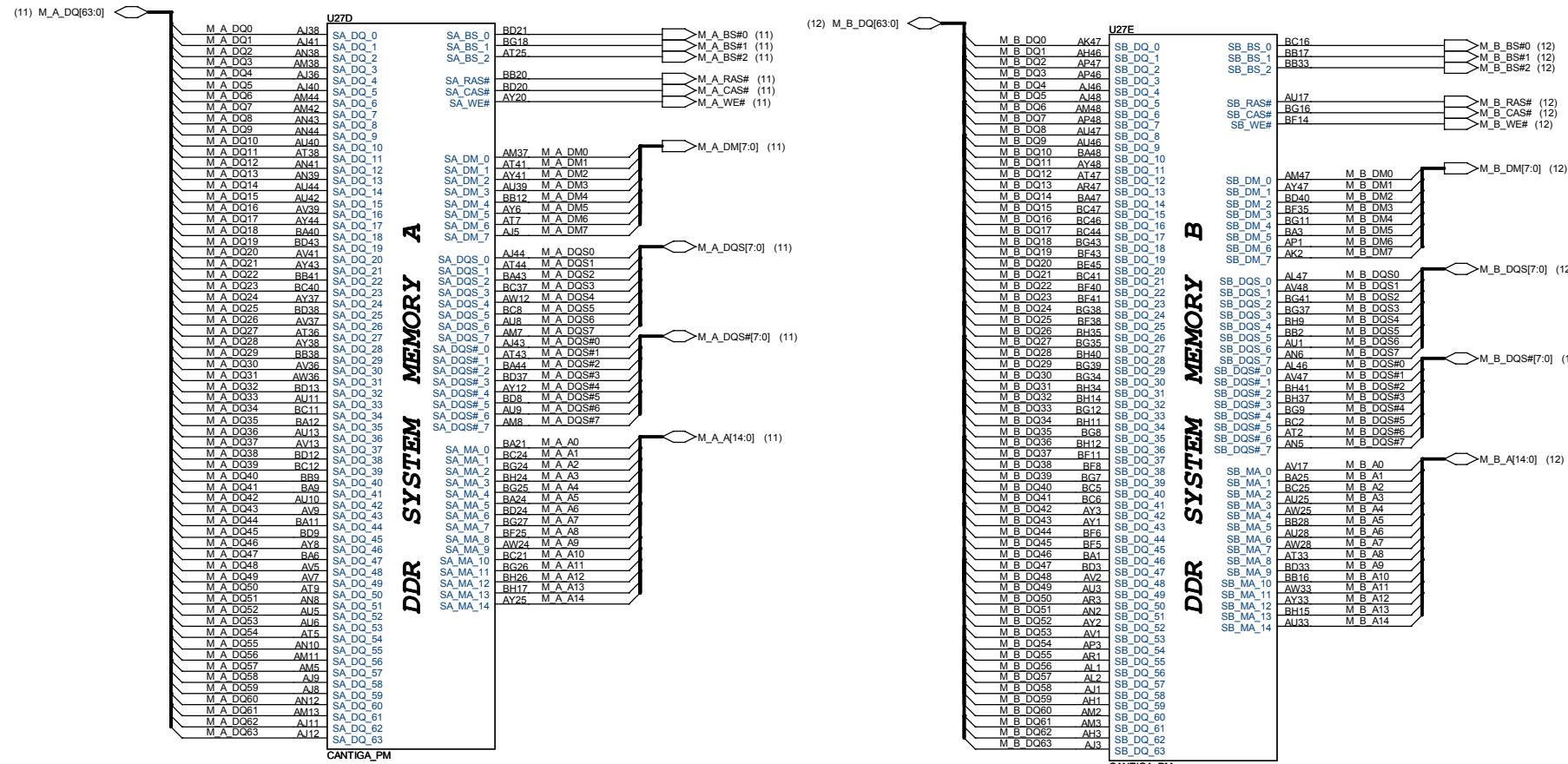


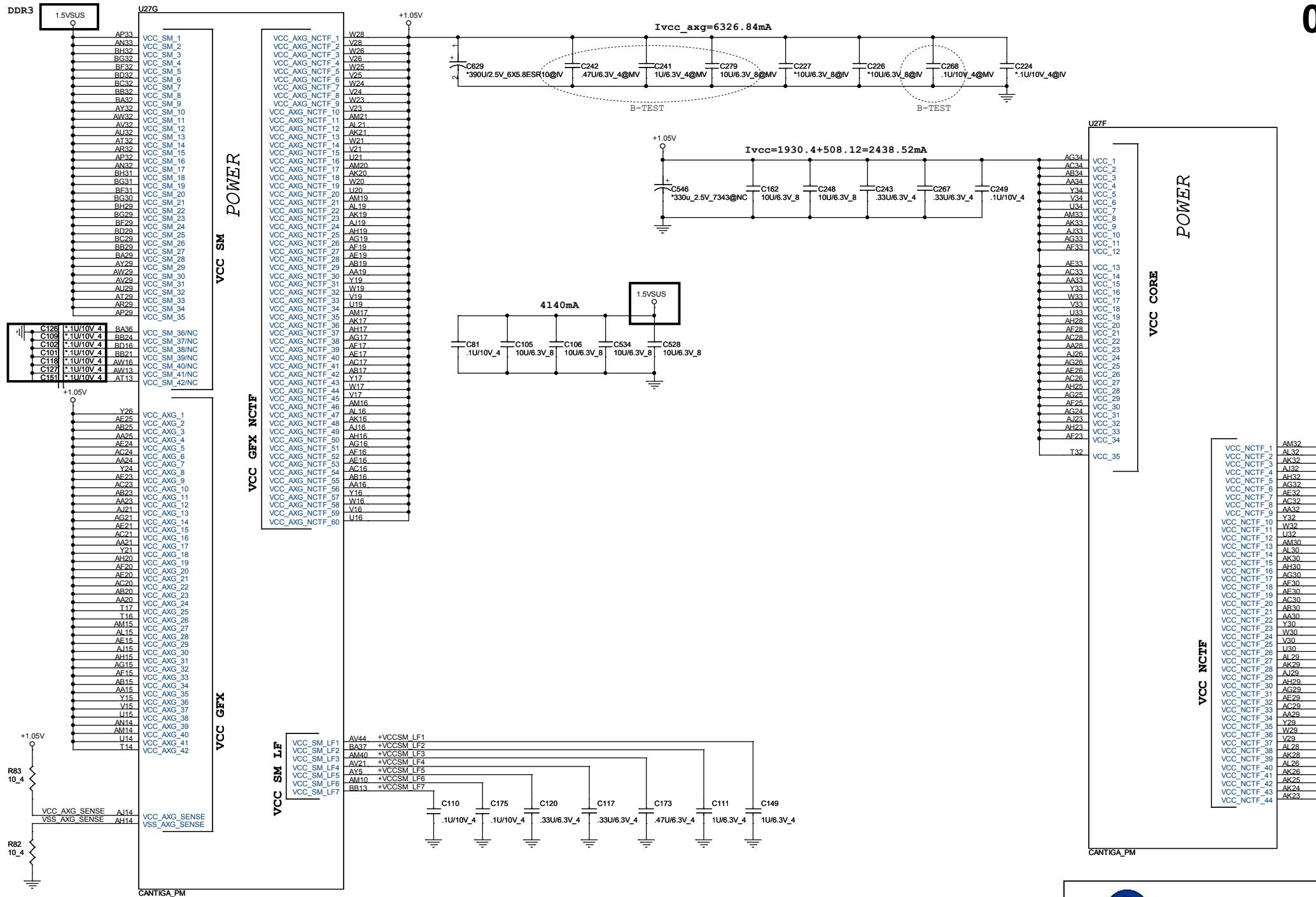


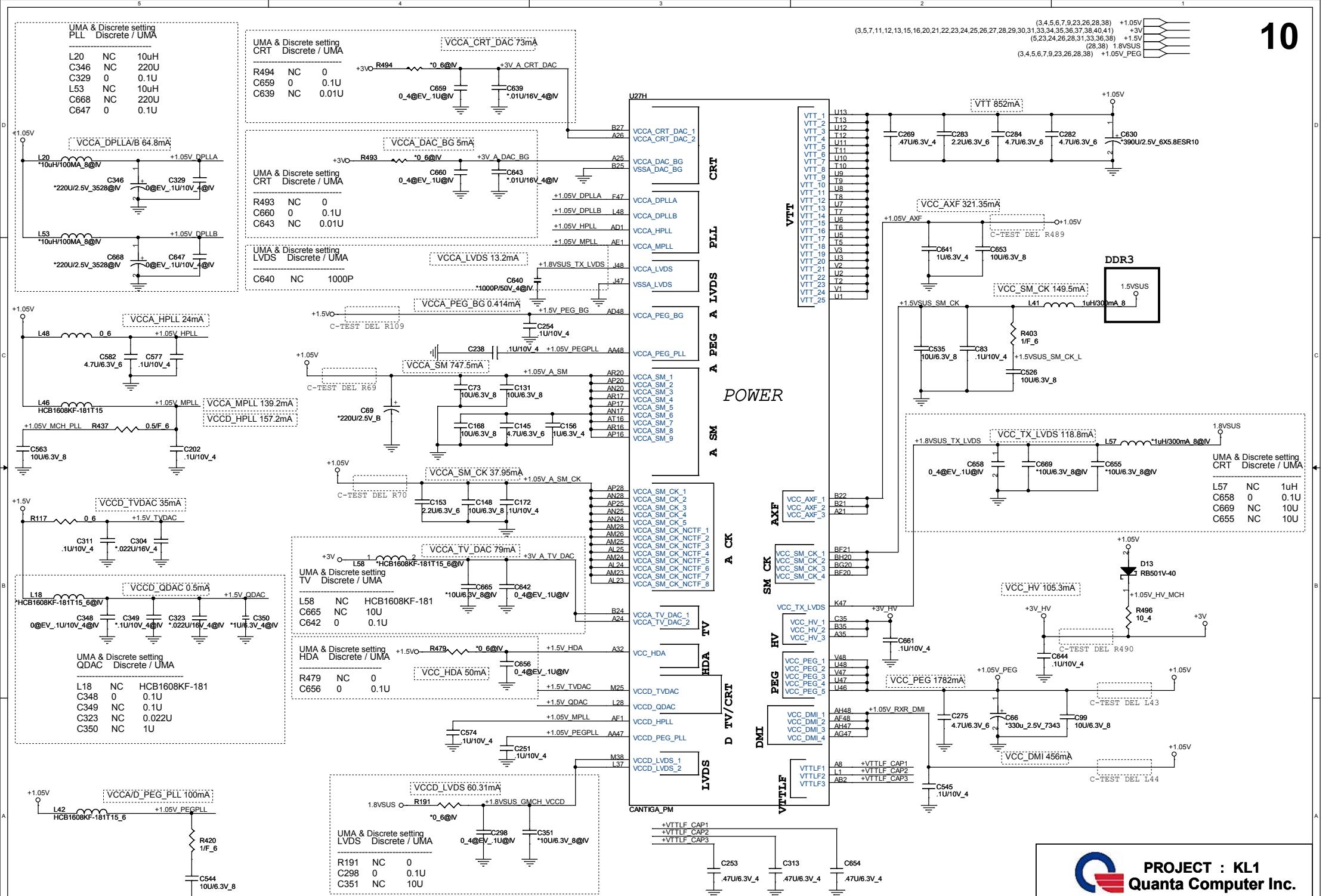


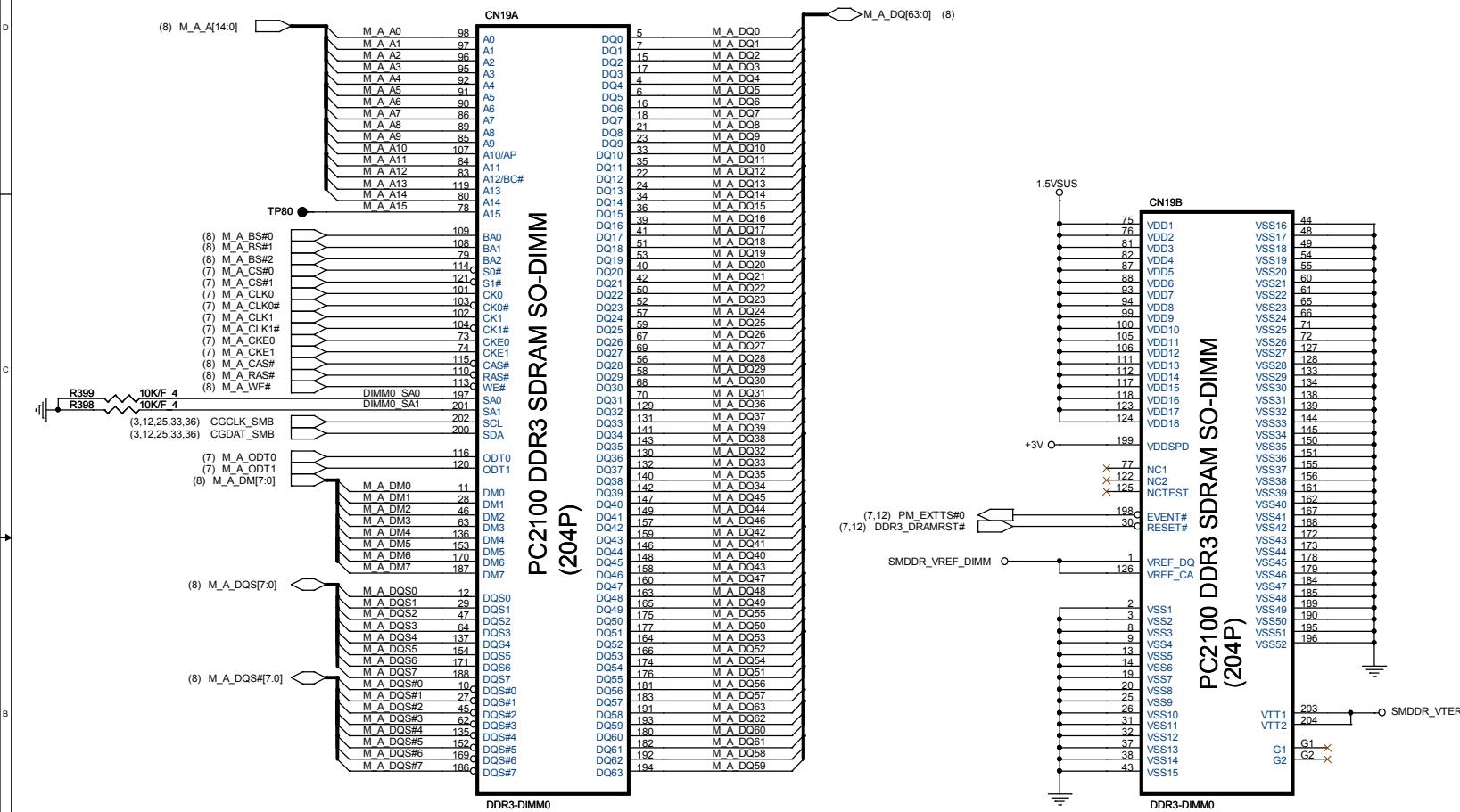
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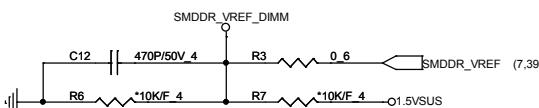
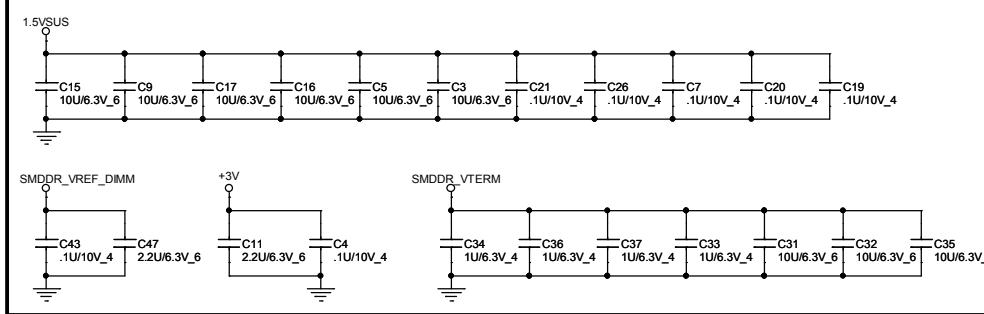


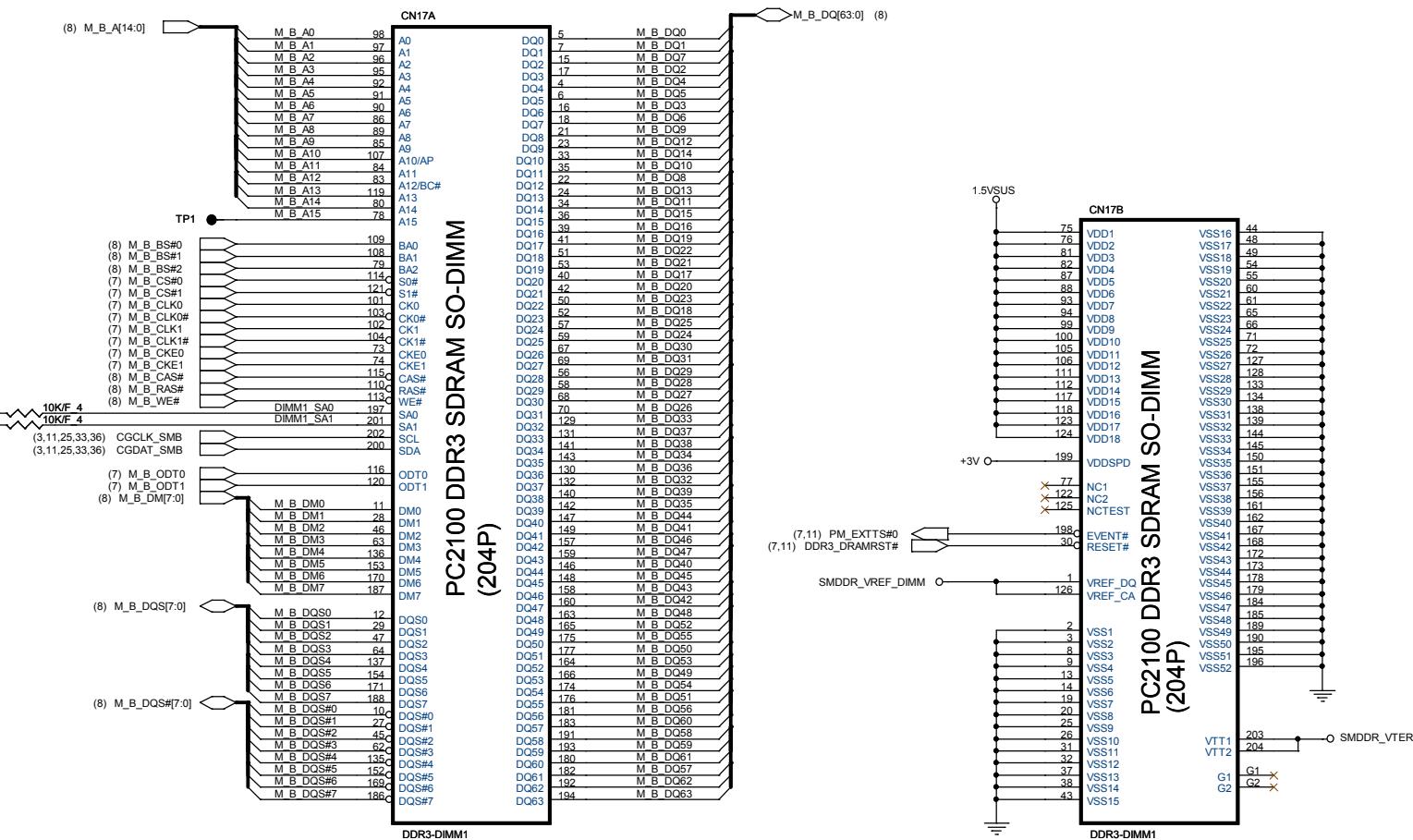




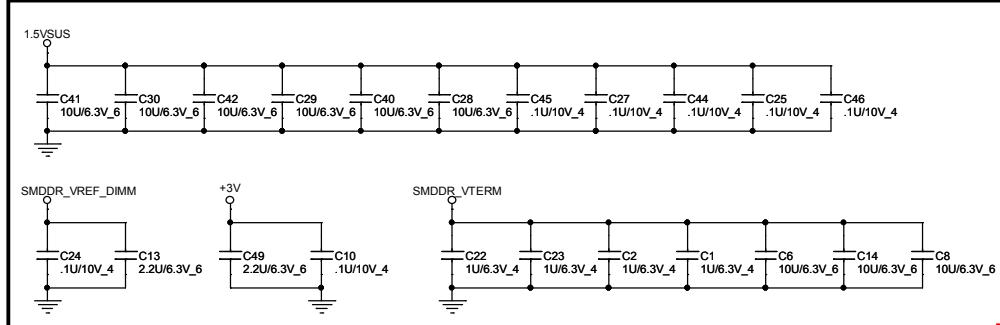


Place these Caps near So-Dimm0



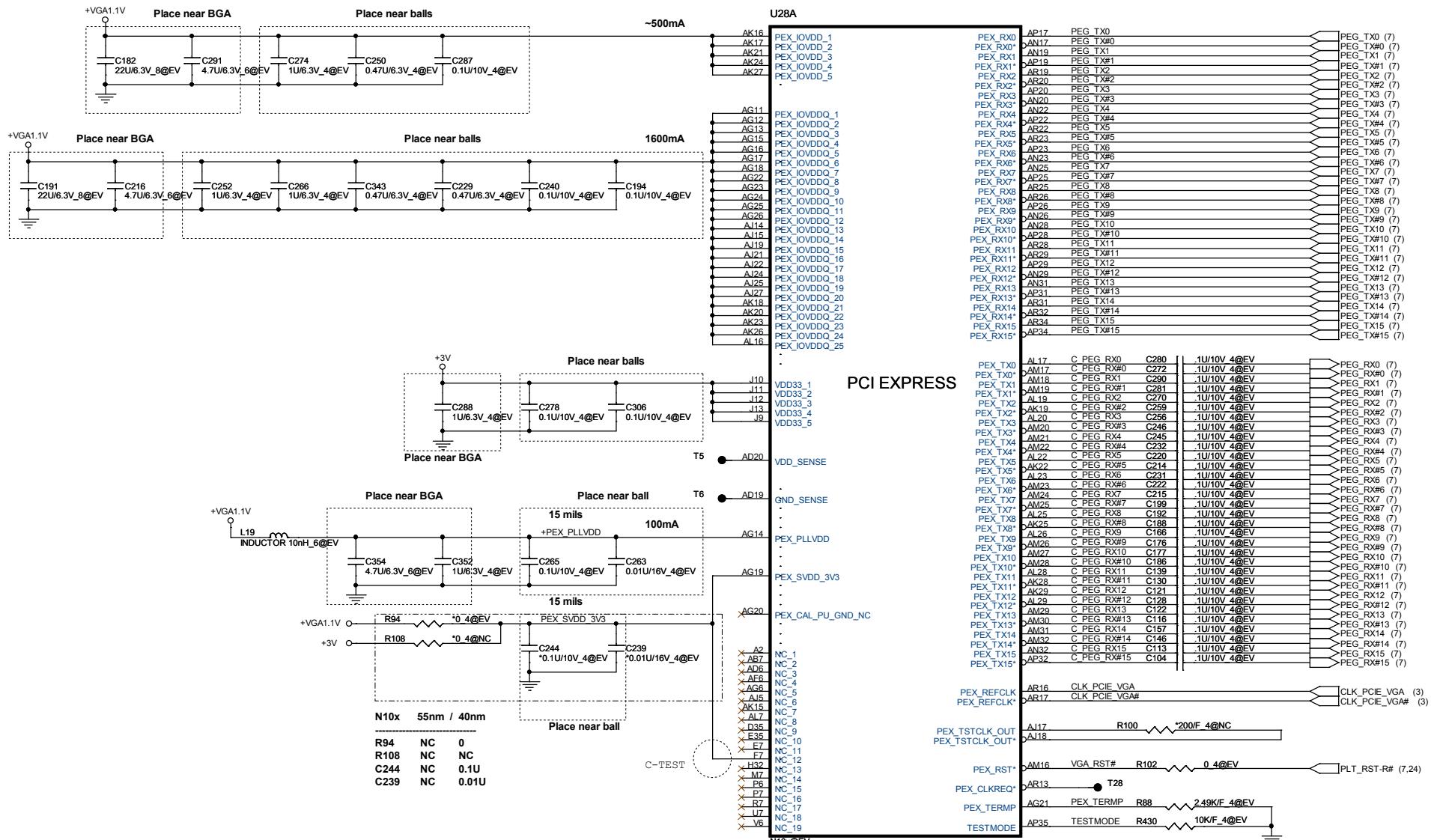


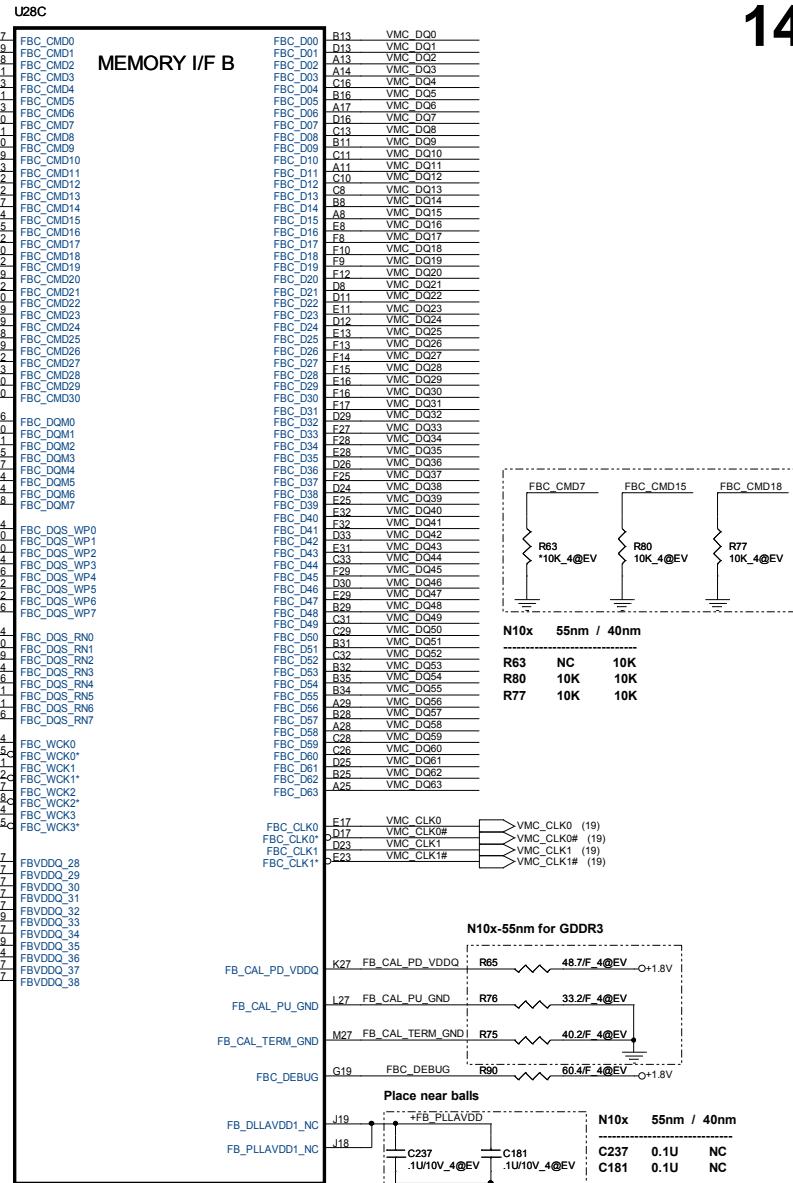
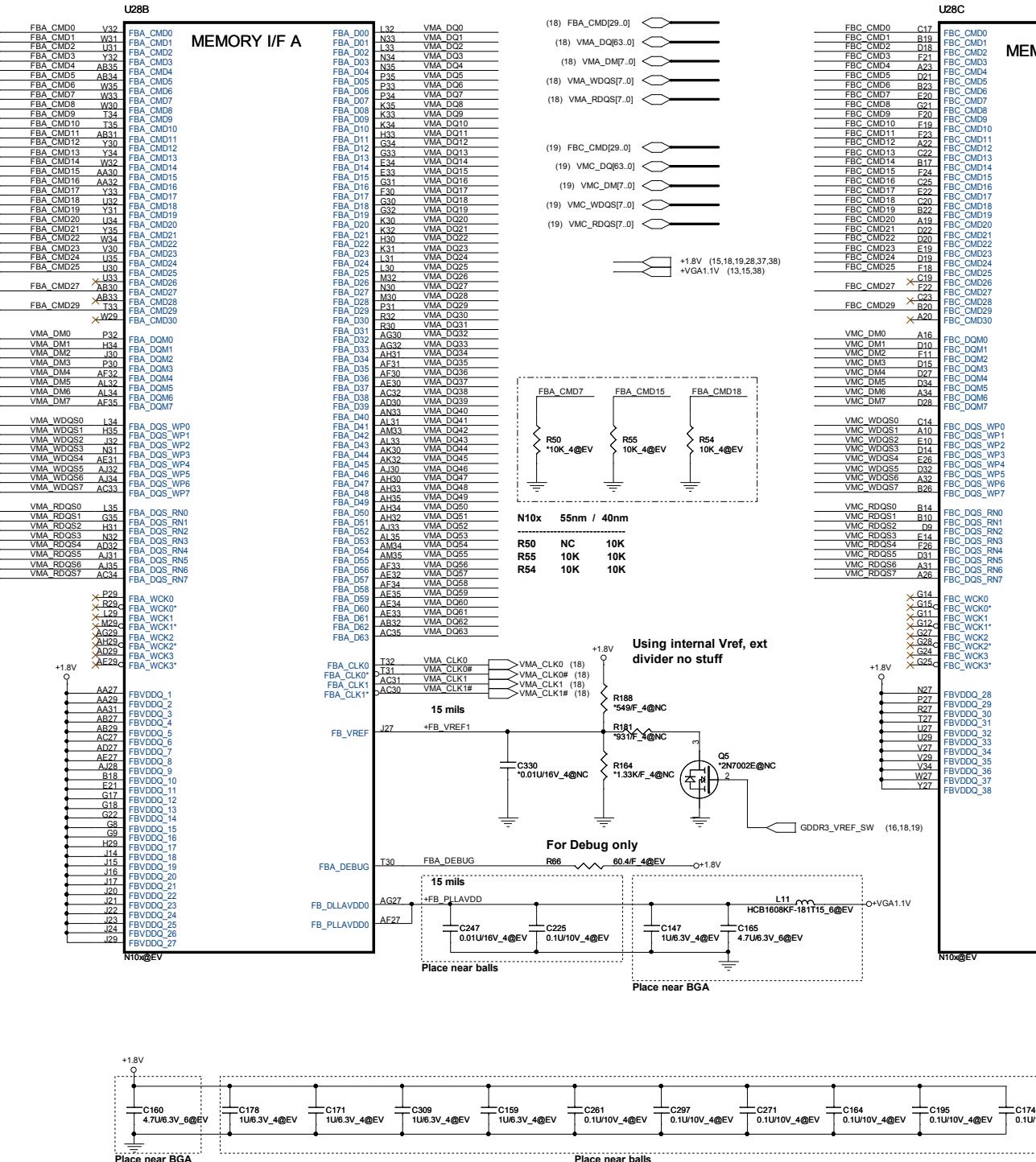
Place these Caps near So-Dimm1



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Size Document Number Rev
Custom DDR3 DIMM-1(H=9.2) A1A
Date Monday, January 05, 2009 Sheet 12 of 43



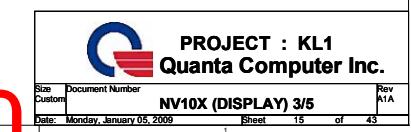
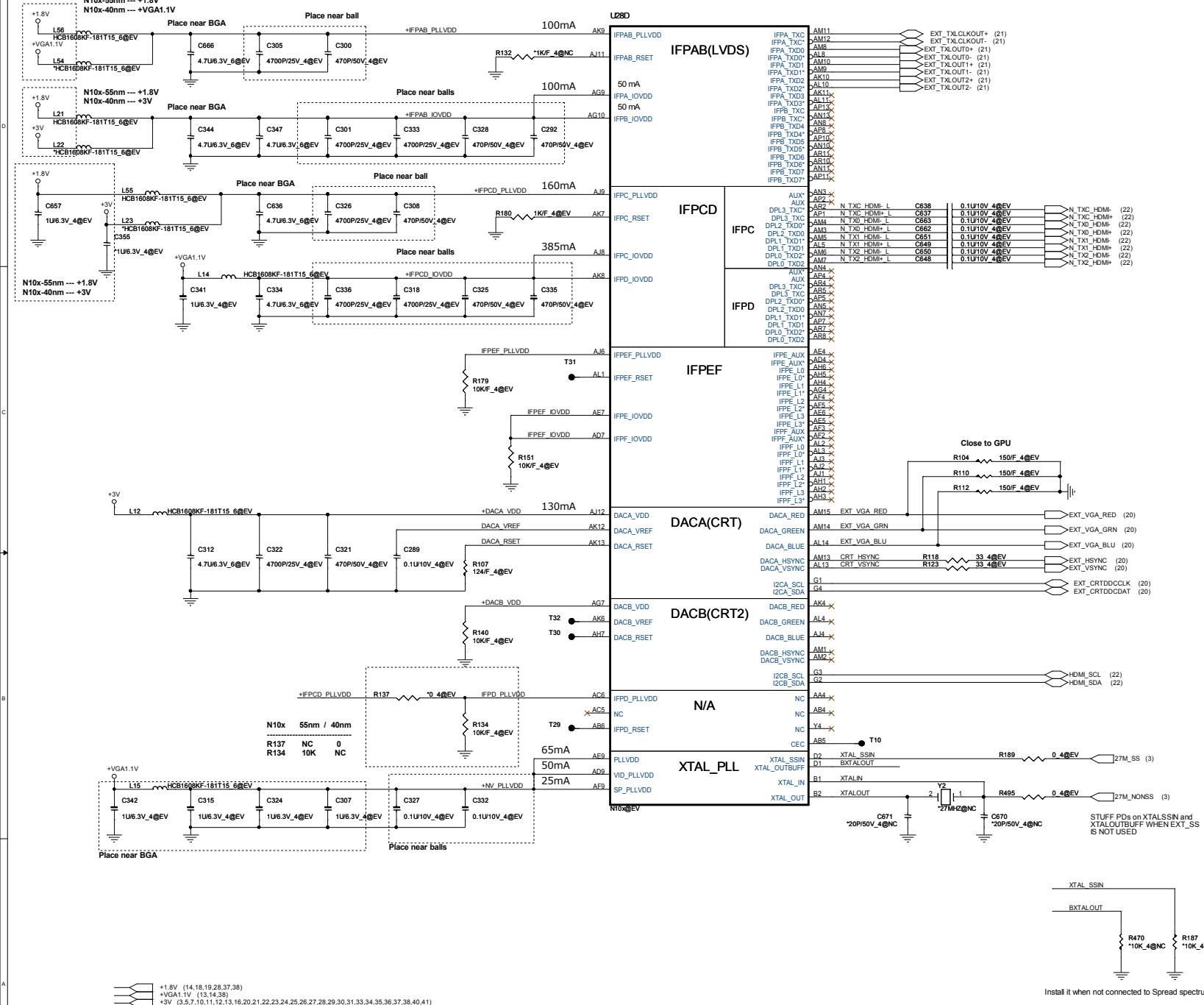


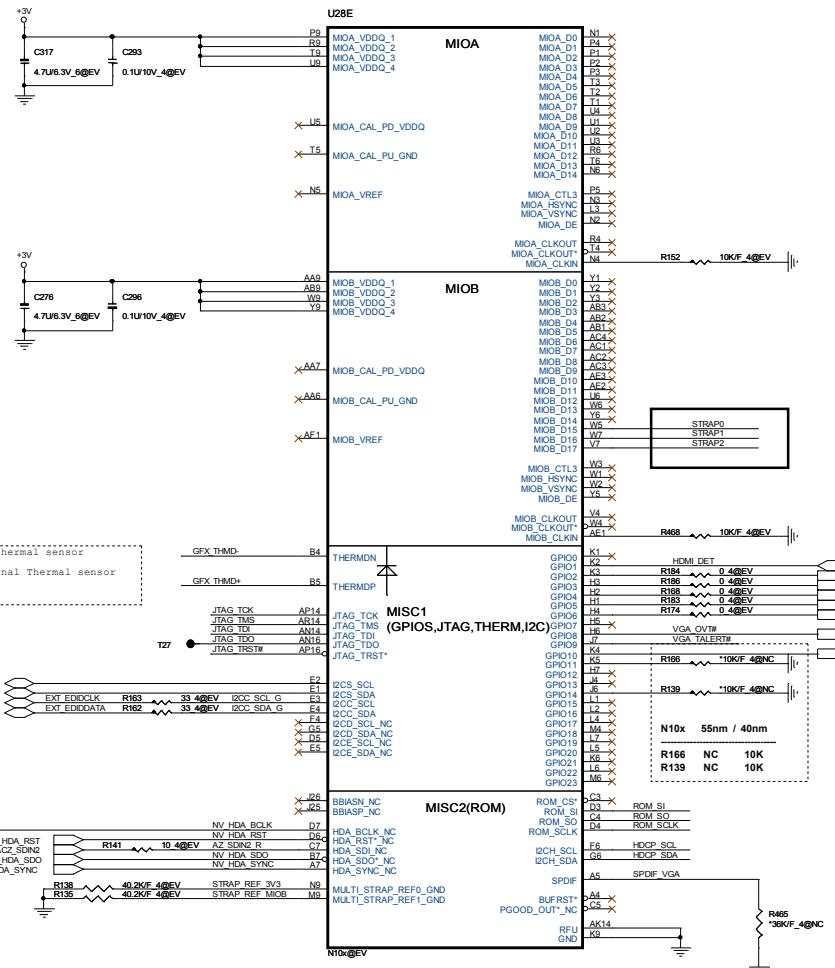
	N10M-55nm N10P-55nm	N10M-40nm N10P-40nm
Signal Name	GDDR3	GDDR3
FBCAL_PD_VDDQ	48.7 ohm	
FBCAL_PU_GND	33.2 ohm	
FBCAL_TERM_GND	40.2 ohm	

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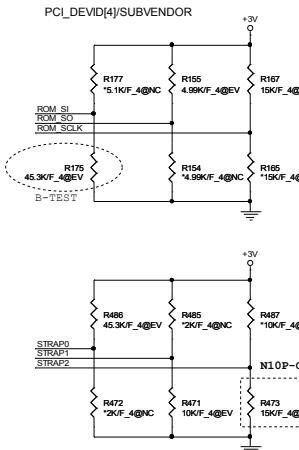
NV10X (MEMORY I/F) 2/5





**N10P-GE1 Straps
N10M-GS1 Straps
GPIO ASSIGNMENTS**

GPIO	I/O	ACTIVE	USAGE
0	IN	N/A	PRIMARY DVI HOTPLUG
1	IN	N/A	SECONDARY DVI HOTPLUG
2	OUT	HIGH	PANEL BACKLIGHT PWM
3	OUT	HIGH	PANEL POWER ENABLE
4	OUT	HIGH	PANEL BACKLIGHT ENABLE
5	OUT	N/A	NVDD VID0
6	OUT	N/A	NVDD VID1
7	OUT	N/A	FVBDD VID0
8	IN	LOW	THERMAL ALERT
9	OUT	LOW	FAN PWM
10	OUT	N/A	FVBREF SELECT
11	OUT	N/A	SLI SYNC0
12	IN	N/A	AC DETECT
13	OUT	LOW	PS CONTROL OR HDMI_CE
14	OUT	HIGH	PS CONTROL



N10P-GE1	55nm / 40nm	N10M-GS1	55nm / 40nm
R167	15K/F	R167	15K/F
R165	NC	R165	NC
R487	NC	R487	NC
R472	15K/F	R472	10K/F

Logical Strap Bit Mapping		
	PU-VDD	PD
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111

STBAP1 MAP

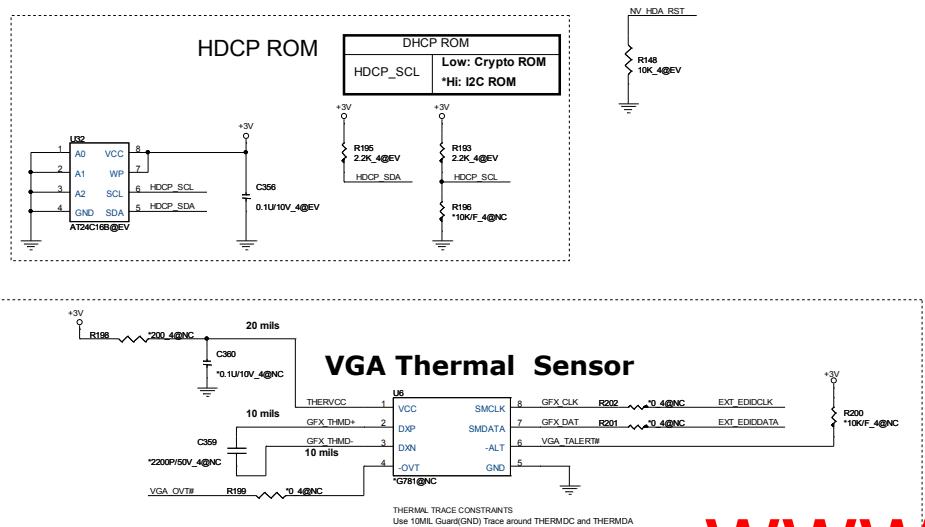
STRAP0: USER[3:0]
STRAP1: 3GIO_PADCFG[3:0]

STRAP2: PCI_DEVID[3:0]
ROM_SCLK: PCI_DEVID_EXT, SUB_VENDOR, SLOT_CLK_CFG, PEX_PLL_EN_TMR
ROM_SI: RAMCFG[3:0]

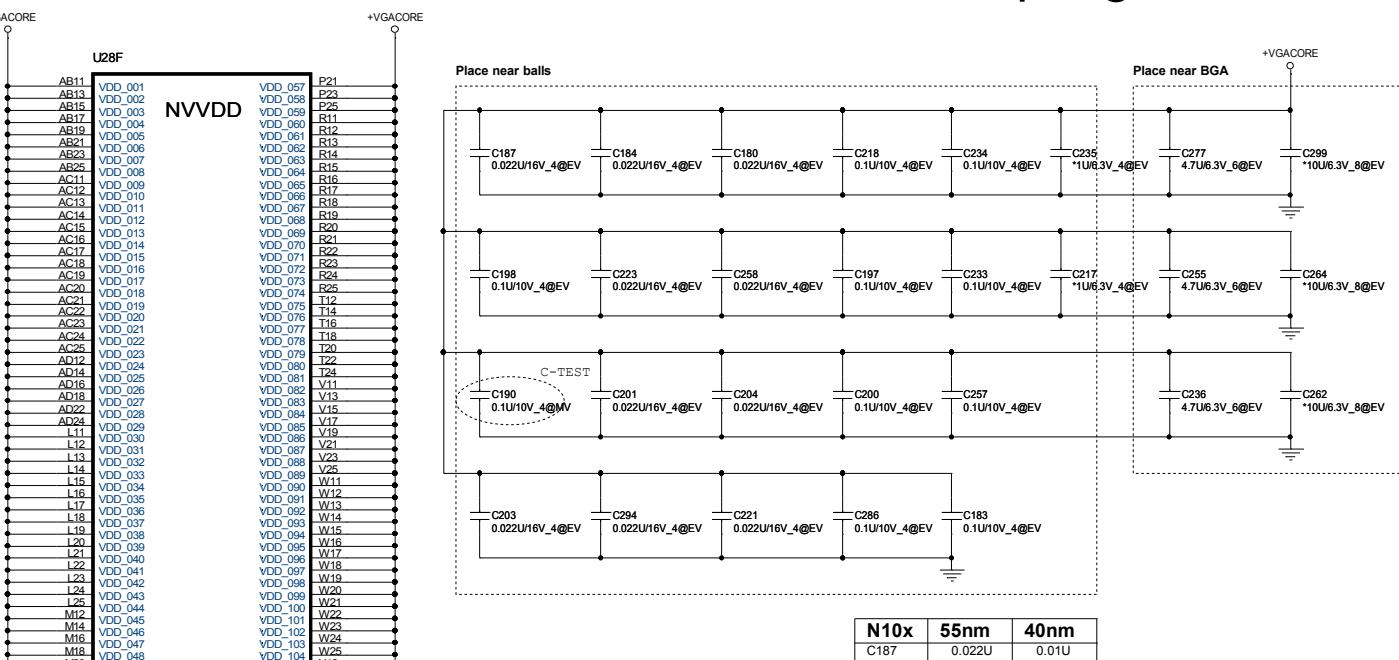
KI.1 STRAP FUNCTION MAPPING

```
USER[3:0]: 1111  
3GIO PADCFG[3:0]: 0001 NoteBook  
PCI DEVID[3:0]: 0001  
SUB-VENDOR: 0 No Vedio BIOS ROM  
SLOT CLK CLG: 1 GPU AND MCH USE COMMON REF C  
PEX PLL EN TERM: 0 DISABLE PEX_PLL TERMINATI  
RAMCFG[3:0]: 0000 AND 0001  
XCLK 417: 1 USING 27MHZ  
FB_0 BAR SIZE:  
SMB ALT ADDR:
```

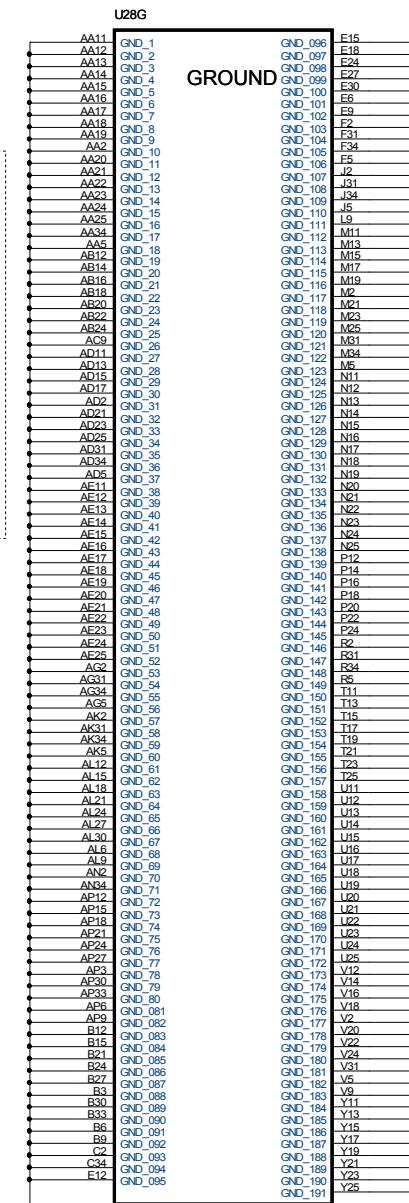
ROM SI	RAMCFG LSIT:
PD R175.45K/F	0111 : SAMSUNG K4J10324QD-HC12 32M32b * 4PCS
PD R175.30K/F	0101 : QIMONDA HYB18HIG32IAF-11 32M32b * 2PCS



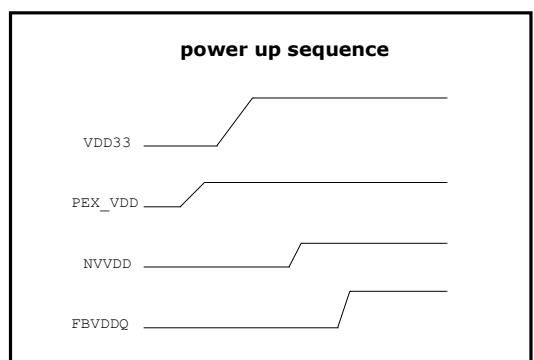
NVVDD Decoupling



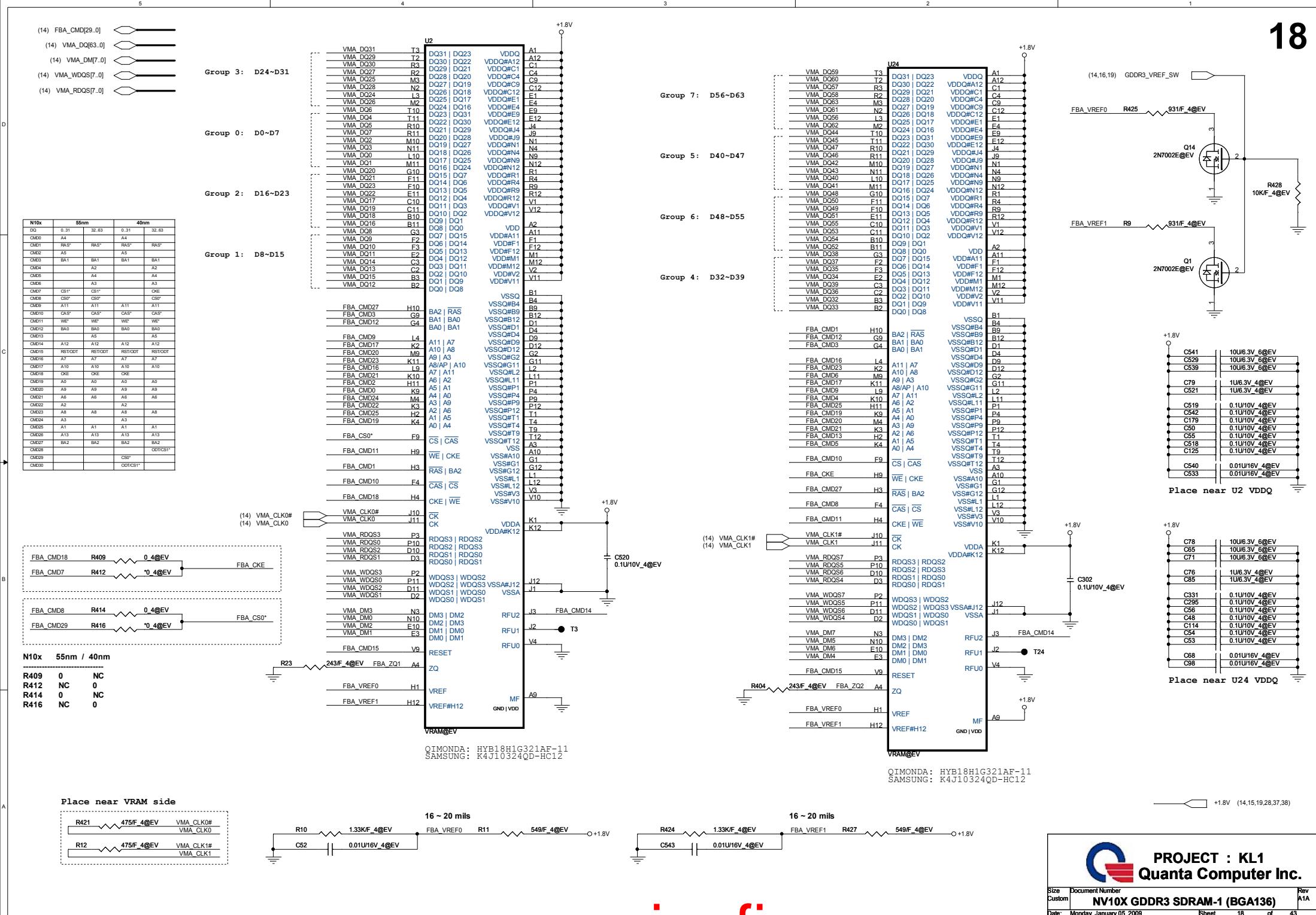
N10x	55nm	40nm
C187	0.022U	0.01U
C198	0.1U	0.01U
C190	0.1U	0.01U
C235	NC	1U
C217	NC	1U
C277	4.7U	1U
C255	4.7U	4.7U
C236	4.7U	NC
C299	NC	10U
C264	NC	10U
C262	NC	10U



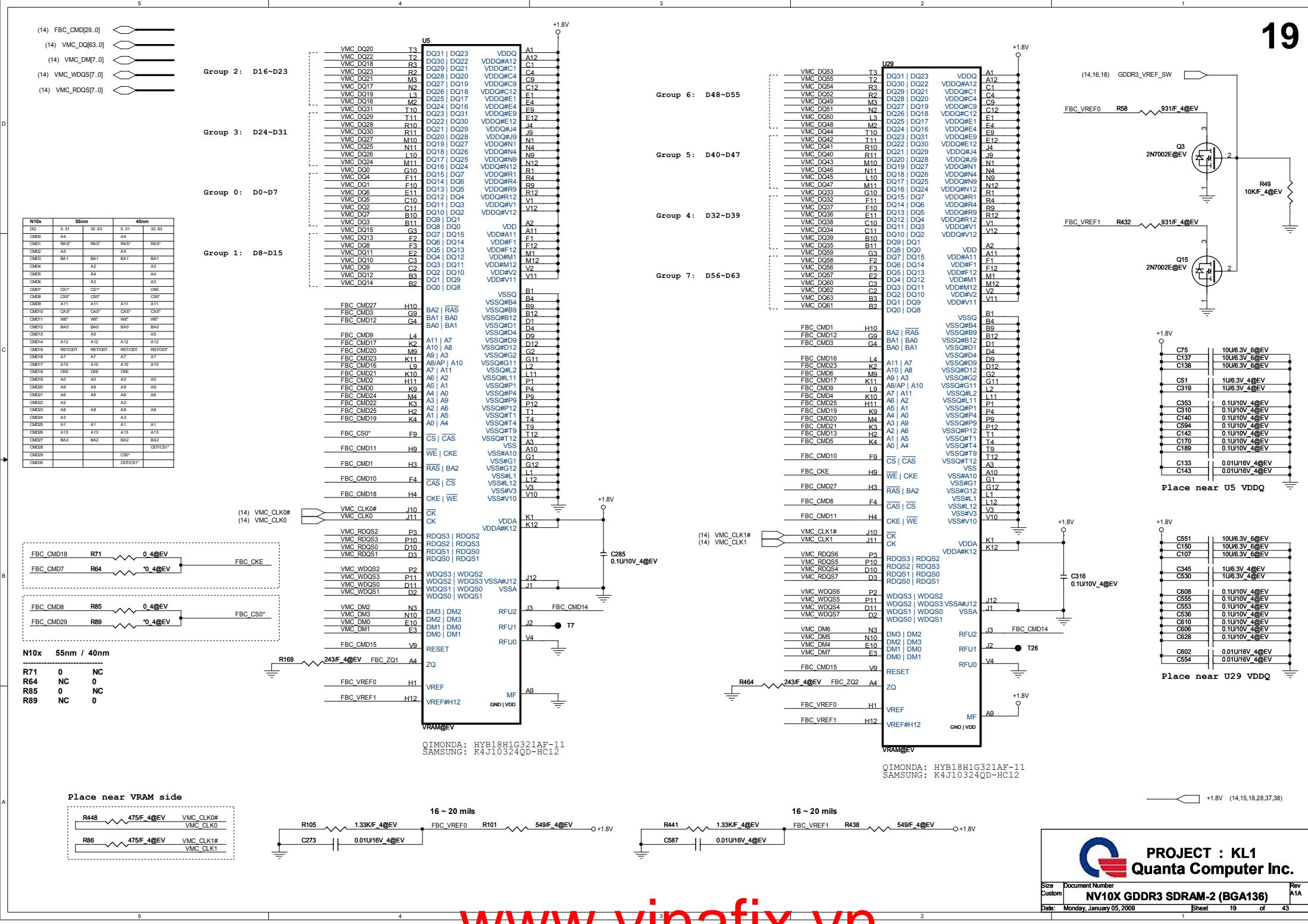
N10P-GE1: +VGACORE +0.90V (Normal)



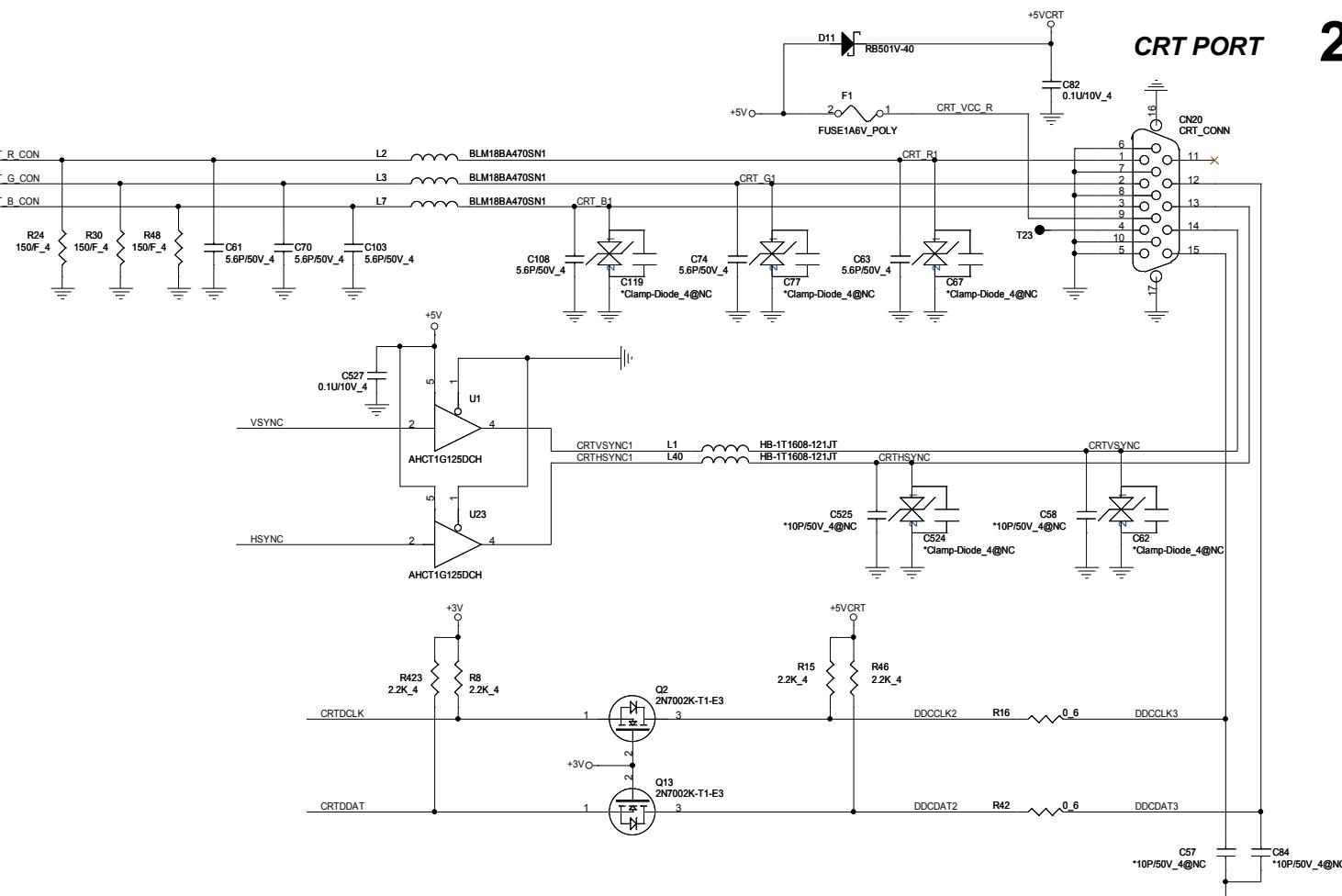
+VGACORE (37.38.42)



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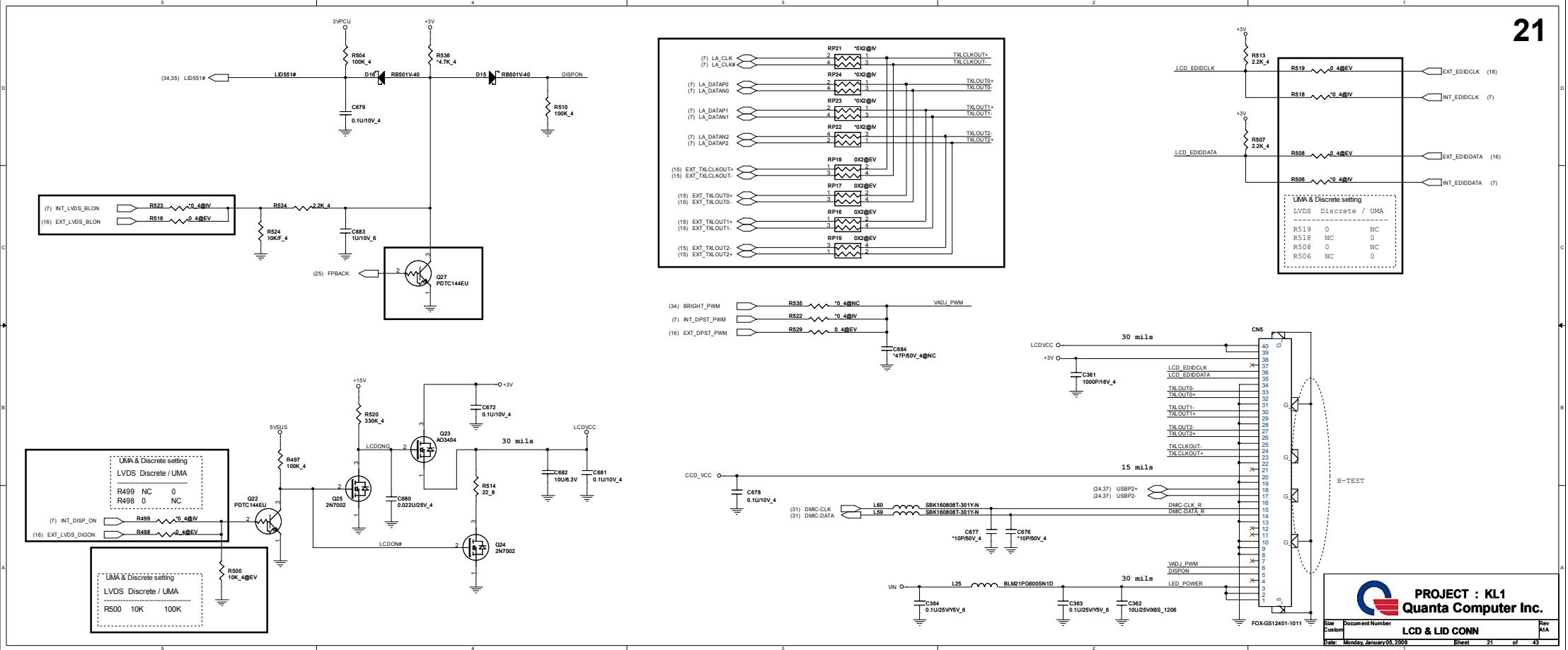


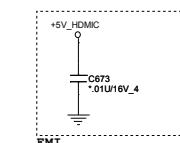
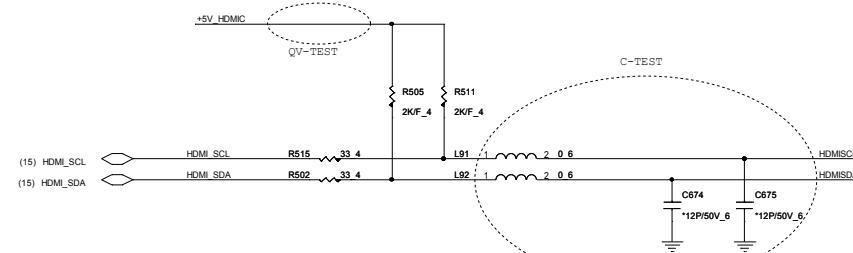
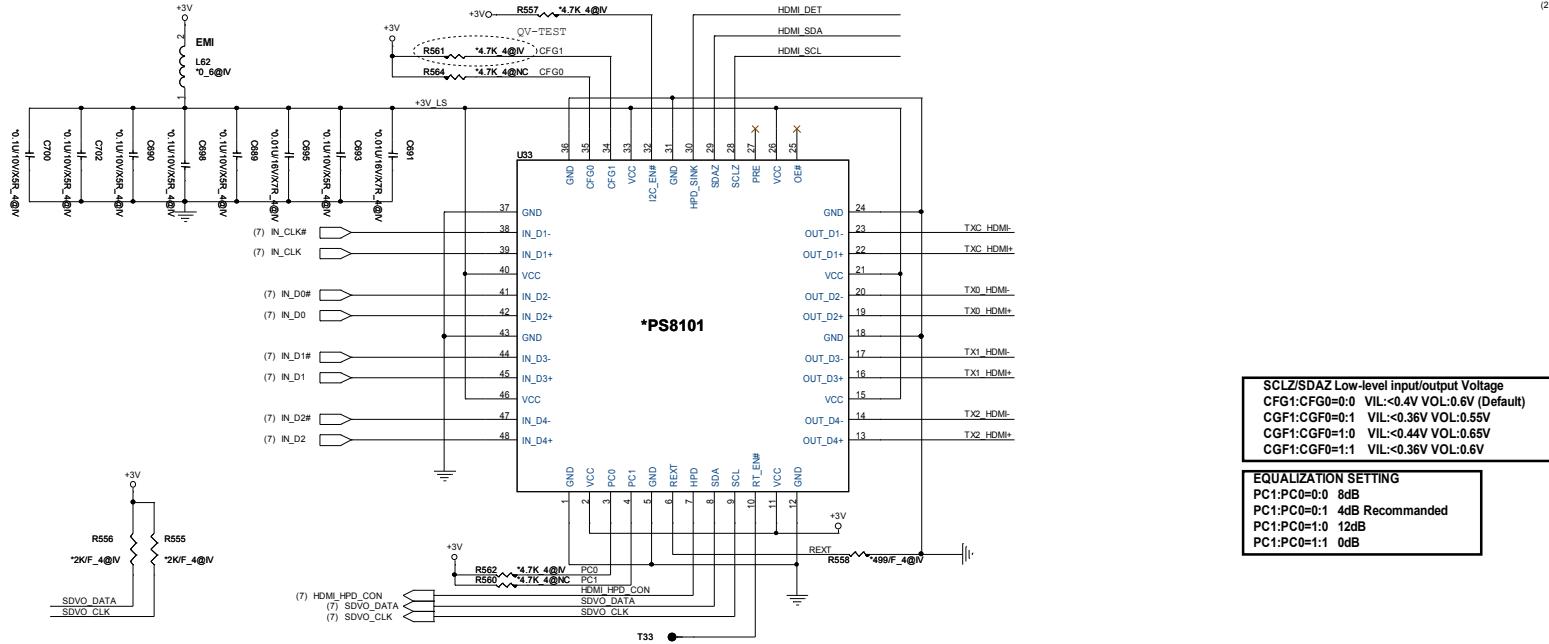
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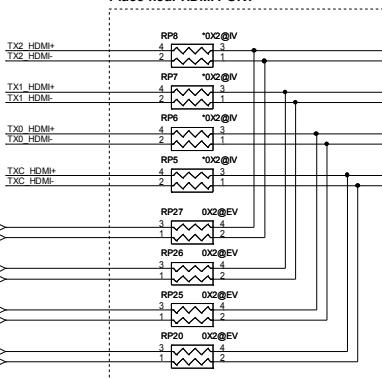
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Size	Document Number	Rev
Custom		A1A
Date: Monday, January 05, 2009	CRT_CONN	Sheet 20 of 43

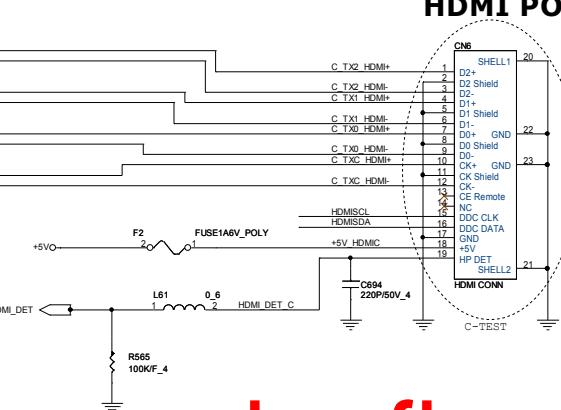




Place near HDMI PORT

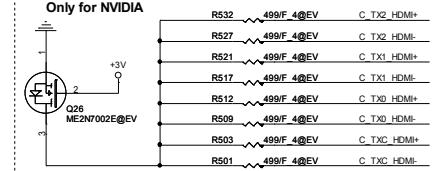


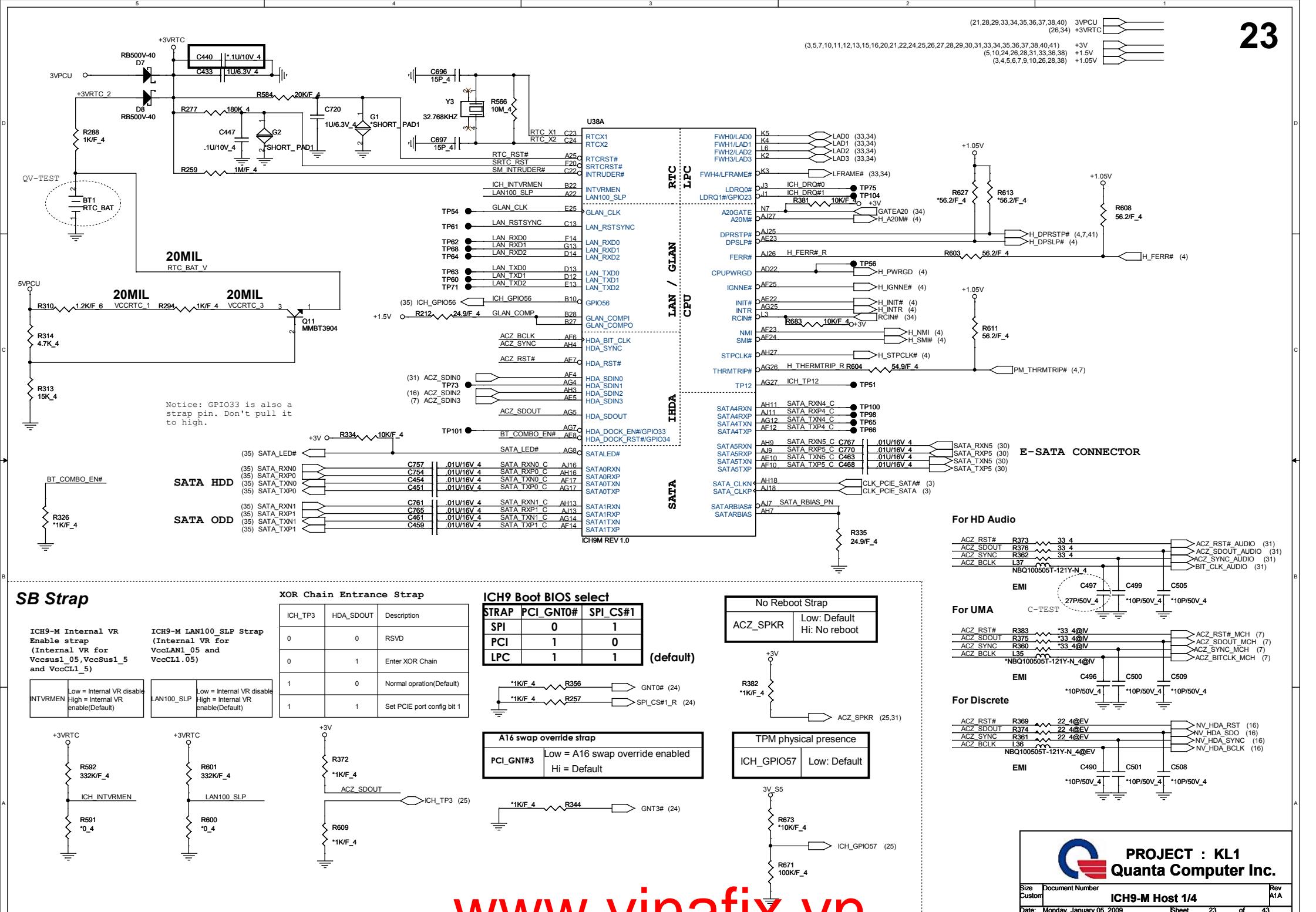
HDMI PORT

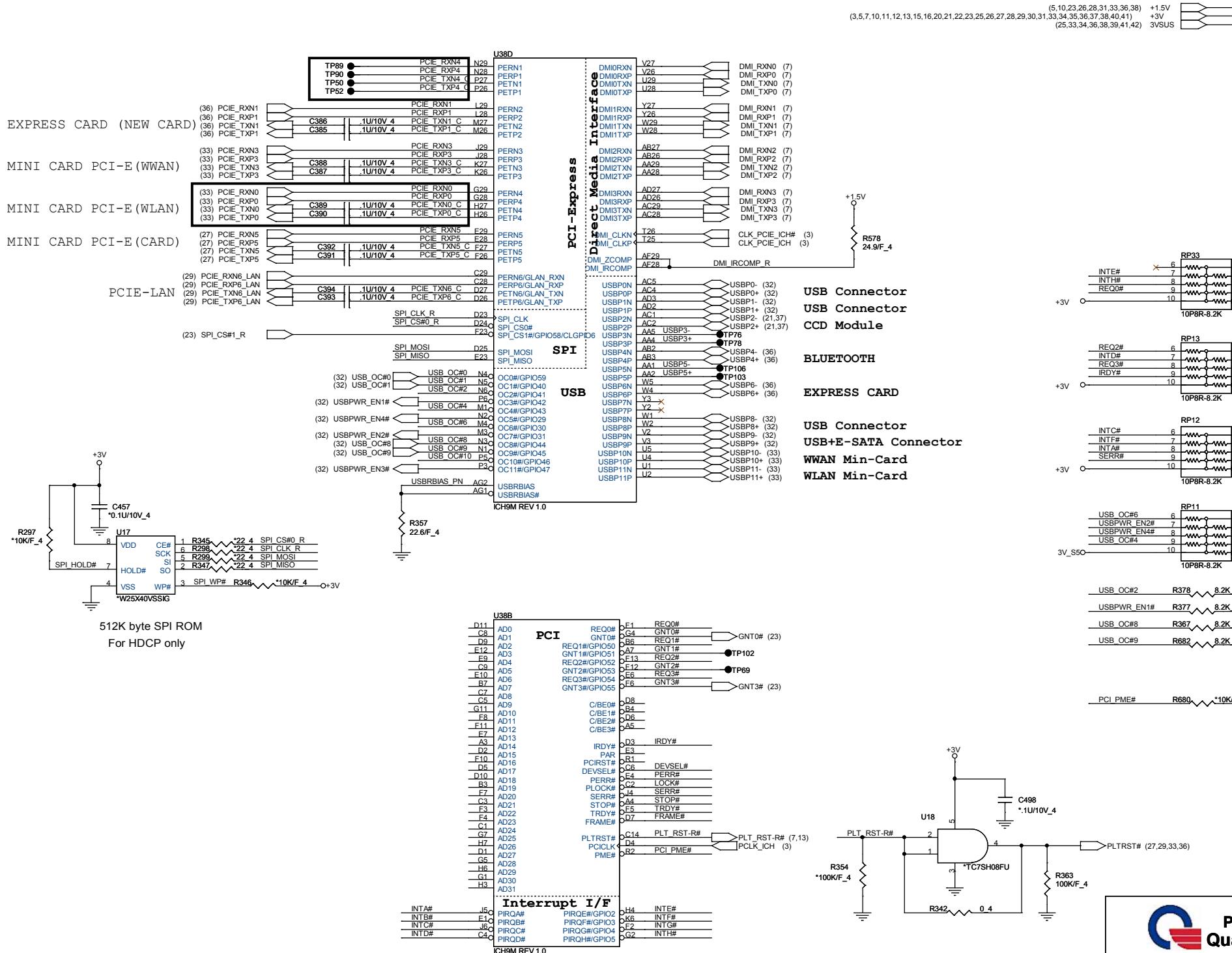


Place near HDMI PORT

Only for NVIDIA





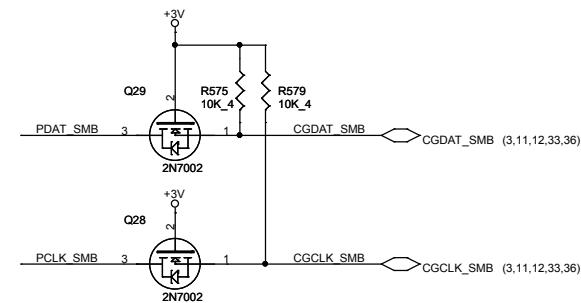
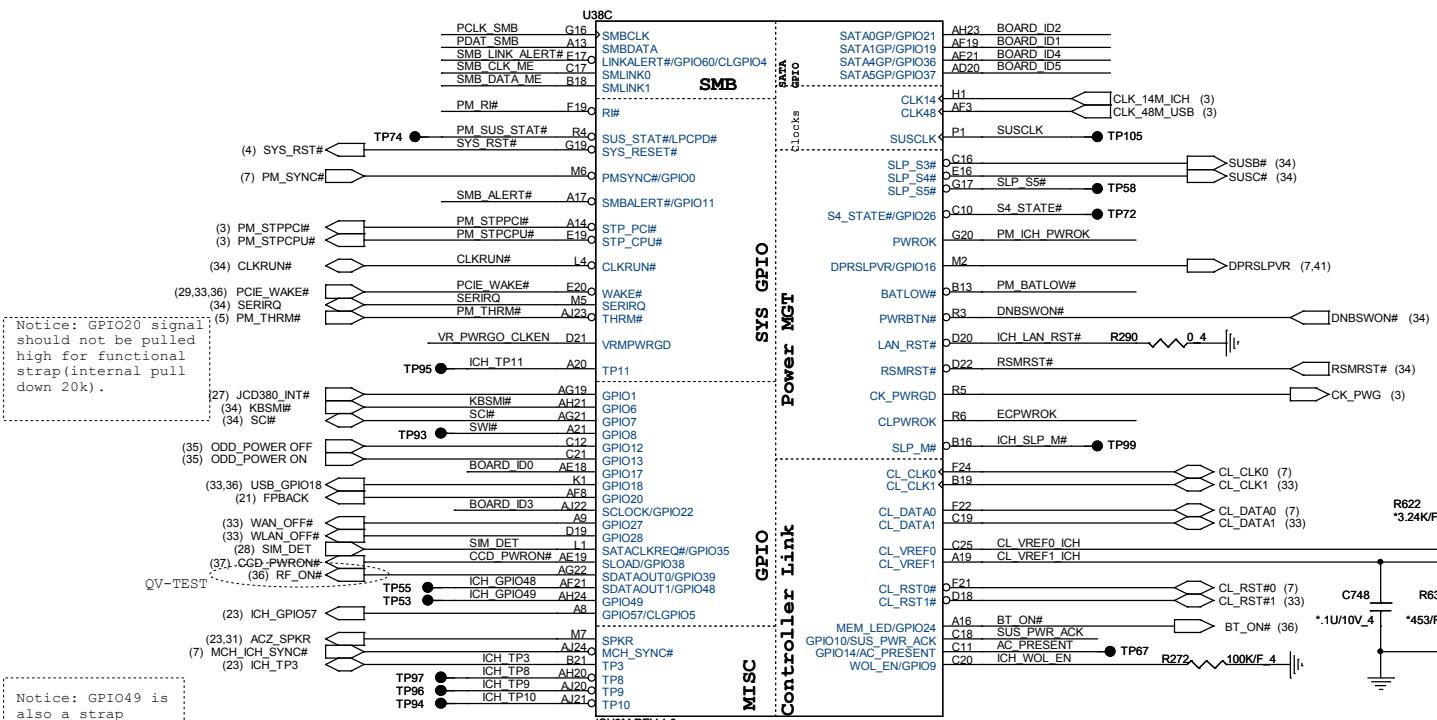


ICH9M REV 1.0
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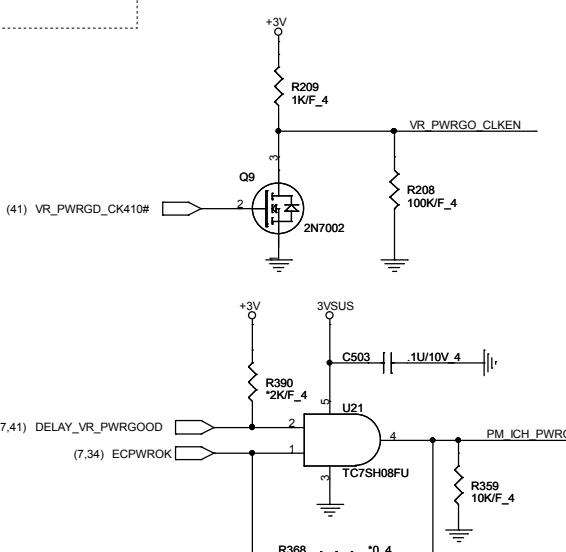
 PROJECT : KL1
Quanta Computer Inc.

Size Custom	Document Number ICH9-M PCIE 2/4	Rev A1A		
Date: Monday, January 05, 2009		Sheet 1	24	of 43

(3,5,7,10,11,12,13,15,16,20,21,22,23,24,26,27,28,29,30,31,33,34,35,36,37,38,40,41)
 (23,24,26,38) +3V
 (33,34,36,38,39,41,42) 3V_S5
 (33,34,36,38,39,41,42) 3VSUS

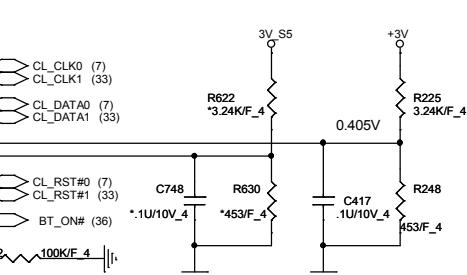


SW#	R617	~10K/F_4
PM_RI#	R304	~10K/F_4
SMB_CLK_ME	R317	~10K/F_4
SMB_DATA_ME	R650	~10K/F_4
DNBSWON#	R681	~10K/F_4
PCLK_SMB	R289	~2.2K_4
PDAT_SMB	R322	~2.2K_4
SMB_ALERT#	R657	~10K/F_4
PCIE_WAKE#	R295	~10K/F_4
PM_BATLOW#	R319	~8.2K_4
SMB_LINK_ALERT#	R324	~10K/F_4
SYS_RST#	R276	~10K/F_4
BT_ON#	R309	~10K/F_4

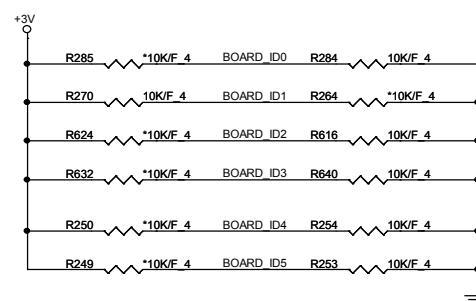


Board ID For Function	ID3 GPIO22	ID2 GPIO21	ID1 GPIO19	ID0 GPIO17
Default	0	0	1	0
GL40	0	1	1	0
GM47	1	0	1	0

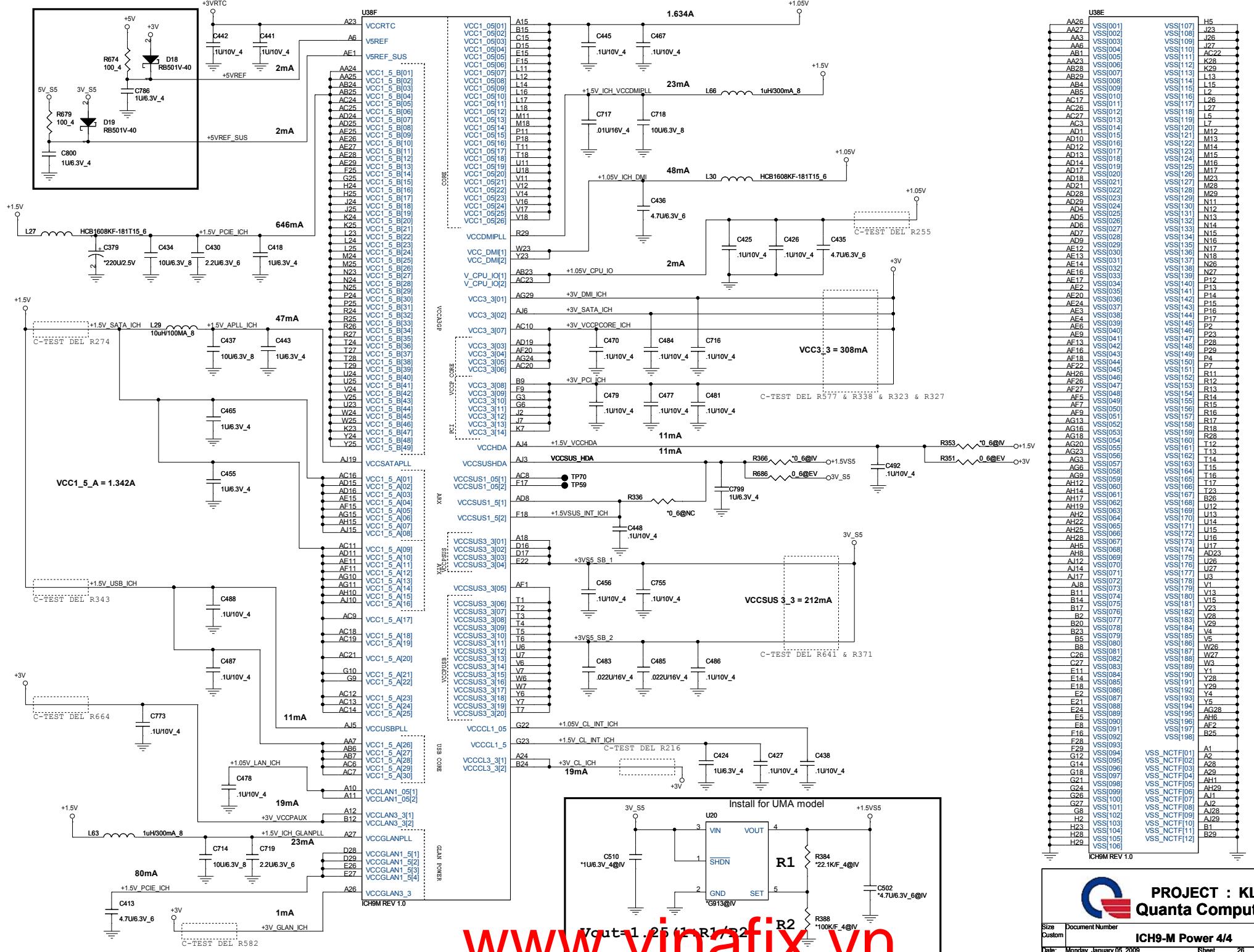
Board ID For Model	ID5 GPIO37	ID4 GPIO36
LE6	0	0
LE7	0	1
LE8	1	0
LE9	1	1
KL1	0	0



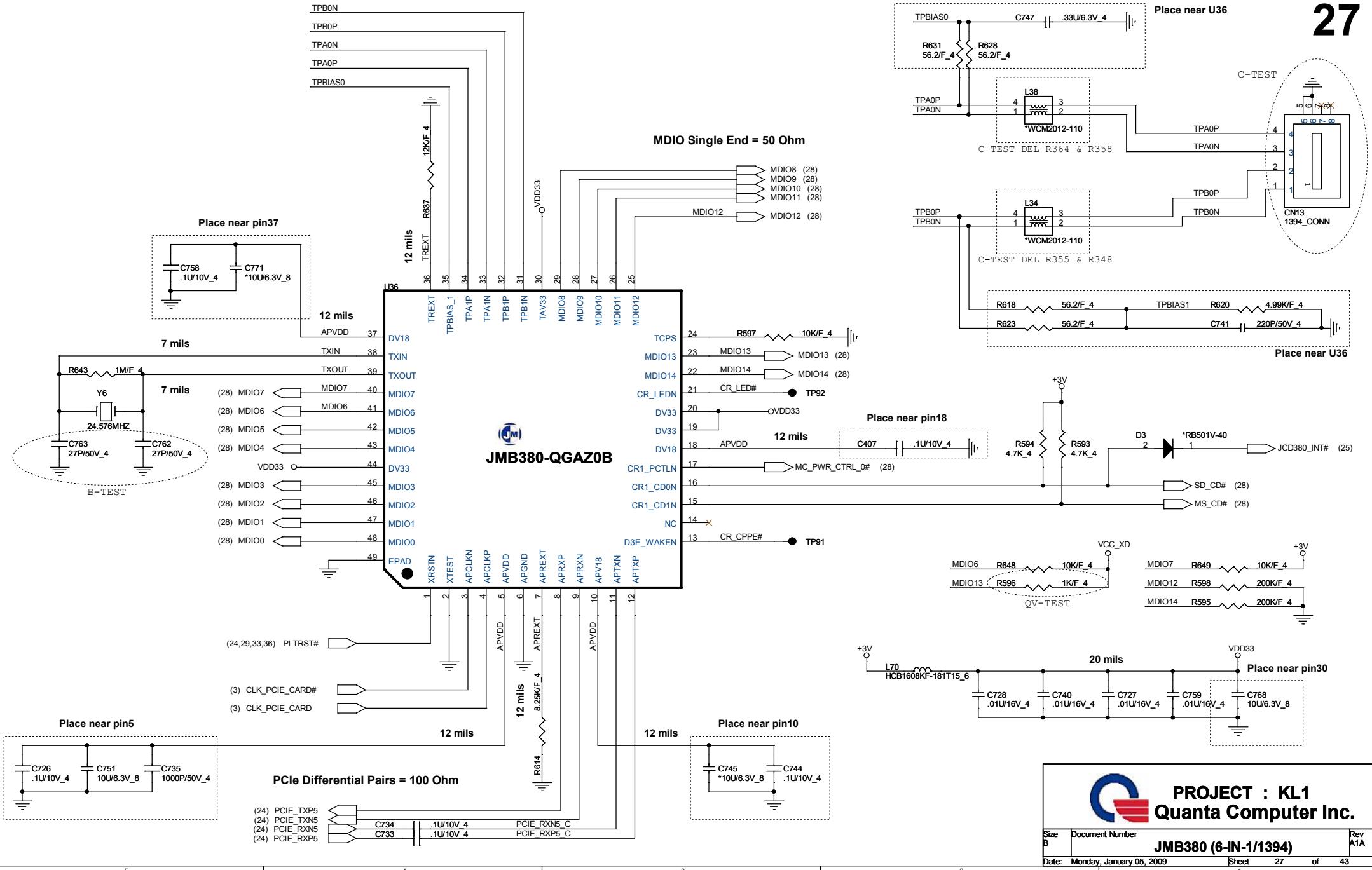
ICH_TP3	R610	~10K/F_4
PM_THRM#	R621	~8.2K_4
SERIRQ	R380	~10K/F_4
CLKRUN#	R379	~8.2K_4
KBSMI#	R633	~10K/F_4
SC#	R252	~10K/F_4
SIM_DET	R684	~10K/F_4



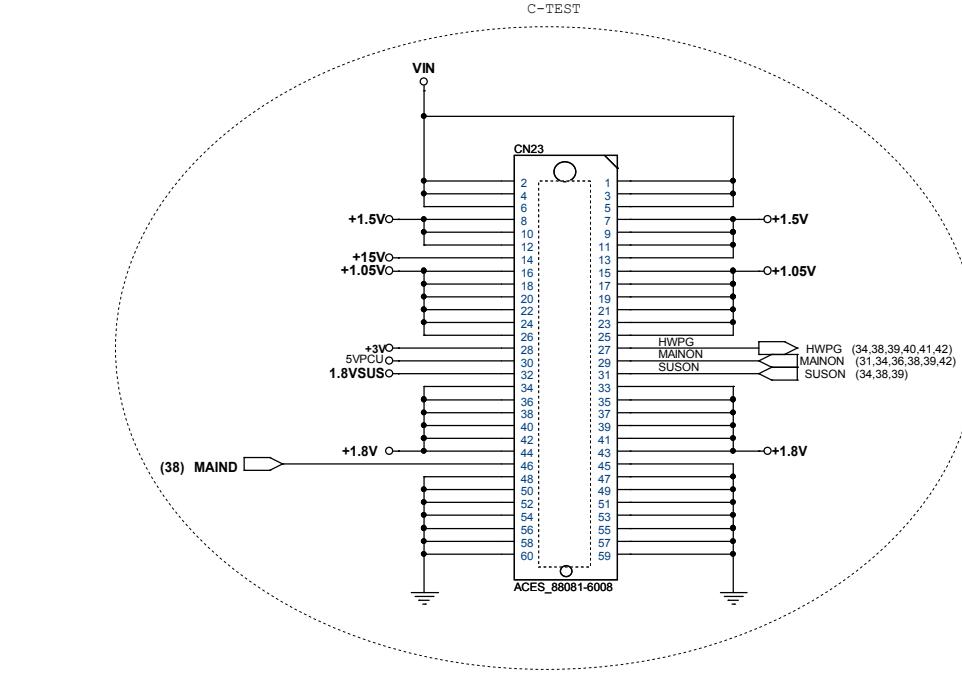
CDC_PWRON#	R271	~10K_4
SUS_PWR_ACK	R287	~10K/F_4
RSMRST#	R258	~10K/F_4



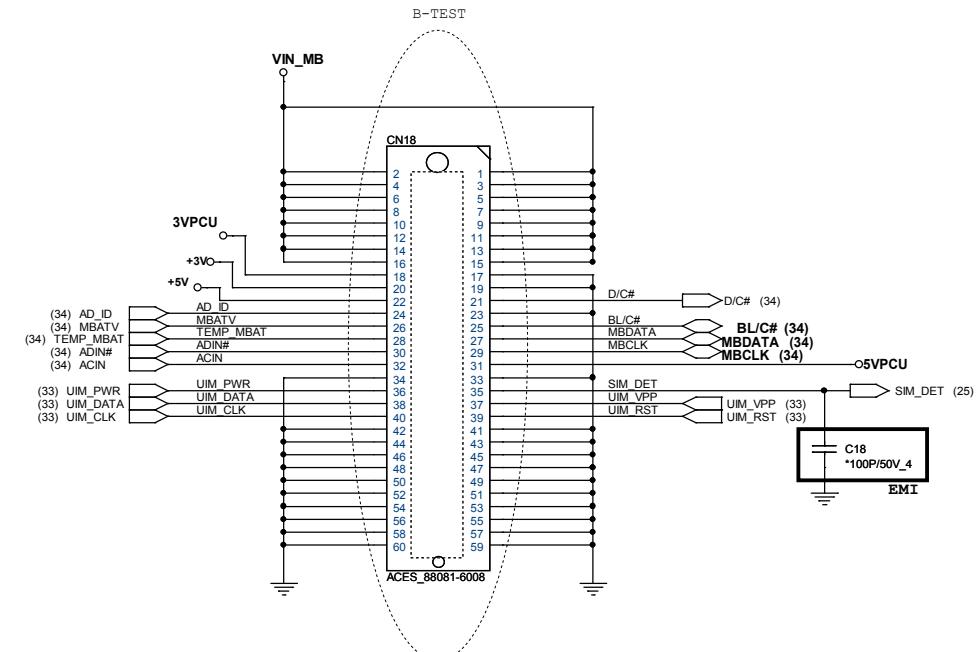
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Power Board B to B 60PIN CONN

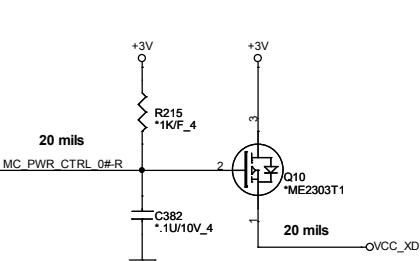
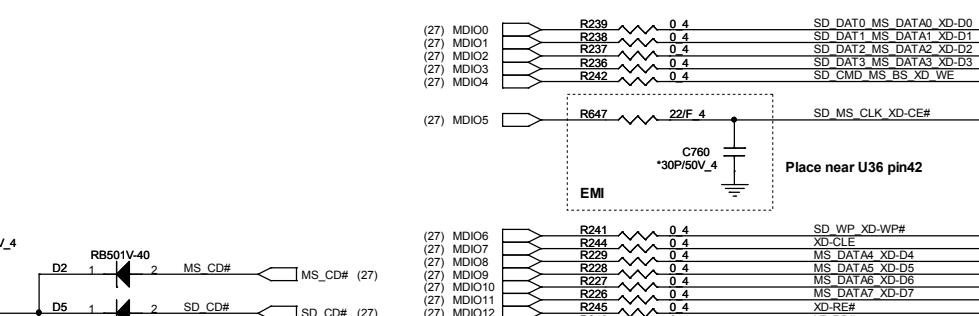
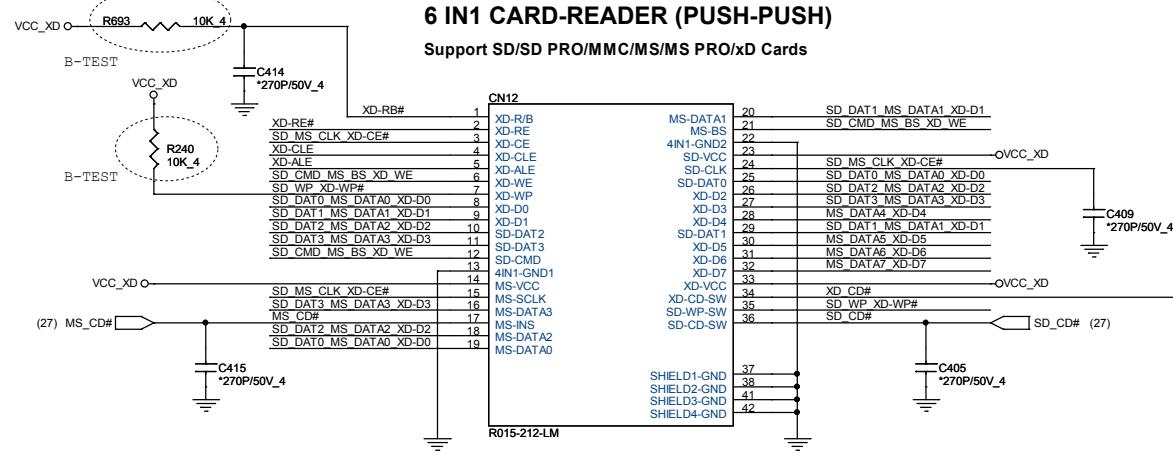


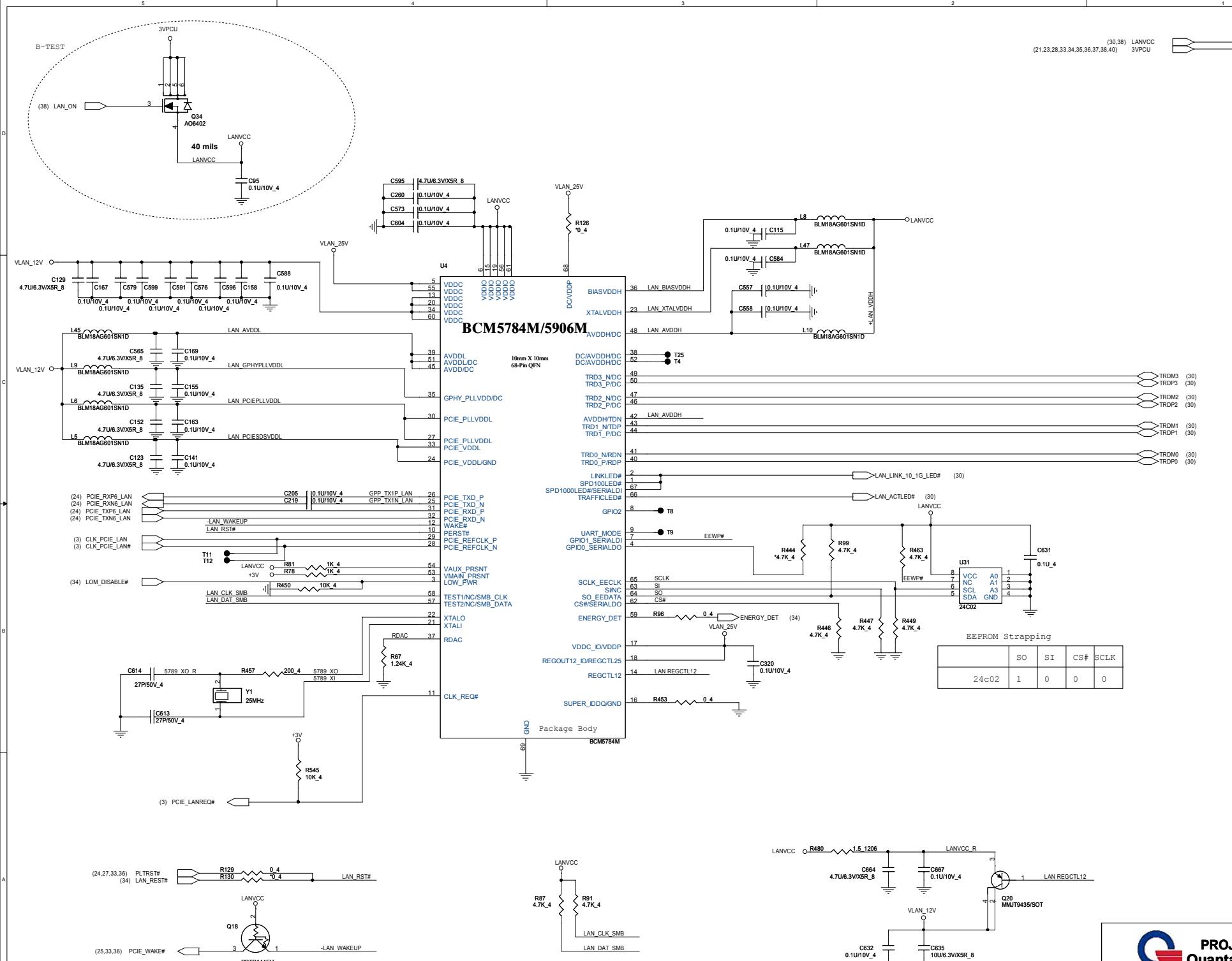
Charger+SIM CARD B to B 60PIN CONN



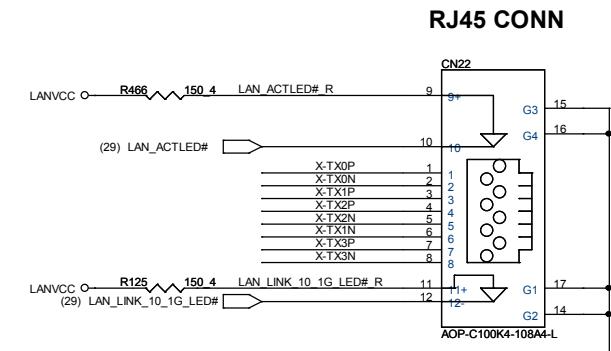
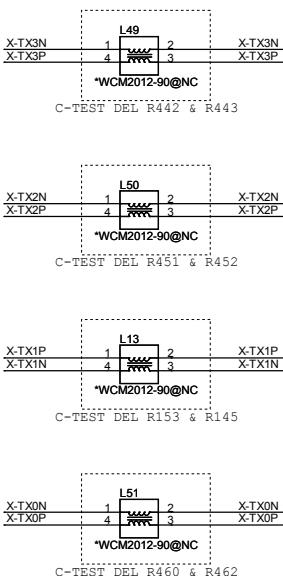
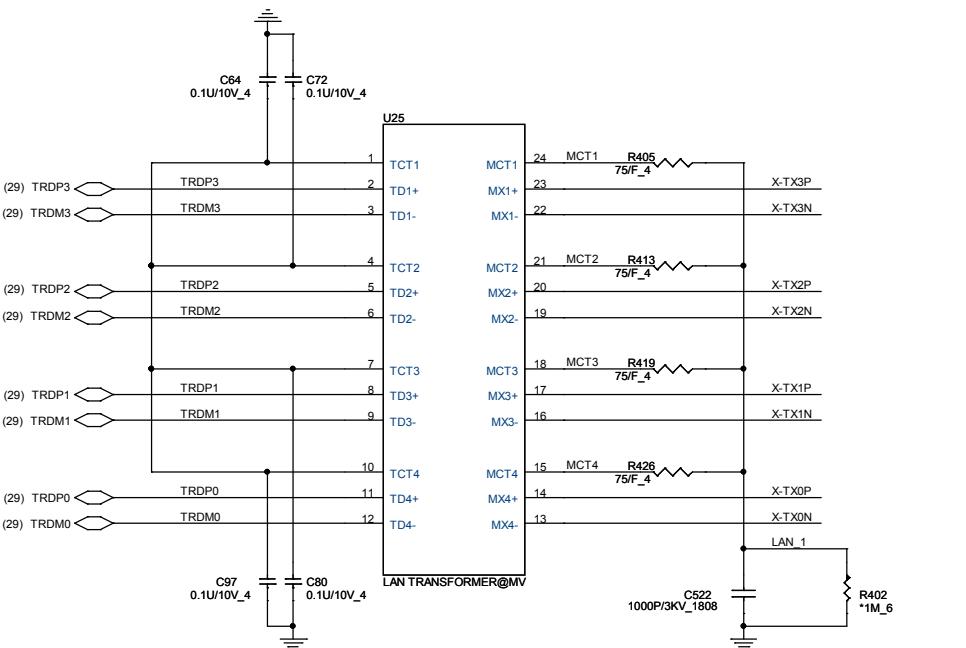
6 IN1 CARD-READER (PUSH-PUSH)

Support SD/SD PRO/MMC/MS/MS PRO/xD Cards

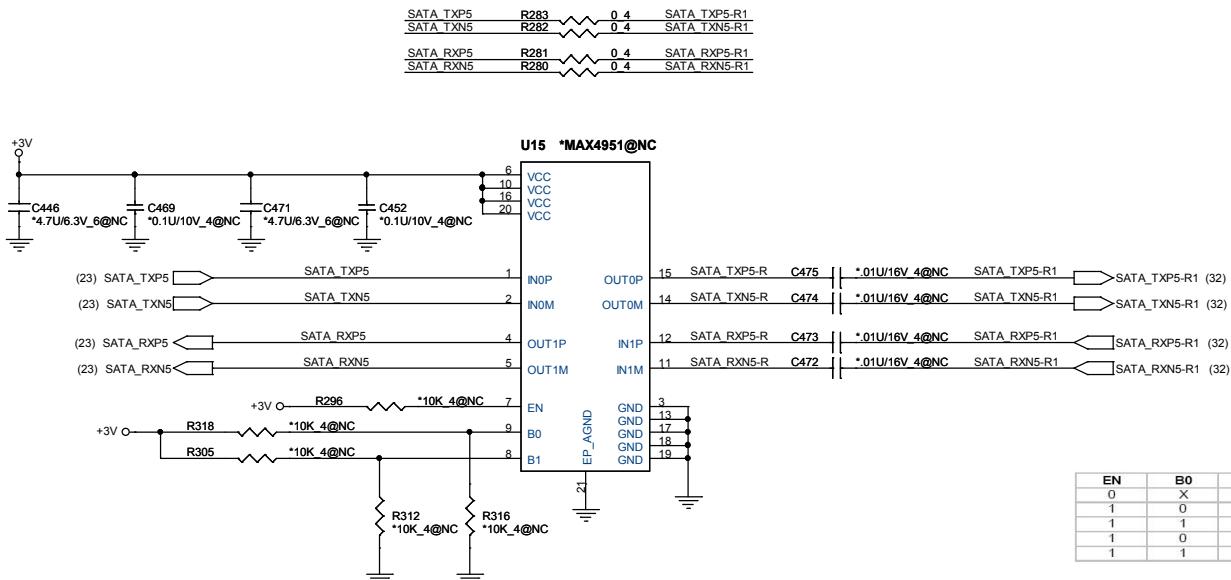


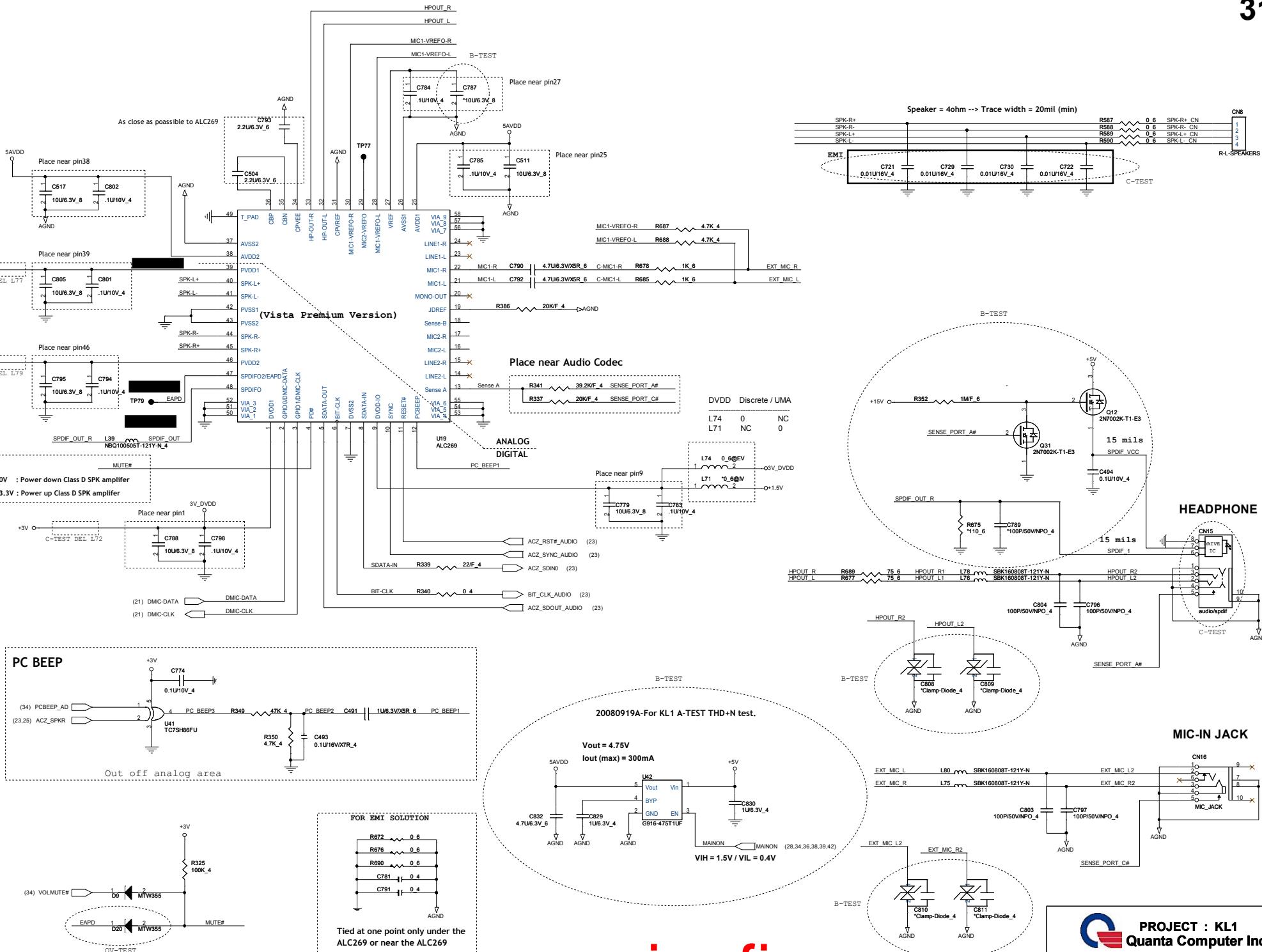


(29,38) LANVCC

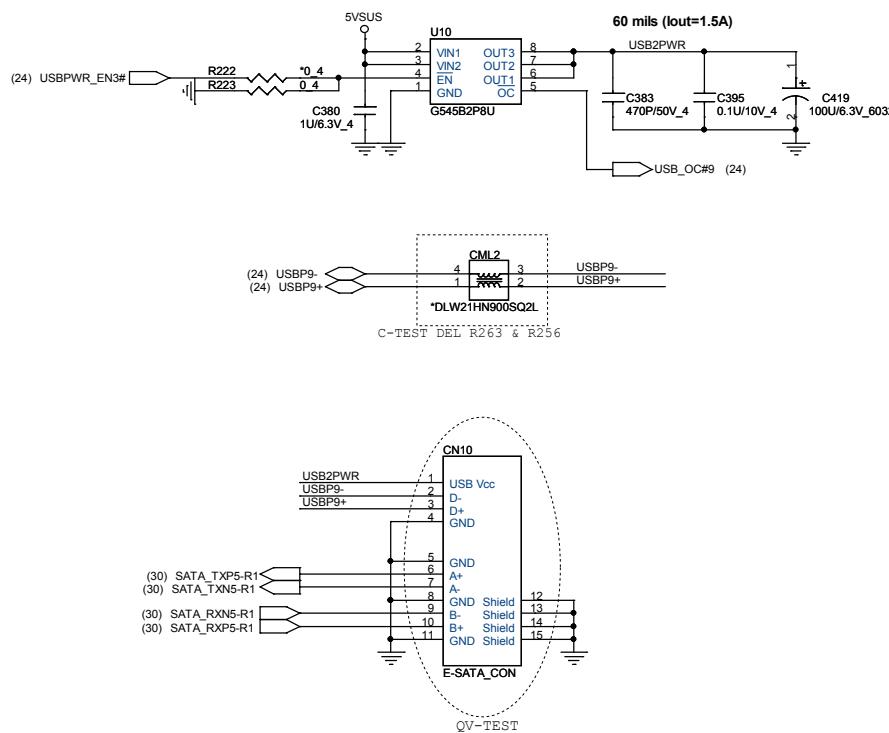


E-SATA RE-DRIVER

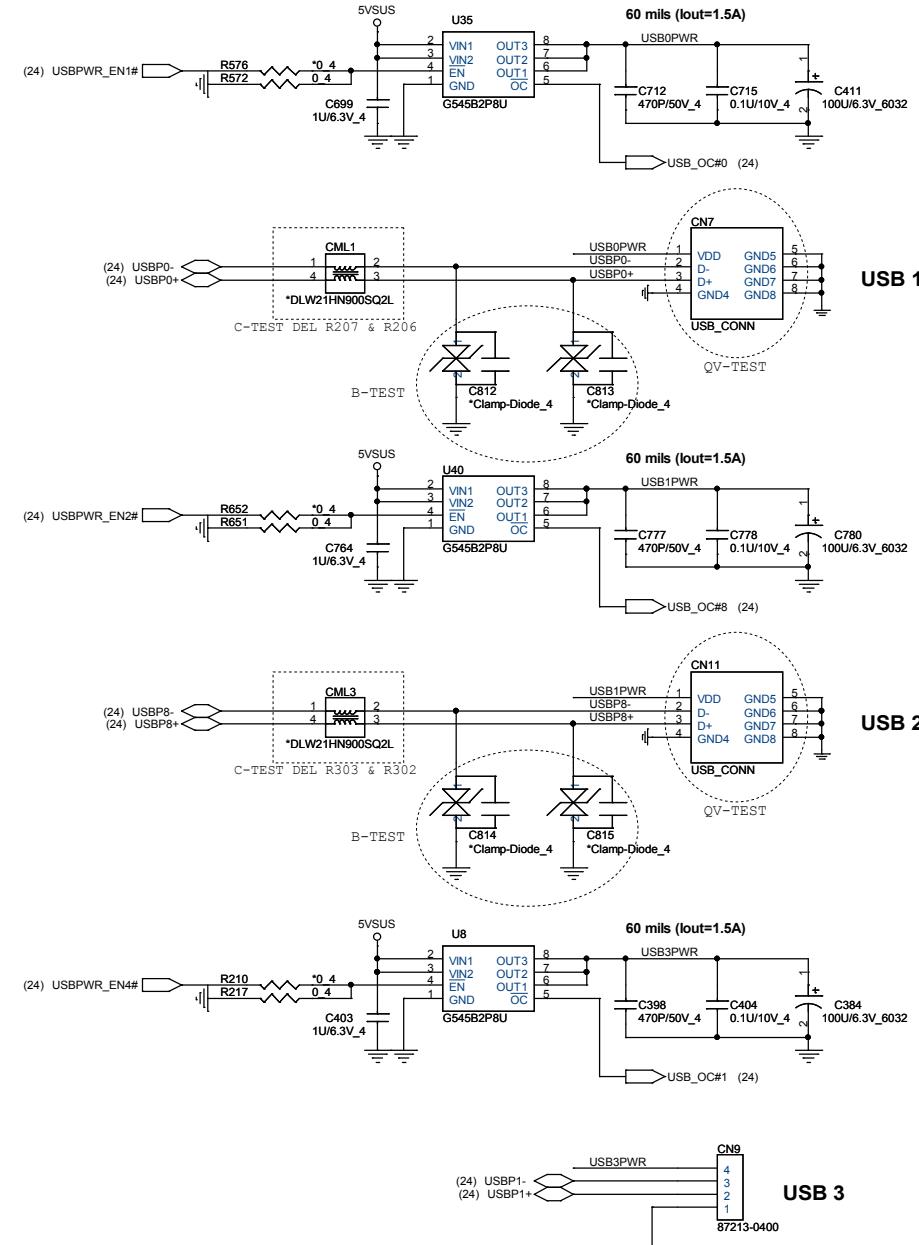




USB + eSATA CONNECTOR



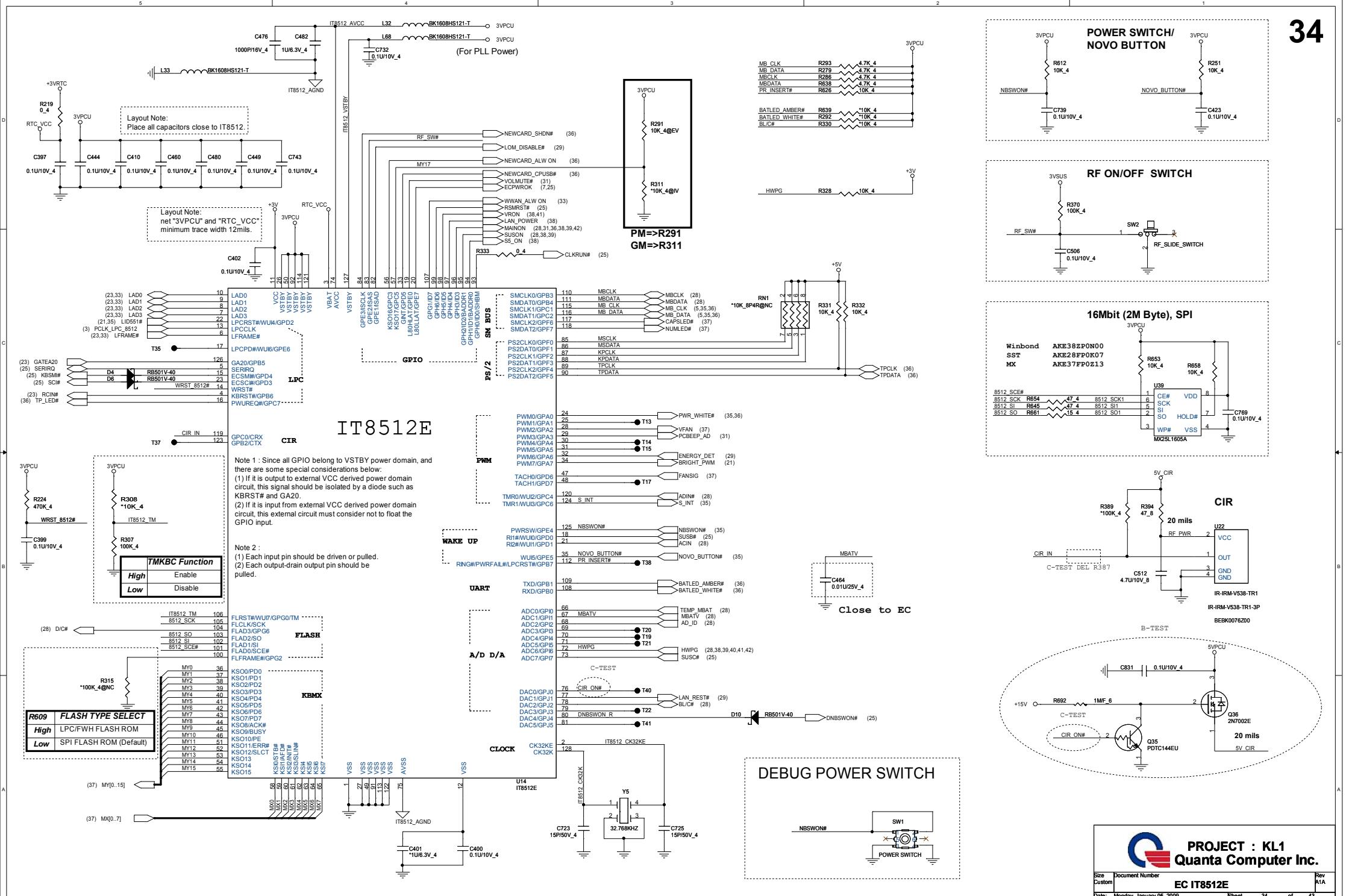
USBX3



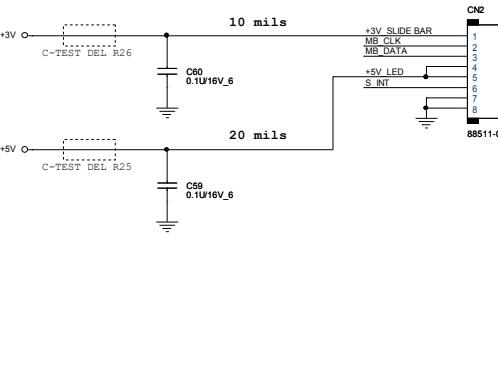
 PROJECT : KL1
Quanta Computer Inc.

Size	Document Number	Rev
Custom	USBX3, USB+E-SATA CONN	A1A

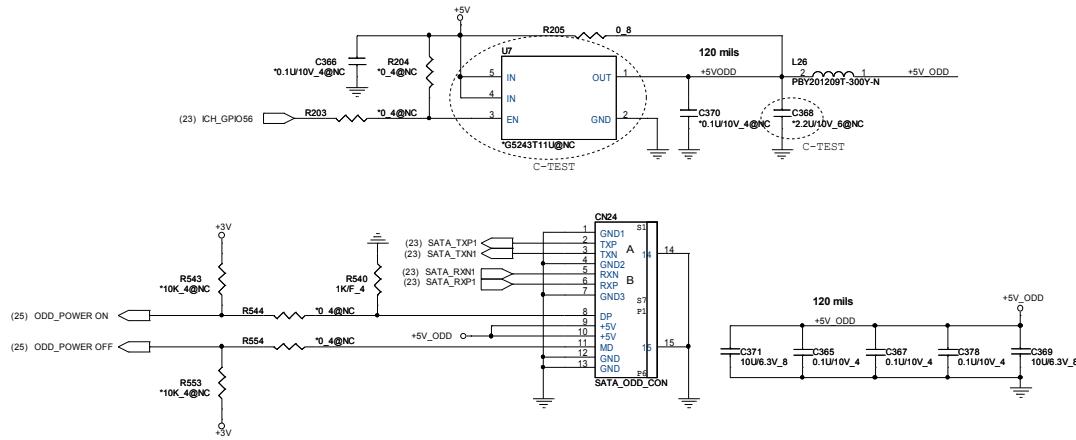
Date: Monday, January 05, 2009 Sheet 32 of 43



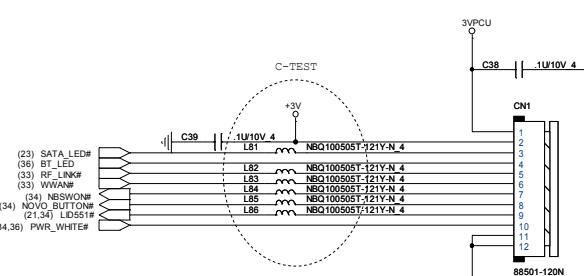
SLIDE BAR CONN



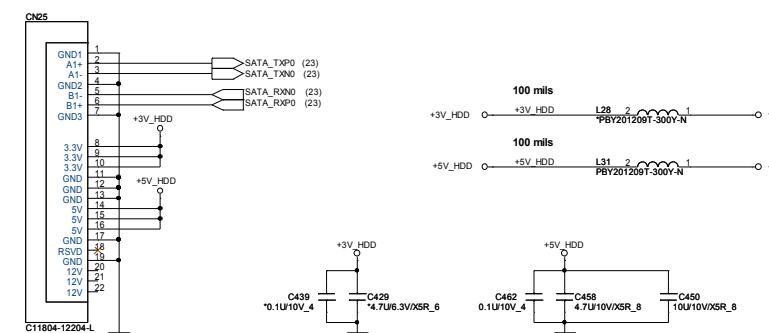
SATA CD-ROM



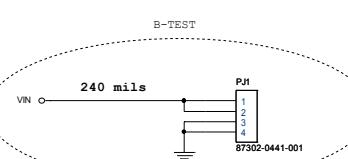
POWER BUTTON/B CONN



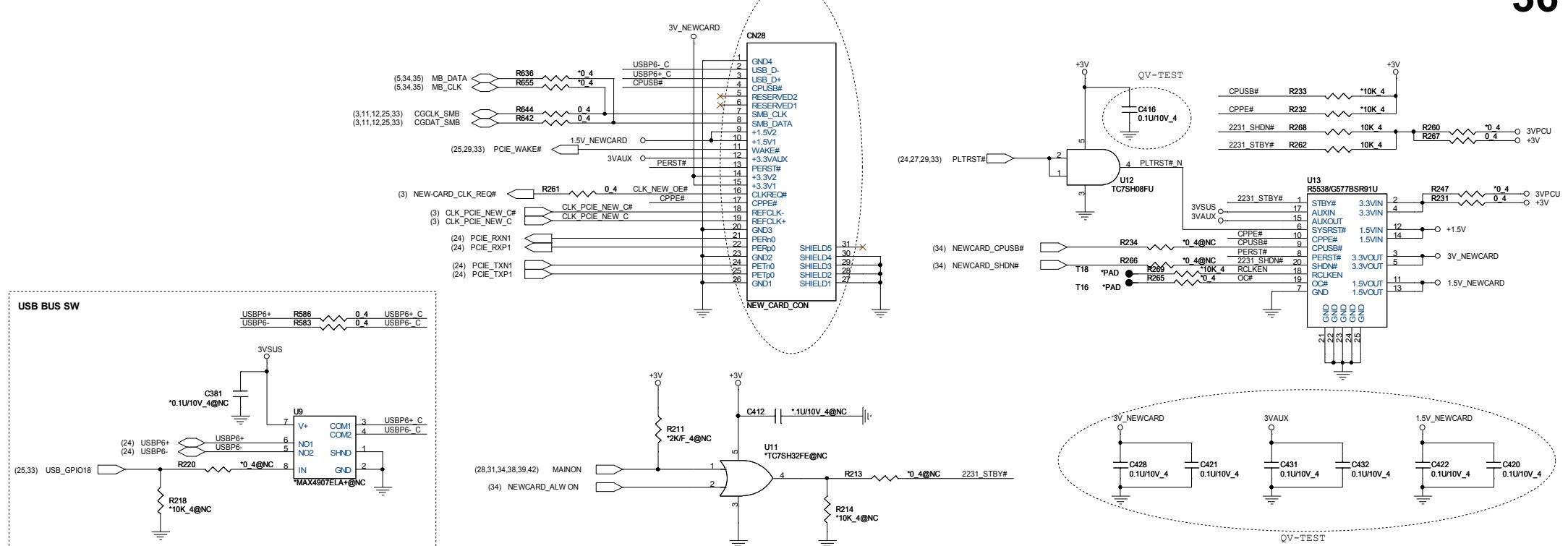
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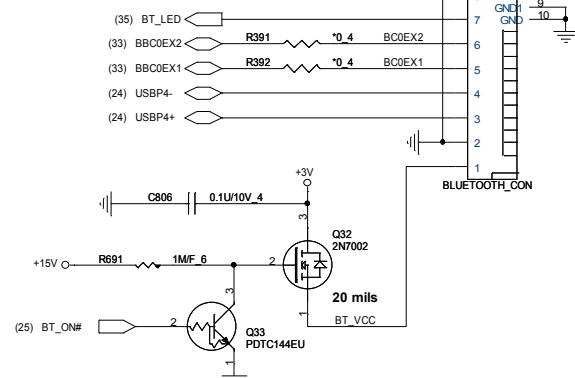
VIN CABLE CONN



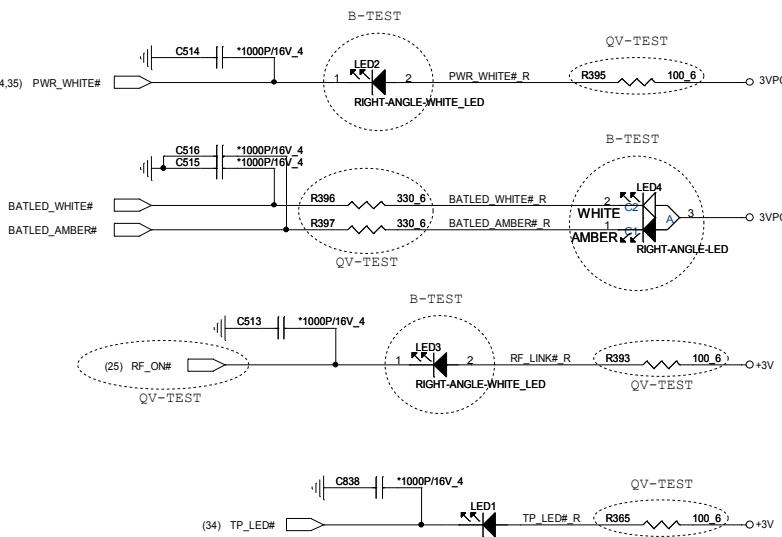
NEWCARD



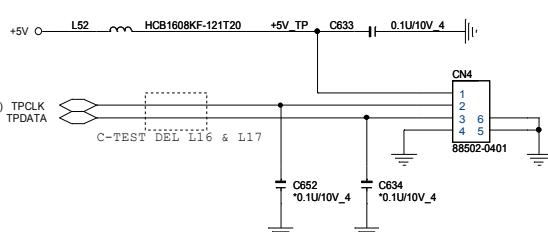
BLUETOOTH



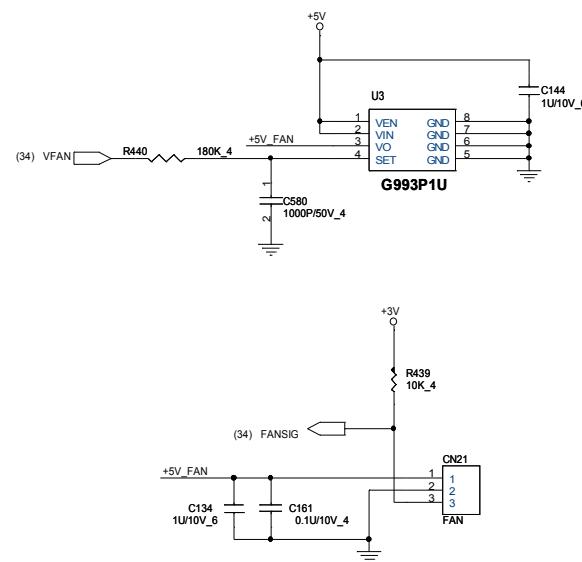
LED



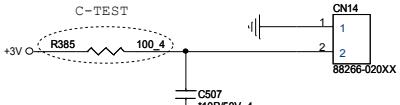
TOUCH PAD



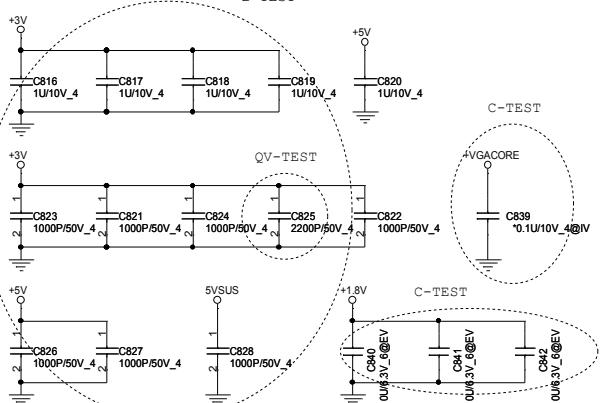
FAN CONTROL



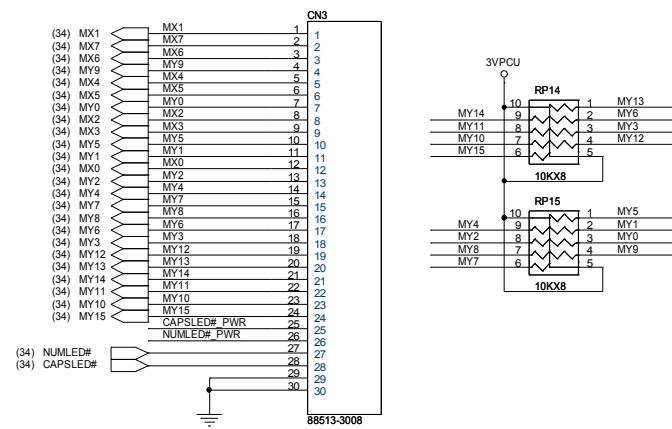
Logo LED CONN



EMI CAPACITORS



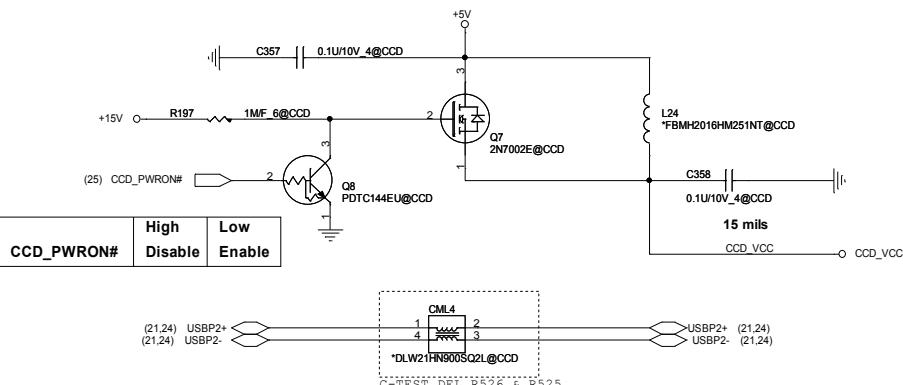
KEYBOARD



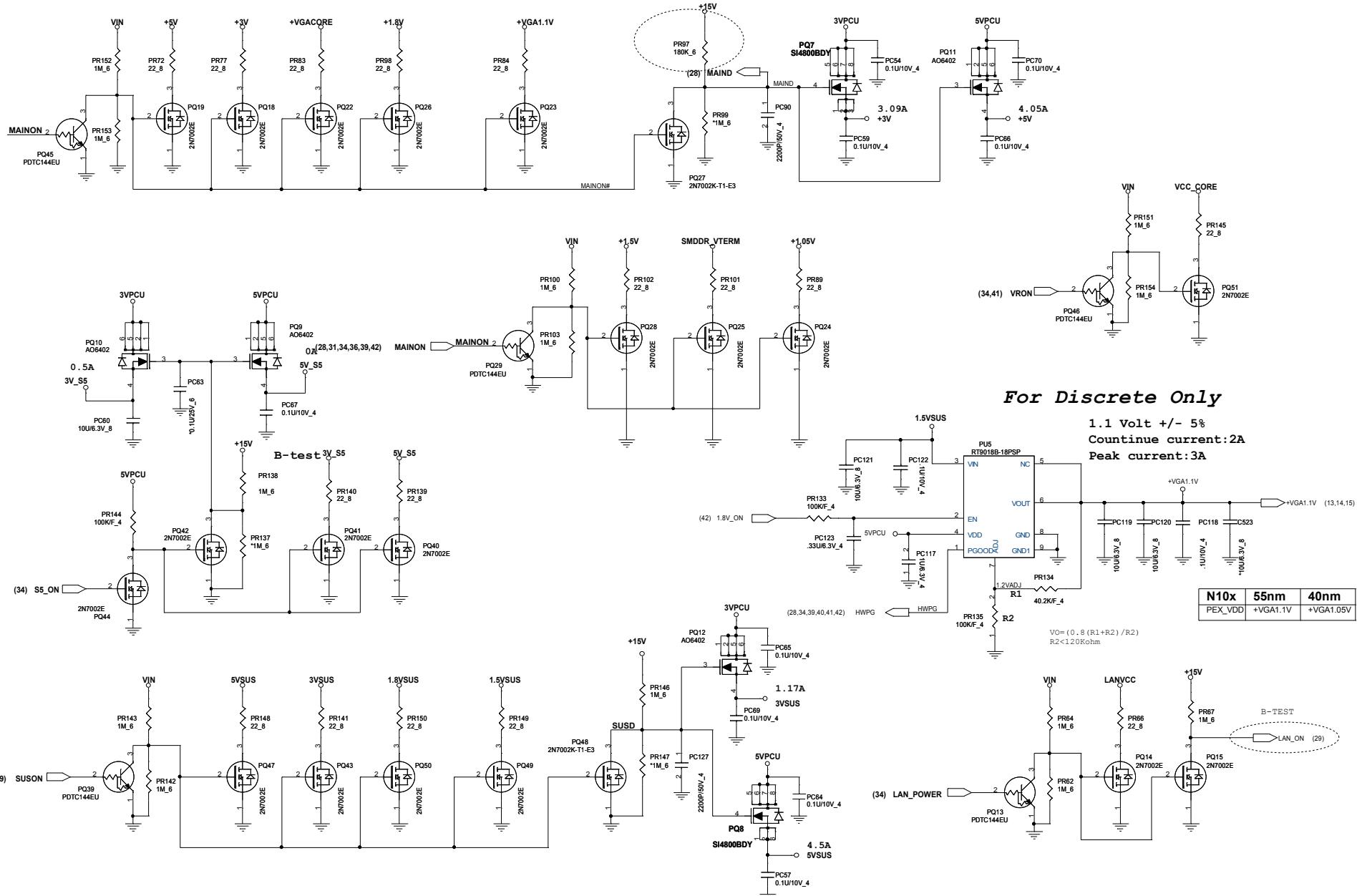
For EMI request

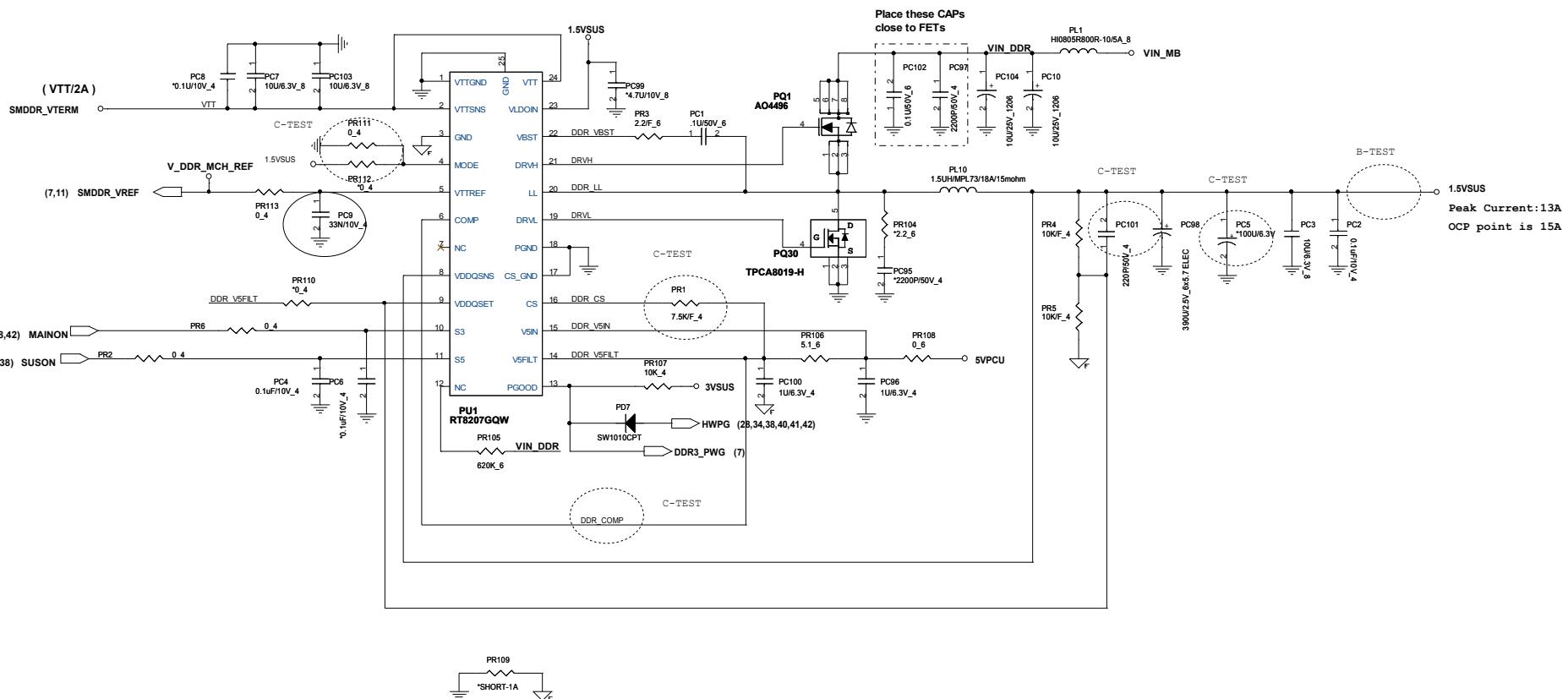


CCD MODULE

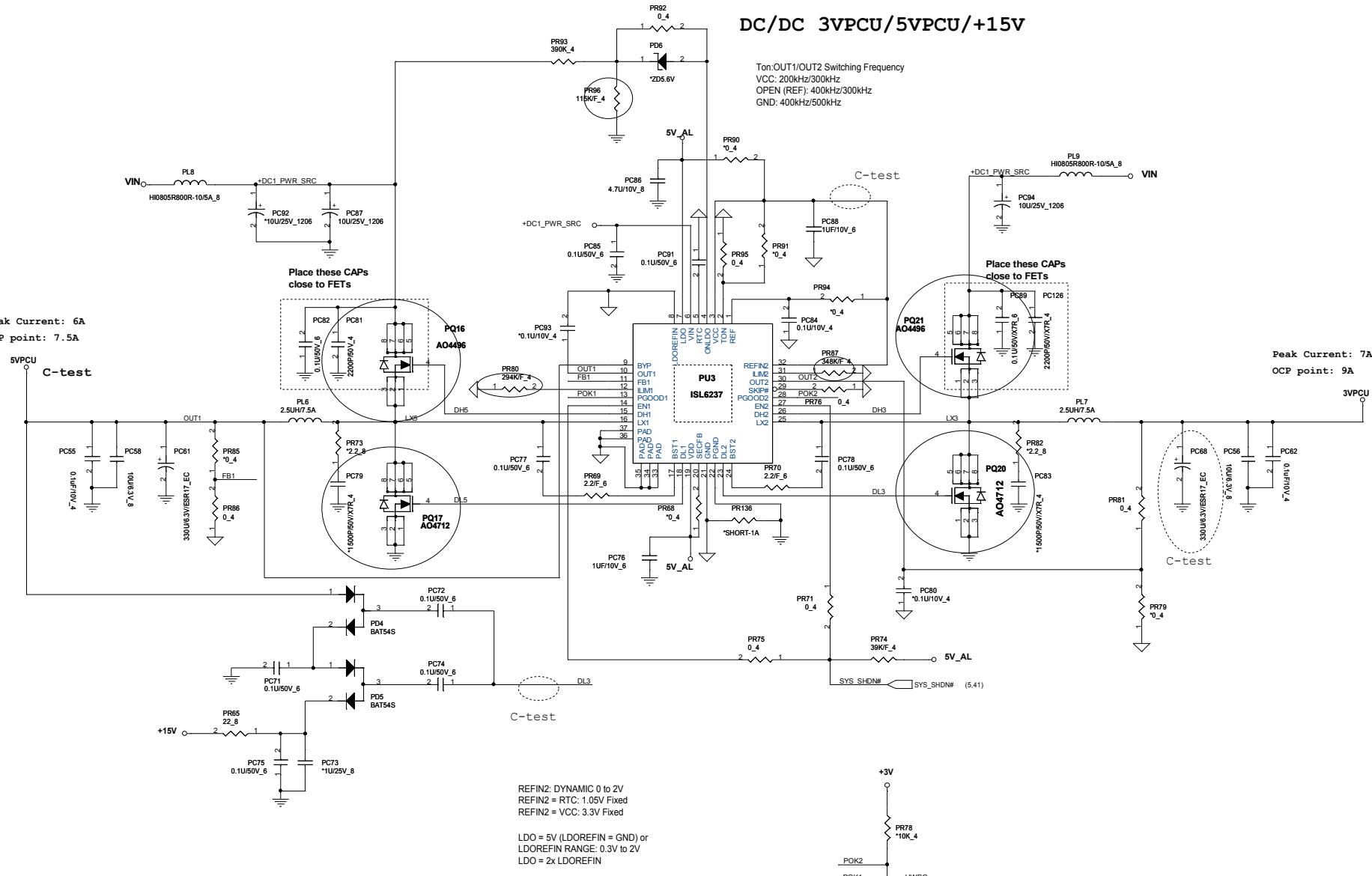


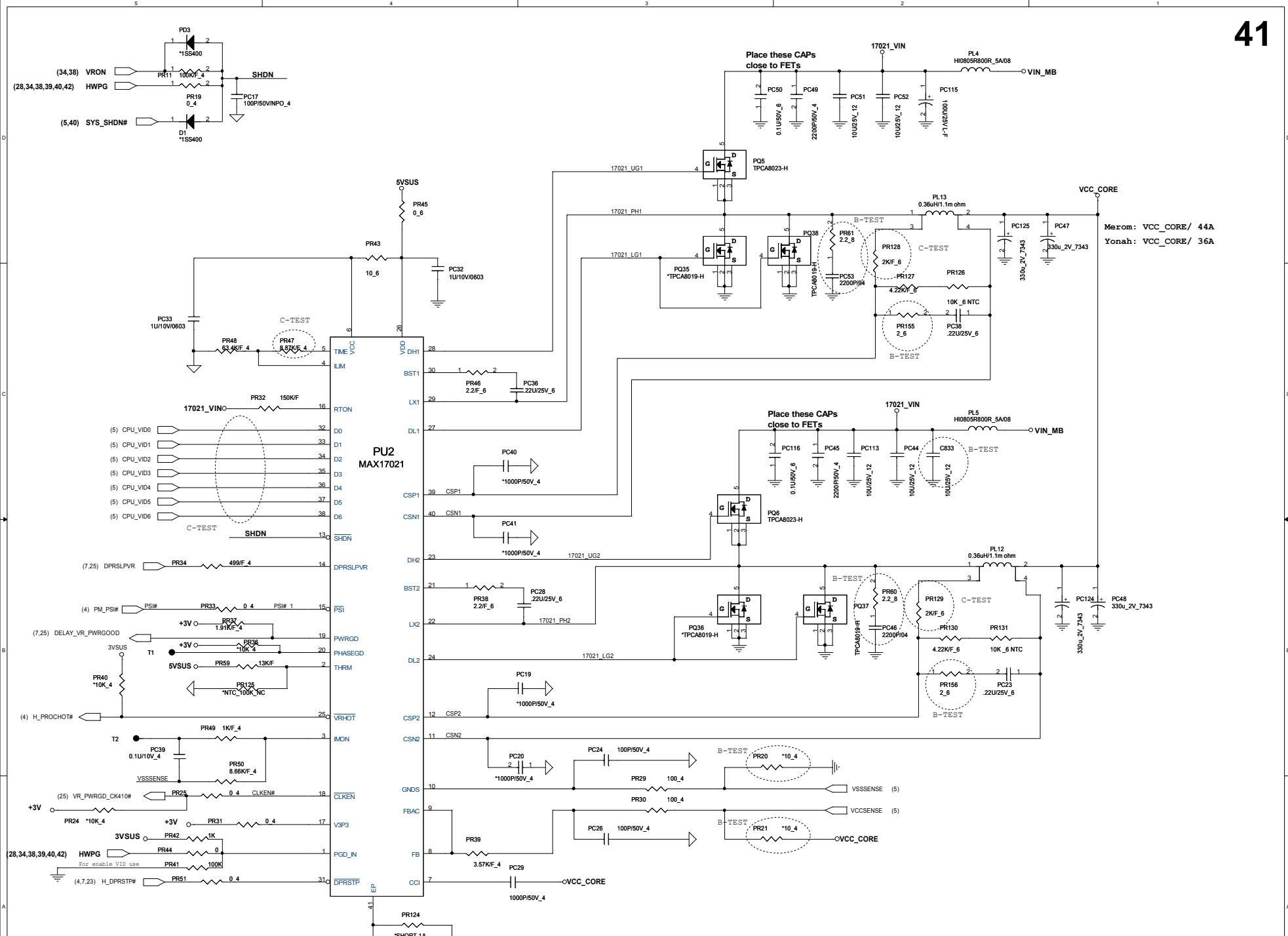
DISCHARGE





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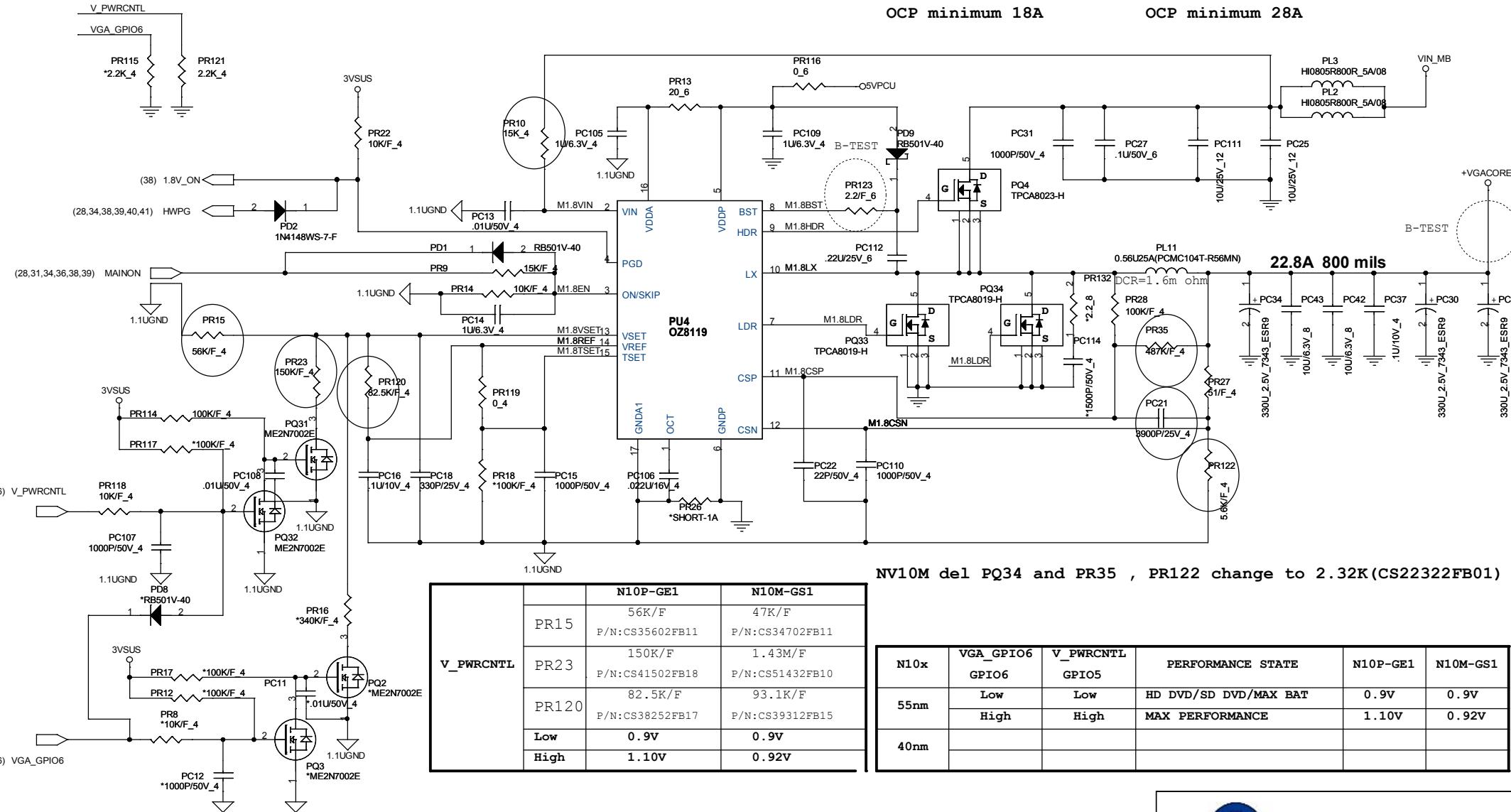


VGA Core & VCC1.1

NV10M +0.92Volt +/- 5%
Countinue current:13A
Peak current:16A
OCP minimum 18A

NV10P +1.1Volt +/- 5%
Countinue current:20A
Peak current:23A
OCP minimum 28A

42



NV10M del PQ34 and PR35 , PR122 change to 2.32K(CS22322FB01)

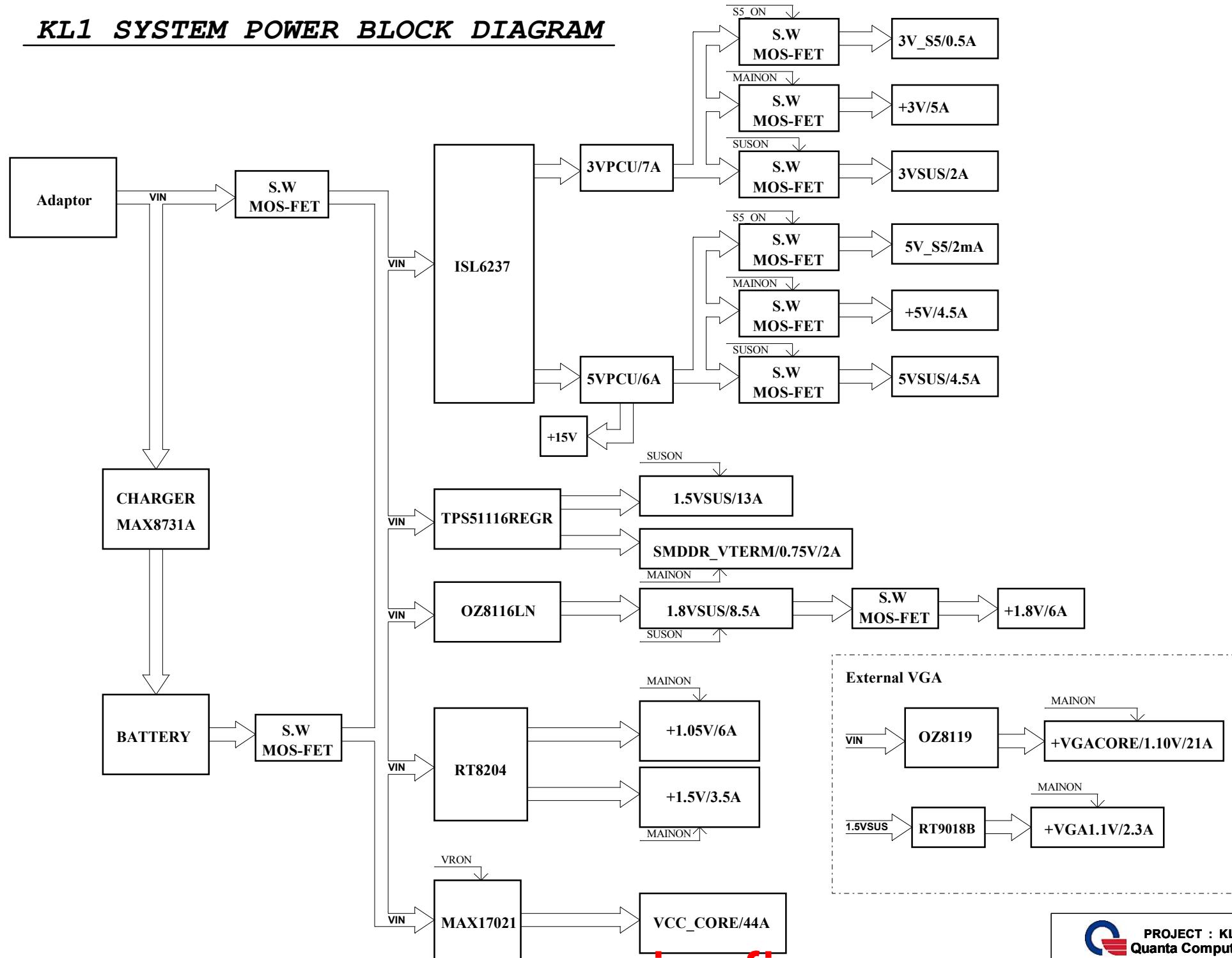
	N10P-GE1	N10M-GS1
V_PWRCNTL	56K/F P/N:CS35602FB11	47K/F P/N:CS34702FB11
	150K/F P/N:CS41502FB18	1.43M/F P/N:CS51432FB10
	82.5K/F P/N:CS38252FB17	93.1K/F P/N:CS39312FB15
	Low	0.9V
	High	1.10V
		0.92V

N10x	VGA_GPIO6 GPIO6	V_PWRCNTL GPIO5	PERFORMANCE STATE	N10P-GE1	N10M-GS1
55nm	Low	Low	HD DVD/SD DVD/MAX BAT	0.9V	0.9V
	High	High	MAX PERFORMANCE	1.10V	0.92V
40nm					

 PROJECT : KL1
Quanta Computer Inc.

Size	Document Number	Rev
B	VGA Core (OZ8119)	A1A
Date:	Monday, January 05, 2009	Sheet 42 of 43

KL1 SYSTEM POWER BLOCK DIAGRAM



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