

Homework Assignments and Due Dates

Note: in those HW problems where you are asked to write VHDL code, write the code but you do not have to implement it on the CAD tool. We will do that in lab.

1) Chapter 2: Read entire chapter, to be completed by 2/3/15.

Do HW problems 2.1, 2, 3, 4, 5, 8, 10, 12, 15, 31, 32, 33, 34, 37, 38, 41.

Written homework assignment due: Feb 5, 2015 at 2:00 PM.

2) Chapter 3: Read Sections 3.1 to 3.11 to be completed by 2/17/2015.

Do HW problems 3.1, 2, 3, 4, 5, 12, 13, 36, 37, 38, 39, 43, 44, 45, 46, 49, 51, 52, 53.

Written assignment due: Feb 19, 2015 by 2:00 PM.

3) Chapter 4: Read Sections 4.1 to 4.12 to be completed by 3/4/2015.

Do HW problems 4.1, 2, 3, 4, 6, 7, 12, 14, 16, 17, 20, 35, 37, 45, 46, 49.

Written assignment due: March 5, 2015 by COB.

4) Chapter 5 Read Sections 5.1 to 5.8 to be completed by 3/25/15.

Do HW problems 5.1, 2, 3, 5, 7, 9, 12, 13, 17, 18, 21, 23, 26, 28.

Written assignment due: March 26, 2015 by COB.

5) Chapter 6 Read Sections 6.1 to 6.7 to be completed by 4/8/15.

Do HW problems 6.1, 2, 3, 6, 11, 16, 17, 18, 19, 30, 33, 34, 37.

Written assignment due: April 9, 2015 by COB.

6) Chapter 7 Read Sections 7.1 to 7.13 to be completed by 4/22/15.

Do HW problems 7.1, 2, 3, 4, 5, 6, 10, 13, 15, 19, 22, 24, 26, 33, 36.

Written assignment due: April 23, 2015 by COB.

7) Chapter 8 Read Sections 8.1 to 8.13 to be completed by 4/29/15.

You will be responsible for HW problems 8.1, 2, 3, 5, 6, 7, 9, 11, 12, 14, 20, 23, 30, 31, 36, 37, 44.