A picture containing table

Description automatically generated

100%Done

2

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| inst | 9 bit raw binary  Inst[30]  Inst[14:12]  inst[6:2] | 9-bit HEX | ImmSel  (1bit) | RegWen  (1bit) | BSel(1bit) | ALU\_OP(4bit) | Control signal in HEX |
| ADDI | 0 000 00100 | 004 | 1 | 1 | 1 | 1111 | 7F |
| SLLI | 0 001 00100 | 024 | 1 | 1 | 1 | 0011 | 73 |
| XORI | 0 100 00100 | 084 | 1 | 1 | 1 | 0100 | 74 |
| SRLI | 0 101 00100 | 0A4 | 1 | 1 | 1 | 0110 | 76 |
| SRAI | 1 101 00100 | 1A4 | 1 | 1 | 1 | 0111 | 77 |
| ORI | 0 110 00100 | 0C4 | 1 | 1 | 1 | 0001 | 71 |
| ANDI | 0 111 00100 | 0E4 | 1 | 1 | 1 | 0000 | 70 |
| ADD | 0 000 01100 | 00C | 0 | 1 | 0 | 1111 | 2F |
| SUB | 1 000 01100 | 10C | 0 | 1 | 0 | 1100 | 2C |
| SLL | 1 000 01100 | 02C | 0 | 1 | 0 | 0011 | 23 |
| XOR | 0 100 01100 | 08C | 0 | 1 | 0 | 0100 | 24 |
| SRL | 0 101 01100 | 0AC | 0 | 1 | 0 | 0110 | 26 |
| SRA | 1 101 01100 | 1AC | 0 | 1 | 0 | 0111 | 27 |
| OR | 0 110 01100 | 0CC | 0 | 1 | 0 | 0001 | 21 |
| AND | 0 111 01100 | 0EC | 0 | 1 | 0 | 0000 | 20 |

Almost done